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Version 3, 29 June 2007

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Expect 1.20

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GCC 2.95.3

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libvirt 1.1.2

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Daniel Veillard <veillard@redhat.com>

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```

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libxslt 1.1.22

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linux 3.10.19 wr16

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Version 1.0

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/* nicstar.c v0.22 Jawaid Bazayar (bazayar@hypermall.com)

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* nicstar.c, M. Welsh (matt.welsh@cl.cam.ac.uk)
*
* Hacked October, 1997 by Jawaid Bazyar, Interlink Advertising Services Inc.
* http://www.hypermall.com/
* 10/1/97 - commented out CFG_PHYIE bit - we don't care when the PHY
*interrupts us (except possibly for removal/insertion of the cable?)
* 10/4/97 - began heavy inline documentation of the code. Corrected typos
*and spelling mistakes.
* 10/5/97 - added code to handle PHY interrupts, disable PHY on
*loss of link, and correctly re-enable PHY when link is
*re-established. (put back CFG_PHYIE)
*
* Modified to work with the IDT7721 nicstar -- AAL5 (tested) only.
*
* R. D. Rechenmacher <ron@fnal.gov> , Aug. 6, 1997
*
* Linux driver for the IDT77201 NICStAR PCI ATM controller.
* PHY component is expected to be 155 Mbps S/UNI-Lite or IDT 77155;
* see init_nicstar() for PHY initialization to change this. This driver
* expects the Linux ATM stack to support scatter-gather lists
* (skb-> atm.iovcnt != 0) for Rx skb's passed to vcc-> push.
*
* Implementing minimal-copy of received data:
* IDT always receives data into a small buffer, then large buffers
* as needed. This means that data must always be copied to create
* the linear buffer needed by most non-ATM protocol stacks (e.g. IP)
* Fix is simple: make large buffers large enough to hold entire
* SDU, and leave <small_buffer_data> bytes empty at the start. Then
* copy small buffer contents to head of large buffer.
* Trick is to avoid fragmenting Linux, due to need for a lot of large
* buffers. This is done by 2 things:
* 1) skb-> destructor / skb-> atm.recycle_buffer
* combined, allow nicstar_free_rx_skb to be called to
* recycle large data buffers
* 2) skb_clone of received buffers
* See nicstar_free_rx_skb and linearize_buffer for implementation
* details.
*

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*
*
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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*
* M. Welsh, 6 July 1996
*
*
*/
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NianticPFDriver-3.13.10-k 3.13.10-k

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```
/nobackup/xdxu/tp310bf/thirdparty/opensource/linux/linux-3.10.19/drivers/net/ethernet/intel/ixgbe:85
9 grep LIC *.*
```

```
ixgbe_main.c:MODULE_LICENSE("GPL");
```

```
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```

```
Version 2, June 1991
```

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NianticVFDriver.2.7.12-k 2.7.12-k

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```
/nobackup/xdxu/tp310bf/thirdparty/opensource/linux/linux-3.10.19/drivers/net/ethernet/intel/ixgbev:8
62 grep LIC *.*
```

```
ixgbev_main.c:MODULE_LICENSE("GPL");
```

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this work.

1. [1]Mark Andrews <mark_andrews@isc.org> Leitch atomic clock controller
2. [2]Bernd Altmeier <altmeier@atsoft.de> hopf Elektronik serial line and PCI-bus devices
3. [3]Viraj Bais <vbais@mailman1.intel.com> and [4]Clayton Kirkwood <kirkwood@striderfm.intel.com> port to WindowsNT 3.5
4. [5]Michael Barone <michael,barone@lmco.com> GPSVME fixes
5. [6]Jean-Francois Boudreault <Jean-Francois.Boudreault@viagenie.qc.ca> IPv6 support
6. [7]Karl Berry <karl@owl.HQ.ileaf.com> syslog to file option
7. [8]Greg Brackley <greg.brackley@bigfoot.com> Major rework of WINNT port. Clean up recvbuf and iosignal code into separate modules.
8. [9]Marc Brett <Marc.Brett@westgeo.com> Magnavox GPS clock driver
9. [10]Piete Brooks <Piete.Brooks@cl.cam.ac.uk> MSF clock driver, Trimble PARSE support
10. [11]Reg Clemens <reg@dwf.com> Oncore driver (Current maintainer)
11. [12]Steve Clift <clift@ml.csiro.au> OMEGA clock driver
12. [13]Casey Crellin <casey@csc.co.za> vxWorks (Tornado) port and help with target configuration
13. [14]Sven Dietrich <sven_dietrich@trimble.com> Palisade reference clock driver, NT adj. residuals, integrated Greg's Winnt port.
14. [15]John A. Dundas III <dundas@salt.jpl.nasa.gov> Apple A/UX port
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16. [17]Dennis Ferguson <dennis@mrbill.canet.ca> foundation code for NTP Version 2 as specified in RFC-1119
17. [18]John Hay <jhay@icomtek.csiro.co.za> IPv6 support and testing
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20. [21]Jim Jagielski <jim@jagubox.gsfc.nasa.gov> A/UX port
21. [22]Jeff Johnson <jbj@chatham.usdesign.com> massive prototyping overhaul
22. [23]Hans Lambermont <Hans.Lambermont@nl.origin-it.com> or [24]<H.Lambermont@chello.nl> ntpswEEP
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- PARSE <GENERIC> driver (14 reference clocks), STREAMS modules for PARSE, support scripts, syslog cleanup
25. [28]William L. Jones <jones@hermes.chpc.utexas.edu> RS/6000 AIX modifications, HPUX modifications
 26. [29]Dave Katz <dkatz@cisco.com> RS/6000 AIX port
 27. [30]Craig Leres <leres@ee.lbl.gov> 4.4BSD port, ppsclock, Magnavox GPS clock driver
 28. [31]George Lindholm <lindholm@ucs.ubc.ca> SunOS 5.1 port
 29. [32]Louis A. Mamakos <louie@ni.umd.edu> MD5-based authentication
 30. [33]Lars H. Mathiesen <thorinn@diku.dk> adaptation of foundation code for Version 3 as specified in RFC-1305
 31. [34]Danny Mayer <mayer@ntp.org> Network I/O, Windows Port, Code Maintenance
 32. [35]David L. Mills <mills@udel.edu> Version 4 foundation: clock discipline, authentication, precision kernel; clock drivers: Spectracom, Austron, Arbiter, Heath, ATOM, ACTS, KSI/Odetics; audio clock drivers: CHU, WWV/H, IRIG
 33. [36]Wolfgang Moeller <moeller@gwdgv1.dnet.gwdg.de> VMS port
 34. [37]Jeffrey Mogul <mogul@pa.dec.com> ntptrace utility
 35. [38]Tom Moore <tmoore@fivel.daytonoh.ncr.com> i386 svr4 port
 36. [39]Kamal A Mostafa <kamal@whence.com> SCO OpenServer port
 37. [40]Derek Mulcahy <derek@toybox.demon.co.uk> and [41]Damon Hart-Davis <d@hd.org> ARCRON MSF clock driver
 38. [42]Rainer Pruy <Rainer.Pruy@informatik.uni-erlangen.de> monitoring/trap scripts, statistics file handling
 39. [43]Dirce Richards <dirce@zk3.dec.com> Digital UNIX V4.0 port
 40. [44]Wilfredo S?nchez <wsanchez@apple.com> added support for NetInfo
 41. [45]Nick Sayer <mrapple@quack.kfu.com> SunOS streams modules
 42. [46]Jack Sasportas <jack@innovativeinternet.com> Saved a Lot of space on the stuff in the html/pic/ subdirectory
 43. [47]Ray Schnitzler <schnitz@unipress.com> Unixware1 port
 44. [48]Michael Shields <shields@tembel.org> USNO clock driver
 45. [49]Jeff Steinman <jss@pebbles.jpl.nasa.gov> Datum PTS clock driver
 46. [50]Harlan Stenn <harlan@pfcs.com> GNU automake/autoconfigure makeover, various other bits (see the ChangeLog)
 47. [51]Kenneth Stone <ken@sdd.hp.com> HP-UX port

48. [52]Ajit Thyagarajan <ajit@ee.udel.edu> IP multicast/anycast support
 49. [53]Tomoaki TSURUOKA <tsuruoka@nc.fukuoka-u.ac.jp> TRAK clock driver
 50. [54]Paul A Vixie <vixie@vix.com> TrueTime GPS driver, generic TrueTime clock driver
 51. [55]Ulrich Windl <Ulrich.Windl@rz.uni-regensburg.de> corrected and validated HTML documents according to the HTML DTD
-

openssl 1.0.0.n

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* Major rewrite to support fully async operation

*

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* Added flexible support for /dev/bus/usb /sys/bus/usb/devices in addition

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* Magor rewrite of the UHCI data structures parser and frame processor

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