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1.3 pycparser 2.21

1.3.1 Available under license :

pycparser -- A C parser in Python

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1.4 namespace 1.4.01

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1.5 bouncycastle-fips 1.0.2

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1.6 binutils 2.33.1

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```
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`Gnomovision' (which makes passes at compilers) written by James Hacker.
```

```
<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice
```

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Version 3, 29 June 2007

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4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative

work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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- b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.
- c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.
- d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the

Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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<signature of Ty Coon>, 1 April 1990

Ty Coon, President of Vice

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Version 2.1, February 1999

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

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When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

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+++++

Dean Elsner wrote the original gas for vax. [more details?]

Jay Fenlason maintained gas for a while, adding support for gdb-specific debug information and the 68k series machines, most of the preprocessing pass, and extensive changes in messages.c, input-file.c, write.c.

K. Richard Pixley maintained gas for a while, adding various enhancements and many bug fixes, including merging support for several processors, breaking gas up to handle multiple object file format backends (including heavy rewrite, testing, an integration of the coff and b.out backends), adding configuration including heavy testing and verification of cross assemblers and file splits and renaming, converted gas to strictly ansi C including full prototypes, added support for m680[34]0 & cpu32, considerable work on i960 including a coff port (including considerable amounts of reverse engineering), a sparc opcode file rewrite, decstation, rs6000, and hp300hpux host ports, updated "know" assertions and made them work, much other reorganization, cleanup, and lint.

Ken Raeburn wrote the high-level BFD interface code to replace most of the code in format-specific I/O modules.

The original Vax-VMS support was contributed by David L. Kashtan. Eric Youngdale and Pat Rankin have done much work with it since.

The Intel 80386 machine description was written by Eliot Dresselhaus.

Minh Tran-Le at IntelliCorp contributed some AIX 386 support.

The Motorola 88k machine description was contributed by Devon Bowen of Buffalo University and Torbjorn Granlund of the Swedish Institute of Computer Science.

Keith Knowles at the Open Software Foundation wrote the original MIPS back end (tc-mips.c, tc-mips.h), and contributed Rose format support that hasn't been merged in yet. Ralph Campbell worked with the MIPS code to support a.out format.

Support for the Zilog Z8k and Hitachi H8/300, H8/500 and SH processors (tc-z8k, tc-h8300, tc-h8500, tc-sh), and IEEE 695 object file format (obj-ieee), was written by Steve Chamberlain of Cygnus Solutions. Steve also modified the COFF back end (obj-coffbfd) to use BFD for some low-level operations, for use with the Hitachi, 29k and Zilog targets.

John Gilmore built the AMD 29000 support, added .include support, and simplified the configuration of which versions accept which pseudo-ops. He updated the 68k machine description so that Motorola's opcodes always produced fixed-size instructions (e.g. jsr), while synthetic instructions remained shrinkable (jbsr). John fixed many bugs, including true tested cross-compilation support, and one bug in relaxation that took a week and required the proverbial one-bit fix.

Ian Lance Taylor of Cygnus Solutions merged the Motorola and MIT syntaxes for the 68k, completed support for some COFF targets (68k, i386 SVR3, and SCO Unix), wrote the ECOFF support based on Michael Meissner's mips-tfile program, wrote the PowerPC and RS/6000 support, and made a few other minor patches. He handled the binutils releases for versions 2.7 through 2.9.

David Edelsohn contributed fixes for the PowerPC and AIX support.

Steve Chamberlain made gas able to generate listings.

Support for the HP9000/300 was contributed by Glenn Engel of HP.

Support for ELF format files has been worked on by Mark Eichin of Cygnus Solutions (original, incomplete implementation), Pete Hoogenboom at the University of Utah (HPPA mainly), Michael Meissner of the Open Software Foundation (i386 mainly), and Ken Raeburn of Cygnus Solutions (sparc, initial 64-bit support).

Several engineers at Cygnus Solutions have also provided many small bug fixes and configuration enhancements.

The initial Alpha support was contributed by Carnegie-Mellon University. Additional work was done by Ken Raeburn of Cygnus Solutions. Richard Henderson then rewrote much of the Alpha support.

Ian Dall updated the support code for the National Semiconductor 32000 series, and added support for Mach 3 and NetBSD running on the PC532.

Klaus Kaempf ported the assembler and the binutils to openVMS/Alpha.

Steve Haworth contributed the support for the Texas Instruction c30 (tms320c30).

H.J. Lu has contributed many patches and much testing.

Alan Modra reworked much of the i386 backend, improving the error checking, updating the code, and improving the 16 bit support, using patches from the work of Martynas Kunigelis and H.J. Lu.

Many others have contributed large or small bugfixes and enhancements. If you've contributed significant work and are not mentioned on this list, and want to be, let us know. Some of the history has been lost; we aren't intentionally leaving anyone out.

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1.7 taglibs-standard-spec 1.2.5

1.7.1 Available under license :

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1.8 json-glib 1.2.8

1.9 six 1.15.0-3.ph4

1.9.1 Available under license :

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1.10 tomcat-servlet-api 8.5.32

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- web-app_3_0.xsd
- web-common_3_0.xsd
- web-fragment_3_0.xsd
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- web-common_3_0.xsd
- web-fragment_3_0.xsd
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1.12 jersey-media-json-jackson 2.28

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1.17 slf4j 1.7.2

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- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/helpers/NOPMDCAdapter.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/impl/StaticMDCBinder.java
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- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/impl/StaticLoggerBinder.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/helpers/FormattingTuple.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/helpers/BasicMarkerFactory.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/helpers/SubstituteLoggerFactory.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/helpers/NamedLoggerBase.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/MarkerFactory.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/spi/LoggerFactoryBinder.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/ILoggerFactory.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/helpers/BasicMDCAdapter.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/helpers/NOPLoggerFactory.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/MDC.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/helpers/BasicMarker.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/impl/StaticMarkerBinder.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/LoggerFactory.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/Marker.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/IMarkerFactory.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/helpers/NOPLogger.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/helpers/MessageFormatter.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/spi/MarkerFactoryBinder.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/Logger.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/spi/LocationAwareLogger.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/helpers/MarkerIgnoringBase.java
- * /opt/cola/permits/1519500121_1671744390.4177406/0/slf4j-api-1-7-2-sources-3-jar/org/slf4j/spi/MDCAdapter.java

1.18 openssl 1.1.1k

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1.19 jetty-servlets 9.4.16.v20190411

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- * org.eclipse.jetty.orbit:javax.security.auth.message

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- * org.eclipse.jetty.orbit:javax.mail.glassfish

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<https://glassfish.dev.java.net/nonav/public/CDDL+GPL.html>

- * javax.servlet:javax.servlet-api
- * javax.annotation:javax.annotation-api
- * javax.transaction:javax.transaction-api
- * javax.websocket:javax.websocket-api

Oracle OpenJDK

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- * java.sun.security.ssl

These artifacts replace/modify OpenJDK classes. The modifications are hosted at github and both modified and original are under GPL v2 with classpath exceptions.

<http://openjdk.java.net/legal/gplv2+ce.html>

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- org.ow2.asm:asm-commons
- org.ow2.asm:asm

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org.apache.taglibs:taglibs-standard-spec
org.apache.taglibs:taglibs-standard-impl

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org.mortbay.jasper:apache-jsp
org.apache.tomcat:tomcat-jasper
org.apache.tomcat:tomcat-juli
org.apache.tomcat:tomcat-jsp-api
org.apache.tomcat:tomcat-el-api
org.apache.tomcat:tomcat-jasper-el
org.apache.tomcat:tomcat-api
org.apache.tomcat:tomcat-util-scan
org.apache.tomcat:tomcat-util

org.mortbay.jasper:apache-el
org.apache.tomcat:tomcat-jasper-el
org.apache.tomcat:tomcat-el-api

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<https://glassfish.dev.java.net/nonav/public/CDDL+GPL.html>

org.eclipse.jetty.toolchain:jetty-schemas

Assorted

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1.20 nss 3.61

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Jean-loup Gailly Mark Adler
jloup@gzip.org madler@alumni.caltech.edu

The following list of people, sorted by last name, have contributed code or patches to this implementation of sudo since I began maintaining it in 1993. This list is known to be incomplete--if you believe you should be listed, please send a note to sudo@sudo.ws.

Ackeret, Matt
Adler, Mark
Allbery, Russ
Anderson, Jamie
Andrew, Nick
Andric, Dimitry
Barron, Danny
Bates, Tom
Behan, Zdenk
Bellis, Ray
Benali, Elias
Beverly, Jamie
Boardman, Spider
Bos, Sander
Bostley, P.J.
Bowes, Keith
Boyce, Keith Garry
Brantley, Michael
Braun, Rob
Bezina, Pavel
Brooks, Piete

Brown, Jerry
Burr, Michael E
Burton, Ross
Bussjaeger, Andreas
Calvin, Gary
Campbell, Aaron
Chazelas, Stephane
Cheloha, Scott
ek, Vtzslav
Coleman, Chris
Corzine, Deven T.
Cusack, Frank
Dai, Wei
Dill, David
Earickson, Jeff
Eckhardt, Drew
Edgington, Ben
Esipovich, Marc
Espie, Marc
Faigon, Ariel
Farrell, Brian
Fobes, Steve
Frysinger, Mike
G., Daniel Richard
Gailly, Jean-loup
Gelman, Stephen
Gerraty, Simon J.
Graber, Stephane
Guillory, B.
Hayman, Randy M.
Henke, Joachim
Hideaki, Yoshifuji
Hieb, Dave
Holloway, Nick
Hoover, Adam
Hunter, Michael T.
Hutchings, Ben
Irrgang, Eric
Jackson, Brian
Jackson, John R.
Jackson, Richard L., Jr.
Janssen, Mark
Jepeway, Chris
Jorge, Joel Pele
Jover, Guillem
Juhani, Timo
Kikuchi, Ayamura
Kadow, Kevin

Kasal, Stepan
Kienenberger, Mike
King, Dale
King, Michael
Klyachkin, Andrey
Knoble, Jim
Knox, Tim
Komarnitsky, Alek O.
Kondrashov, Nikolai
Kopeek, Daniel
Kranenburg, Paul
Krause, David
Lakin, Eric
Larsen, Case
Levin, Dmitry V.
Libby, Kendall
Lobbes, Phillip E.
McIntyre, Jason
MacKenzie, David J.
McLaughlin, Tom
Makey, Jeff
Mallayya, Sangamesh
Marchionna, Michael D.
Markham, Paul
Martinian, Emin
Meskes, Michael
Michael, David
Miller, Todd C.
Minier, Loc
Moffat, Darren
Moldung, Jan Thomas
Morris, Charles
Mueller, Andreas
Mller, Dworkin
Nieuwma, Jeff
Nikitser, Peter A.
Nussel, Ludwig
Ouellet, Jean-Philippe
Paquet, Eric
Paradis, Chantal
Pasteleurs, Frederic
Percival, Ted
Perera, Andres
Peron, Christian S.J.
Peschel, Aaron
Peslyak, Alexander
Peterson, Toby
Petten, Diego Elio

Pickett, Joel
Plotnick, Alex
de Raadt, Theo
Rasch, Gudleik
Reid, Steve
Richards, Matt
Rossum, Guido van
Rouillard, John P.
Rowe, William A., Jr.
Roy, Alain
Ruusame, Elan
Ryabinkin, Eygene
Sato, Yuichi
Snchez, Wilfredo
Sanders, Miguel
Sasaki, Kan
Saucier, Jean-Francois
Schoenfeld, Patrick
Schuring, Arno
Schwarze, Ingo
Scott, Dougal
Sieger, Nick
Simon, Thor Lancelot
Slemko, Marc
Smith, Andy
Sobrado, Igor
Soulen, Steven
Spangler, Aaron
Spradling, Cloyce D.
Spradling, Michael
Stier, Matthew
Stoeckmann, Tobias
Street, Russell
Stritzky, Tilo
Stroucken, Michael
Tarrall, Robert
Thomas, Matthew
Todd, Giles
Toft, Martin
Torek, Chris
Tucker, Darren
Uhl, Robert
Uzel, Petr
Valery, Reznic
Van Dinter, Theo
Venckus, Martynas
de Vries, Maarten
Wagner, Klaus

Walsh, Dan
Warburton, John
Webb, Kirk
Wetzel, Timm
Wieringen, Marco van
Wilk, Jakub
Winiger, Gary
Wood, David
Zacarias, Gustavo
Zolnowsky, John

The following people have worked to translate sudo into other languages as part of the Translation Project, see <https://translationproject.org> for more details.

Albuquerque, Pedro
Blttermann, Mario
Bogusz, Jakub
Buo-ren, Lin
Casagrande, Milo
Castro, Felipe
Cho, Seong-ho
Chornoivan, Yuri
Diguez, Francisco
Fontenelle, Rafael
Garca-Fontes, Walter
Gezer, Volkan
Hamasaki, Takeshi
Hamming, Peter
Hansen, Joe
Hantrais, Frdric
Hein, Jochen
Hufthammer, Karl Ove
Jerovek, Damir
Karvonen, Jorma
Kazik, Duan
Kelemen, Gbor
Keeci, Mehmet
Koir, Klemen
Kozlov, Yuri
Kramer, Jakob
Krznar, Tomislav
Marchal, Frdric
Margeviius, Algimantas
Maryanov, Pavel
Nikoli, Miroslav
Nylander, Daniel
Psa, Petr

Puente, Enol
Putanec, Boidar
Qun, Trn Ngc
Rasmussen, Sebastian
Regueiro, Leandro
Sarer, zgr
Sendn, Abel
Sikrom, ka
Spingos, Dimitris
Taniguchi, Yasuaki
Tomat, Fbio
r, Balzs
Uranga, Mikel Olasagasti
Vorotnikov, Artem
Wang, Wylmer

1.23 glew 3.20.18

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Mesa 3-D graphics library

Version: 7.0

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1.26 nettle 3.4

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Version 3, 29 June 2007

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1.28 protobuf-java 2.2.0

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```
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/RpcUtil.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/RpcCallback.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/AbstractMessage.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/ProtocolMessageEnum.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/BlockingRpcChannel.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/CodedOutputStream.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/DynamicMessage.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/Descriptors.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/AbstractMessageLite.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/GeneratedMessageLite.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/RpcChannel.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/CodedInputStream.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/TextFormat.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/ByteString.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/ExtensionRegistry.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/ExtensionRegistryLite.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/GeneratedMessage.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/UninitializedMessageException.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/RpcController.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/FieldSet.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/BlockingService.java
```

```
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-  
jar/com/google/protobuf/Internal.java  
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-  
jar/com/google/protobuf/ServiceException.java  
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-  
jar/com/google/protobuf/InvalidProtocolBufferException.java  
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-  
jar/com/google/protobuf/MessageLite.java  
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-  
jar/com/google/protobuf/Message.java  
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-  
jar/com/google/protobuf/WireFormat.java  
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-  
jar/com/google/protobuf/Service.java  
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-  
jar/com/google/protobuf/UnknownFieldSet.java
```

1.29 urllib3 1.25.3

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Contributions to the urllib3 project

Creator & Maintainer

* Andrey Petrov <andrey.petrov@shazow.net>

Contributors

In chronological order:

- * victor.vde <<http://code.google.com/u/victor.vde/>>
 - * HTTPS patch (which inspired HTTPSConnectionPool)
- * erikcederstrand <<http://code.google.com/u/erikcederstrand/>>
 - * NTLM-authenticated HTTPSConnectionPool
 - * Basic-authenticated HTTPSConnectionPool (merged into make_headers)
- * niphlod <niphlod@gmail.com>
 - * Client-verified SSL certificates for HTTPSConnectionPool
 - * Response gzip and deflate encoding support
 - * Better unicode support for filepost using StringIO buffers
- * btoconnor <brian@btoconnor.net>
 - * Non-multipart encoding for POST requests
- * p.dobrogost <<http://code.google.com/u/@WBRSRIBZDhBFXQB6/>>
 - * Code review, PEP8 compliance, benchmark fix
- * kennethreitz <me@kennethreitz.com>
 - * Bugfixes, suggestions, Requests integration
- * georgemarshall <<https://github.com/georgemarshall>>
 - * Bugfixes, Improvements and Test coverage
- * Thomas Kluyver <thomas@kluyver.me.uk>
 - * Python 3 support
- * brandon-rhodes <<http://rhodesmill.org/brandon>>
 - * Design review, bugfixes, test coverage.
- * studer <theo.studer@gmail.com>
 - * IPv6 url support and test coverage
- * Shivaram Lingamneni <slingamn@cs.stanford.edu>
 - * Support for explicitly closing pooled connections
- * hartator <hartator@gmail.com>
 - * Corrected multipart behavior for params
- * Thomas Weischuh <thomas@t-8ch.de>
 - * Support for TLS SNI
 - * API unification of ssl_version/cert_reqs
 - * SSL fingerprint and alternative hostname verification

- * Bugfixes in testsuite

- * Sune Kirkeby <mig@ibofobi.dk>
- * Optional SNI-support for Python 2 via PyOpenSSL.

- * Marc Schlaich <marc.schlaich@gmail.com>
- * Various bugfixes and test improvements.

- * Bryce Boe <bbzbryce@gmail.com>
- * Correct six.moves conflict
- * Fixed pickle support of some exceptions

- * Boris Figovsky <boris.figovsky@ravellosystems.com>
- * Allowed to skip SSL hostname verification

- * Cory Benfield <<https://lukasa.co.uk/about/>>
- * Stream method for Response objects.
- * Return native strings in header values.
- * Generate 'Host' header when using proxies.

- * Jason Robinson <jaywink@basshero.org>
- * Add missing WrappedSocket.fileno method in PyOpenSSL

- * Audrius Butkevicius <audrius.butkevicius@elastichosts.com>
- * Fixed a race condition

- * Stanislav Vitkovskiy <stas.vitkovsky@gmail.com>
- * Added HTTPS (CONNECT) proxy support

- * Stephen Holsapple <sholsapp@gmail.com>
- * Added abstraction for granular control of request fields

- * Martin von Gagern <Martin.vGagern@gmx.net>
- * Support for non-ASCII header parameters

- * Kevin Burke <kev@inburke.com> and Pavel Kirichenko <juanych@yandex-team.ru>
- * Support for separate connect and request timeouts

- * Peter Waller <p@pwaller.net>
- * HTTPResponse.tell() for determining amount received over the wire

- * Nipunn Koorapati <nipunn1313@gmail.com>
- * Ignore default ports when comparing hosts for equality

- * Danilo @dbrgn <<https://dbrgn.ch/>>
- * Disabled TLS compression by default on Python 3.2+
- * Disabled TLS compression in pyopenssl contrib module
- * Configurable cipher suites in pyopenssl contrib module

- * Roman Bogorodskiy <roman.bogorodskiy@ericsson.com>
- * Account retries on proxy errors

- * Nicolas Delaby <nicolas.delaby@ezeep.com>
- * Use the platform-specific CA certificate locations

- * Josh Schneier <<https://github.com/jschneier>>
- * HTTPHeaderDict and associated tests and docs
- * Bugfixes, docs, test coverage

- * Tahia Khan <<http://tahia.tk/>>
- * Added Timeout examples in docs

- * Arthur Grunseid <<https://grunseid.com>>
- * source_address support and tests (with <https://github.com/bui>)

- * Ian Cordasco <graffatcolmingov@gmail.com>
- * PEP8 Compliance and Linting
- * Add ability to pass socket options to an HTTP Connection

- * Erik Tollerud <erik.tollerud@gmail.com>
- * Support for standard library io module.

- * Krishna Prasad <kprasad.iitd@gmail.com>
- * Google App Engine documentation

- * Aaron Meurer <asmeurer@gmail.com>
- * Added Url.url, which unparses a Url

- * Evgeny Kapun <abacabadabacaba@gmail.com>
- * Bugfixes

- * Benjamin Meyer <bm_witness@yahoo.com>
- * Security Warning Documentation update for proper capture

- * Shivan Sornarajah <github@sornars.com>
- * Support for using ConnectionPool and PoolManager as context managers.

- * Alex Gaynor <alex.gaynor@gmail.com>
- * Updates to the default SSL configuration

- * Tomas Tomecek <ttomecek@redhat.com>
- * Implemented generator for getting chunks from chunked responses.

- * tlynn <<https://github.com/tlynn>>
- * Respect the warning preferences at import.

- * David D. Riddle <ddriddle@illinois.edu>
- * IPv6 bugfixes in testsuite

- * Thea Flowers <magicalgirl@google.com>
- * App Engine environment tests.
- * Documentation re-write.

- * John Krauss <https://github.com/talos>
- * Clues to debugging problems with `cryptography` dependency in docs

- * Disassem <https://github.com/Disassem>
- * Fix pool-default headers not applying for url-encoded requests like GET.

- * James Atherfold <jlatherfold@hotmail.com>
- * Bugfixes relating to cleanup of connections during errors.

- * Christian Pedersen <https://github.com/chripede>
- * IPv6 HTTPS proxy bugfix

- * Jordan Moldow <https://github.com/jmoldow>
- * Fix low-level exceptions leaking from ``HTTPResponse.stream()``.
- * Bugfix for ``ConnectionPool.urlopen(release_conn=False)``.
- * Creation of ``HTTPConnectionPool.ResponseCls``.

- * Predrag Gruevski <https://github.com/obi1kenobi>
- * Made cert digest comparison use a constant-time algorithm.

- * Adam Talsma <https://github.com/a-tal>
- * Bugfix to ca_cert file paths.

- * Evan Meagher <https://evanmeagher.net>
- * Bugfix related to `memoryview` usage in PyOpenSSL adapter

- * John Vandenberg <jayvdb@gmail.com>
- * Python 2.6 fixes; pyflakes and pep8 compliance

- * Andy Caldwell <andy.m.caldwell@googlemail.com>
- * Bugfix related to reusing connections in indeterminate states.

- * Ville Skytt <ville.skytta@iki.fi>
- * Logging efficiency improvements, spelling fixes, Travis config.

- * Shige Takeda <smtakeda@gmail.com>
- * Started Recipes documentation and added a recipe about handling concatenated gzip data in HTTP response

- * Jess Shapiro <jesse@jesseshapiro.net>
- * Various character-encoding fixes/tweaks
- * Disabling IPv6 DNS when IPv6 connections not supported

- * David Foster <http://dafoster.net/>
- * Ensure order of request and response headers are preserved.

- * Jeremy Cline <jeremy@jcline.org>
- * Added connection pool keys by scheme

- * Aviv Palivoda <palaviv@gmail.com>
- * History list to Retry object.
- * HTTPResponse contains the last Retry object.

- * Nate Prewitt <nate.prewitt@gmail.com>
- * Ensure timeouts are not booleans and greater than zero.
- * Fixed infinite loop in ``stream`` when amt=None.
- * Added length_remaining to determine remaining data to be read.
- * Added enforce_content_length to raise exception when incorrect content-length received.

- * Seth Michael Larson <sethmichaellarson@protonmail.com>
- * Created selectors backport that supports PEP 475.

- * Alexandre Dias <alex.dias@smarkets.com>
- * Don't retry on timeout if method not in whitelist

- * Moinuddin Quadri <moin18@gmail.com>
- * Lazily load idna package

- * Tom White <s6yg1ez3@mail2tor.com>
- * Made SOCKS handler differentiate socks5h from socks5 and socks4a from socks4.

- * Tim Burke <tim.burke@gmail.com>
- * Stop buffering entire deflate-encoded responses.

- * Tuukka Mustonen <tuukka.mustonen@gmail.com>
- * Add counter for status_forcelist retries.

- * Erik Rose <erik@mozilla.com>
- * Bugfix to pyopenssl vendoring

- * Wolfgang Richter <wolfgang.richter@gmail.com>
- * Bugfix related to loading full certificate chains with PyOpenSSL backend.

- * Mike Miller <github@mikeage.net>
- * Logging improvements to include the HTTP(S) port when opening a new connection

- * Ioannis Tziakos <mail@itziakos.gr>
- * Fix ``util.selectors._fileobj_to_fd`` to accept ``long``.
- * Update appveyor tox setup to use the 64bit python.

- * Akamai (through Jess Shapiro) <jshapiro@akamai.com>
- * Ongoing maintenance; 2017-2018

- * Dominique Leuenberger <dimstar@opensuse.org>
- * Minor fixes in the test suite

- * Will Bond <will@wbond.net>
- * Add Python 2.6 support to ``contrib.securetransport``

- * Aleksei Alekseev <alekseev.yeskela@gmail.com>
- * using auth info for socks proxy

- * Chris Wilcox <git@crwilcox.com>
- * Improve contribution guide
- * Add ``HTTPResponse.geturl`` method to provide ``urllib2.urlopen().geturl()`` behavior

- * Bruce Merry <https://www.brucemerry.org.za>
- * Fix leaking exceptions when system calls are interrupted with zero timeout

- * Hugo van Kemenade <https://github.com/hugovk>
- * Drop support for EOL Python 2.6

- * Tim Bell <https://github.com/timb07>
- * Bugfix for responses with Content-Type: message/* logging warnings

- * Justin Bramley <https://github.com/jbramleycl>
- * Add ability to handle multiple Content-Encodings

- * Katsuhiko YOSHIDA <https://github.com/kyoshidajp>
- * Remove Authorization header regardless of case when redirecting to cross-site

- * [Your name or handle] <[email or website]>
- * [Brief summary of your changes]

1.30 tomcat-jni 8.5.32

1.30.1 Available under license :

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1.31 python-requests 2.5.3

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Requests includes some vendorized python libraries to ease installation.

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=====

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```

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1.36 constantine 0.7

1.36.1 Available under license :

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```
<project xmlns="http://maven.apache.org/POM/4.0.0" xmlns:xsi="http://www.w3.org/2001/XMLSchema-instance"
xsi:schemaLocation="http://maven.apache.org/POM/4.0.0 http://maven.apache.org/maven-v4_0_0.xsd">
  <modelVersion>4.0.0</modelVersion>
  <groupId>org.jruby.extras</groupId>
  <artifactId>constantine</artifactId>
  <packaging>jar</packaging>
  <version>0.7</version>
  <name>constantine</name>
  <description>A set of platform constants (e.g. erno values)</description>
  <url>http://constantine.kenai.com</url>
  <licenses>
    <license>
      <name>The MIT License</name>
      <url>http://www.opensource.org/licenses/mit-license.php</url>
      <distribution>repo</distribution>
    </license>
  </licenses>
  <scm>
    <connection>scm:hg:https://kenai.com/hg/constantine~mercurial</connection>
    <url>https://kenai.com/hg/constantine~mercurial</url>
  </scm>
  <distributionManagement>
    <repository>
      <id>codehaus-jruby-repository</id>
      <name>JRuby Central Repository</name>
      <url>dav:https://dav.codehaus.org/repository/jruby</url>
    </repository>
    <snapshotRepository>
      <id>codehaus-jruby-snapshot-repository</id>
```

```

    <name>JRuby Central Development Repository</name>
    <url>dav:https://dav.codehaus.org/snapshots.repository/jruby</url>
  </snapshotRepository>
</distributionManagement>
<repositories>
  <repository>
    <id>maven2-repository.dev.java.net</id>
    <name>Java.net Repository for Maven</name>
    <url>http://download.java.net/maven/2/</url>
  </repository>
  <repository>
    <id>codehaus</id>
    <name>Codehaus Repository</name>
    <releases>
      <enabled>true</enabled>
    </releases>
    <snapshots>
      <enabled>>false</enabled>
    </snapshots>
    <url>http://repository.codehaus.org</url>
  </repository>
</repositories>

<developers>
  <developer>
    <id>wmeissner</id>
    <name>Wayne Meissner</name>
    <email>wmeissner@gmail.com</email>
  </developer>
</developers>

<dependencies>
  <dependency>
    <groupId>junit</groupId>
    <artifactId>junit</artifactId>
    <version>4.5</version>
    <scope>test</scope>
  </dependency>
</dependencies>
<build>
  <directory>build</directory>
  <outputDirectory>build/classes</outputDirectory>
  <testOutputDirectory>build/test/classes</testOutputDirectory>
  <sourceDirectory>src</sourceDirectory>
  <testSourceDirectory>test</testSourceDirectory>
  <resources>
  </resources>
  <plugins>

```

```
<plugin>
  <groupId>org.apache.maven.plugins</groupId>
  <artifactId>maven-compiler-plugin</artifactId>
  <version>2.0.2</version>
  <configuration>
    <source>1.5</source>
    <target>1.5</target>
  </configuration>
</plugin>
</plugins>
<extensions>
  <extension>
    <groupId>org.jvnet.wagon-svn</groupId>
    <artifactId>wagon-svn</artifactId>
    <version>1.8</version>
  </extension>
  <extension>
    <groupId>org.apache.maven.wagon</groupId>
    <artifactId>wagon-webdav</artifactId>
  </extension>
</extensions>
</build>
<reporting>
  <outputDirectory>build/report</outputDirectory>
</reporting>
</project>
```

Found in path(s):

* /opt/cola/permits/1257263874_1672980538.3560572/0/constantine-0-7-jar-zip/constantine-0-7-jar/META-INF/maven/org.jruby.extras/constantine/pom.xml

1.37 tomcat-util-scan 8.5.32

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1.38 dom 1.0

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```

Found in path(s):

```
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/dom3/as/ElementEditAS.java  
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/dom3/as/ASNotationDeclaration.java  
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/dom3/as/ASObject.java  
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/dom3/as/CharacterDataEditAS.java  
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/dom3/as/DOMImplementationAS.java  
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/dom3/as/ASModel.java  
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/dom3/as/DocumentAS.java  
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/dom3/as/ASAttributeDeclaration.java  
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/dom3/as/ASElementDeclaration.java  
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/dom3/as/ASEntityDeclaration.java  
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/dom3/as/ASNamedObjectMap.java  
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/dom3/as/ASDataType.java  
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/dom3/as/ASContentModel.java
```

* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/dom3/as/DOMASWriter.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/dom3/as/DocumentEditAS.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/dom3/as/ASObjectList.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/dom3/as/DOMASBuilder.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/dom3/as/DOMASException.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/dom3/as/NodeEditAS.java

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Found in path(s):

* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/xs/datatypes/package.html

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* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/w3c/dom/html/HTMLDOMImplementation.java

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*/

/**

* Checks if this content model has had its min/maxOccurs values reduced for
* purposes of speeding up UPA. If so, this content model should not be used
* for any purpose other than checking unique particle attribution
*
* @return a boolean that says whether this content has been compacted for UPA
*/

Found in path(s):

* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/impl/xs/models/XSCMValidator.java

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```

*/
/**
 * DOM Level 3 LS CR - Experimental.
 * Create a new LSParser. The newly constructed parser may
 * then be configured by means of its DOMConfiguration
 * object, and used to parse documents by means of its parse
 * method.
 * @param mode The mode argument is either
 * MODE_SYNCHRONOUS or MODE_ASYNCHRONOUS, if
 * mode is MODE_SYNCHRONOUS then the
 * LSParser that is created will operate in synchronous
 * mode, if it's MODE_ASYNCHRONOUS then the
 * LSParser that is created will operate in asynchronous
 * mode.
 * @param schemaType An absolute URI representing the type of the schema
 * language used during the load of a Document using the
 * newly created LSParser. Note that no lexical checking
 * is done on the absolute URI. In order to create a
 * LSParser for any kind of schema types (i.e. the
 * LSParser will be free to use any schema found), use the value
 * null.
 * <p><b>Note:</b> For W3C XML Schema ["http://www.w3.org/2001/XMLSchema". For XML DTD \["http://www.w3.org/TR/REC-xml". Other Schema languages
 \\* are outside the scope of the W3C and therefore should recommend an
 \\* absolute URI in order to use this method.
 \\* @return The newly created LSParser object. This
 \\* LSParser is either synchronous or asynchronous
 \\* depending on the value of the mode argument.
 \\* <p><b>Note:</b> By default, the newly created LSParser
 \\* does not contain a DOMErrorHandler, i.e. the value of
 \\* the "null. However, implementations
 \\\* may provide a default error handler at creation time. In that case,
 \\\* the initial value of the "error-handler" configuration
 \\\* parameter on the new created LSParser contains a
 \\\* reference to the default error handler.
 \\\* @exception DOMException
 \\\* NOT\\\_SUPPORTED\\\_ERR: Raised if the requested mode or schema type is
 \\\* not supported.
\\\*/

```

Found in path(s):

* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/dom/CoreDOMImplementationImpl.java

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*/

/**

* used to check the 3 constraints against each complex type
* (should be each model group):
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* Element Declarations Consistent.

*/

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Found in path(s):

* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/impl/xs/traversers/XSDDocumentInfo.java

* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/impl/xs/traversers/XSDElementTraverser.java

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* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/html/dom/HTMLFrameElementImpl.java

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* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/dv/xs/DayDV.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/xs/XMLSchemaValidator.java
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Found in path(s):

* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/msg/XMLSchemaMessages.properties
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/xpath/regex/message.properties
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/msg/DOMMessages.properties
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/msg/XPointerMessages.properties
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/msg/DatatypeMessages.properties
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/xpath/regex/message_fr.properties
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/msg/SAXMessages.properties
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/msg/XIncludeMessages.properties

```
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/msg/XMLMessages.properties
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/msg/XMLSerializerMessages.properties
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/xpath/regex/message_ja.properties
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/msg/JAXPValidationMessages.properties
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```
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/xs/models/XSDFACM.java
```

1.39 zstd 1.4.5

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1.40 python-pip 20.2.3

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Release	Derived from	Year	Owner	GPL-compatible? (1)
2.5	2.4	2006	PSF	yes
2.5.1	2.5	2007	PSF	yes
2.5.2	2.5.1	2008	PSF	yes
2.5.3	2.5.2	2008	PSF	yes
2.6	2.5	2008	PSF	yes
2.6.1	2.6	2008	PSF	yes
2.6.2	2.6.1	2009	PSF	yes
2.6.3	2.6.2	2009	PSF	yes
2.6.4	2.6.3	2009	PSF	yes
2.6.5	2.6.4	2010	PSF	yes
3.0	2.6	2008	PSF	yes
3.0.1	3.0	2009	PSF	yes
3.1	3.0.1	2009	PSF	yes
3.1.1	3.1	2009	PSF	yes
3.1.2	3.1.1	2010	PSF	yes
3.1.3	3.1.2	2010	PSF	yes
3.1.4	3.1.3	2011	PSF	yes
3.2	3.1	2011	PSF	yes
3.2.1	3.2	2011	PSF	yes
3.2.2	3.2.1	2011	PSF	yes
3.3	3.2	2012	PSF	yes

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1.6.1	1.6	2001	CNRI	yes (2)
2.1	2.0+1.6.1	2001	PSF	no
2.0.1	2.0+1.6.1	2001	PSF	yes
2.1.1	2.1+2.0.1	2001	PSF	yes

2.2	2.1.1	2001	PSF	yes
2.1.2	2.1.1	2002	PSF	yes
2.1.3	2.1.2	2002	PSF	yes
2.2.1	2.2	2002	PSF	yes
2.2.2	2.2.1	2002	PSF	yes
2.2.3	2.2.2	2003	PSF	yes
2.3	2.2.2	2002-2003	PSF	yes
2.3.1	2.3	2002-2003	PSF	yes
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2.6.4	2.6.3	2009	PSF	yes
2.6.5	2.6.4	2010	PSF	yes
3.0	2.6	2008	PSF	yes
3.0.1	3.0	2009	PSF	yes
3.1	3.0.1	2009	PSF	yes
3.1.1	3.1	2009	PSF	yes
3.1.2	3.1	2010	PSF	yes
3.2	3.1	2010	PSF	yes

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ca-bundle.crt -- Bundle of CA Root Certificates

Certificate data from Mozilla as of: Thu Nov 3 19:04:19 2011#

This is a bundle of X.509 certificates of public Certificate Authorities (CA). These were automatically extracted from Mozilla's root certificates file (certdata.txt). This file can be found in the mozilla source tree:

<http://mxr.mozilla.org/mozilla/source/security/nss/lib/ckfw/builtins/certdata.txt?raw=1#>

It contains the certificates in PEM format and therefore can be directly used with curl / libcurl / php_curl, or with an Apache+mod_ssl webserver for SSL client authentication.

Just configure this file as the SSLCACertificateFile.#

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1.41 pcre 8.35

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1.42 scon 3.1.2

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1.44 bzip2 1.0.2

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1.45 x-crypto 1.17.9

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- javaee_web_services_1_2.xsd
- javaee_web_services_client_1_2.xsd
- javaee_6.xsd
- javaee_web_services_1_3.xsd
- javaee_web_services_client_1_3.xsd
- jsp_2_2.xsd
- web-app_3_0.xsd
- web-common_3_0.xsd
- web-fragment_3_0.xsd
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- javaee_6.xsd
- javaee_web_services_1_3.xsd
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- javaee_web_services_1_2.xsd

- javaee_web_services_client_1_2.xsd
- javaee_6.xsd
- javaee_web_services_1_3.xsd
- javaee_web_services_client_1_3.xsd
- jsp_2_2.xsd
- web-app_3_0.xsd
- web-common_3_0.xsd
- web-fragment_3_0.xsd
- javaee_7.xsd
- javaee_web_services_1_4.xsd
- javaee_web_services_client_1_4.xsd
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- javaee_web_services_1_3.xsd
- javaee_web_services_client_1_3.xsd
- jsp_2_2.xsd
- web-app_3_0.xsd
- web-common_3_0.xsd
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- javaee_web_services_1_4.xsd
- javaee_web_services_client_1_4.xsd
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- javaee_web_services_1_2.xsd
- javaee_web_services_client_1_2.xsd
- javaee_6.xsd
- javaee_web_services_1_3.xsd
- javaee_web_services_client_1_3.xsd
- jsp_2_2.xsd
- web-app_3_0.xsd
- web-common_3_0.xsd
- web-fragment_3_0.xsd
- javaee_7.xsd
- javaee_web_services_1_4.xsd
- javaee_web_services_client_1_4.xsd
- jsp_2_3.xsd
- web-app_3_1.xsd
- web-common_3_1.xsd
- web-fragment_3_1.xsd

may be obtained from:

<http://www.oracle.com/webfolder/technetwork/jsc/xml/ns/javaee/index.html>

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Version 2.0, January 2004

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1.49 image-size 1.1.0-2.ph3

1.49.1 Available under license :

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imagesize

=====

```
.. image:: https://travis-ci.org/shibukawa/imagesize_py.svg?branch=master
   :target: https://travis-ci.org/shibukawa/imagesize_py
```

This module analyzes JPEG/JPEG 2000/PNG/GIF/TIFF/SVG/Netpbm/WebP image headers and returns image size or DIP.

```
.. code:: python
```

```
import imagesize

width, height = imagesize.get("test.png")
print(width, height)

width, height = imagesize.getDPI("test.png")
print(width, height)
```

This module is a pure Python module. You can use file like object like file or something like ``io.BytesIO``.

API

* ``imagesize.get(filepath)``

Returns image size (width, height).

* ``imagesize.getDPI(filepath)``

Returns image DPI (width, height).

Benchmark

It only parses headers, and ignores pixel data. So it is much faster than Pillow.

.. list-table::

:header-rows: 1

- * module
 - * result
- * imagesize (pure Python)
 - * 1.077 seconds per 100 000 times
- * Pillow
 - * 10.569 seconds per 100 000 times

I tested on MacBookPro (2014/Core i7) with 125kB PNG files.

Development

Run test with the following command:

.. code:: bash

```
python -m unittest
```

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Thanks

I referred to the following code:

- * <http://markasread.net/post/17551554979/get-image-size-info-using-pure-python-code>
- * <https://stackoverflow.com/questions/8032642/how-to-obtain-image-size-using-standard-python-class-without-using-external-lib>

I use sample image from here:

- * <https://www.nightprogrammer.org/development/multipage-tiff-example-download-test-image-file/>

Thank you for feedback:

- * tk0miya (<https://github.com/tk0miya>)
- * shimizukawa (<https://github.com/shimizukawa>)
- * xantares (<https://github.com/xantares>)
- * Ivan Zakharyashev (<https://github.com/imz>)
- * Jon Dufresne (<https://github.com/jdufresne>)
- * Geoff Lankow (<https://github.com/darktrojan>)
- * Hugo (<https://github.com/hugovk>)
- * Jack Cherng (<https://github.com/jfcherng>)
- * Tyler A. Young (<https://github.com/s3cur3>)
- * Mark Browning (<https://github.com/mabrowning>)
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- * Nicholas-Schaub (<https://github.com/Nicholas-Schaub>)
- * Nuffknacker (<https://github.com/Nuffknacker>)
- * Hannes Römer (<https://github.com/hroemer>)
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- * Marco (<https://github.com/marcoffee>)
- * ExtReMLapin (<https://github.com/ExtReMLapin>)

Found in path(s):

- * /opt/cola/permits/1422903644_1690268072.167895/0/imagesize-py-master-zip/imagesize_py-master/README.rst

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- * /opt/cola/permits/1422903644_1690268072.167895/0/imagesize-py-master-zip/imagesize_py-master/setup.py

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1.50 pycparser 2.20-1.ph4

1.50.1 Available under license :

pycparser -- A C parser in Python

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The list is sorted in increasing alphabetic order by first name.

- * Andreas Klckner
- * Andrew de los Reyes
- * Benoit Pradelle
- * Dov Feldstern
- * Even Wiik Thomassen
- * Greg Smith
- * Jaroslav Franek
- * Li Xuan Ji
- * Mateusz Czaplinski
- * Paolo Di Maio
- * Rory Yorke
- * Rubin
- * Scott Tsai
- * Sye van der Veen
- * Thomas W. Barr
- * Tomer Segal
- * Weyllor

PLY (Python Lex-Yacc) Version 3.10

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1.51 pillow 1.1.6

1.51.1 Available under license :

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GIMP Palette

Name: custompalette

Columns: 4

#

0 0 0 Index 3

65 38 30 Index 4

103 62 49 Index 6

79 73 72 Index 7

114 101 97 Index 8

208 127 100 Index 9

151 144 142 Index 10

221 207 199 Index 11

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221 207 199 Index 11
GIMP Palette
Name: Test
Columns: 0

GIMP Palette
Name: badpaletteentry
Columns: 4

0 0 0 Index 3
65 38
103 62 49 Index 6
79 73 72 Index 7
114 101 97 Index 8
208 127 100 Index 9
151 144 142 Index 10
221 207 199 Index 11

1.52 distlib 0.2.9.post0

1.52.1 Available under license :

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=====

Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see <http://www.zope.com>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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1.3 thru 1.5.2	1.2	1995-1999	CNRI	yes
1.6	1.5.2	2000	CNRI	no
2.0	1.6	2000	BeOpen.com	no
1.6.1	1.6	2001	CNRI	yes (2)
2.1	2.0+1.6.1	2001	PSF	no
2.0.1	2.0+1.6.1	2001	PSF	yes
2.1.1	2.1+2.0.1	2001	PSF	yes
2.2	2.1.1	2001	PSF	yes
2.1.2	2.1.1	2002	PSF	yes
2.1.3	2.1.2	2002	PSF	yes
2.2.1	2.2	2002	PSF	yes
2.2.2	2.2.1	2002	PSF	yes
2.2.3	2.2.2	2003	PSF	yes
2.3	2.2.2	2002-2003	PSF	yes
2.3.1	2.3	2002-2003	PSF	yes
2.3.2	2.3.1	2002-2003	PSF	yes
2.3.3	2.3.2	2002-2003	PSF	yes
2.3.4	2.3.3	2004	PSF	yes
2.3.5	2.3.4	2005	PSF	yes
2.4	2.3	2004	PSF	yes
2.4.1	2.4	2005	PSF	yes
2.4.2	2.4.1	2005	PSF	yes
2.4.3	2.4.2	2006	PSF	yes
2.4.4	2.4.3	2006	PSF	yes
2.5	2.4	2006	PSF	yes
2.5.1	2.5	2007	PSF	yes
2.5.2	2.5.1	2008	PSF	yes
2.5.3	2.5.2	2008	PSF	yes
2.6	2.5	2008	PSF	yes
2.6.1	2.6	2008	PSF	yes
2.6.2	2.6.1	2009	PSF	yes
2.6.3	2.6.2	2009	PSF	yes
2.6.4	2.6.3	2009	PSF	yes
2.6.5	2.6.4	2010	PSF	yes
3.0	2.6	2008	PSF	yes
3.0.1	3.0	2009	PSF	yes
3.1	3.0.1	2009	PSF	yes
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=====
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=====

The Distutils2 project was started by Tarek Ziad and is currently maintained by ric Araujo. Many people have contributed to the project.

distlib has started off using some of the code from distutil2.

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1.53 xml-apis 1.3.04

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1.54 coverage 3.6

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Metadata-Version: 1.1

Name: coverage

Version: 3.6

Summary: Code coverage measurement for Python

Home-page: <http://nedbatchelder.com/code/coverage>

Author: Ned Batchelder and others

Author-email: ned@nedbatchelder.com

License: BSD

Description: Coverage.py measures code coverage, typically during test execution. It uses the code analysis tools and tracing hooks provided in the Python standard library to determine which lines are executable, and which have been executed.

Coverage.py runs on Pythons 2.3 through 3.3, and PyPy 1.9.

Documentation is at nedbatchelder.com <<http://nedbatchelder.com/code/coverage>>`. Code repository and issue

tracker are at bitbucket.org <<http://bitbucket.org/ned/coveragepy>>`.

New in 3.6: ``--fail-under``, and >20 bugs closed.

New in 3.5: Branch coverage exclusions, keyboard shortcuts in HTML report.

New in 3.4: Better control over source to measure, and unexecuted files can be reported.

New in 3.3: `.coveragerc` files.

New in 3.2: Branch coverage!

Keywords: code coverage testing

Platform: UNKNOWN

Classifier: Environment :: Console

Classifier: Intended Audience :: Developers

Classifier: License :: OSI Approved :: BSD License

Classifier: Operating System :: OS Independent

Classifier: Programming Language :: Python :: 2

Classifier: Programming Language :: Python :: 3

Classifier: Topic :: Software Development :: Quality Assurance

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* Based upon the plugin by Tzury Bar Yochay:

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* Original idea by:

* Binny V A, http://www.openjs.com/scripts/events/keyboard_shortcuts/

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* QUnit - A JavaScript Unit Testing Framework

*

* <http://docs.jquery.com/QUnit>

*

```

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*/
/**
* jsDump
* Copyright (c) 2008 Ariel Flesler - aflesler(at)gmail(dot)com | http://flesler.blogspot.com
* Licensed under BSD (http://www.opensource.org/licenses/bsd-license.php)
* Date: 5/15/2008
* @projectDescription Advanced and extensible data dumping for Javascript.
* @version 1.0.0
* @author Ariel Flesler
* @link {http://flesler.blogspot.com/2008/05/jsdump-pretty-dump-of-any-javascript.html}
*/
/*
* Javascript Diff Algorithm
* By John Resig (http://ejohn.org/)
* Modified by Chu Alan "sprite"
*
* Released under the MIT license.
*
* More Info:
* http://ejohn.org/projects/javascript-diff-algorithm/
*
* Usage: QUnit.diff(expected, actual)
*
* QUnit.diff("the quick brown fox jumped over", "the quick fox jumps over") == "the quick <del>brown </del> fox
<del>jumped </del><ins>jumps </ins> over"
*/

```

Found in path(s):

```
* /opt/cola/permits/1230279813_1637354508.37/0/coverage-3-6-tar-gz/coverage-3.6/test/qunit/qunit.js
```

No license file was found, but licenses were detected in source scan.

```

/**
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*
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*
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```

Found in path(s):

```
* /opt/cola/permits/1230279813_1637354508.37/0/coverage-3-6-tar-gz/coverage-3.6/test/qunit/qunit.css
```

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```
/*
 * jQuery Templates Plugin 1.0.0pre
 * http://github.com/jquery/jquery-tmpl
 * Requires jQuery 1.4.2
 *
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 */
```

Found in path(s):

```
* /opt/cola/permits/1230279813_1637354508.37/0/coverage-3-6-tar-gz/coverage-3.6/test/qunit/jquery.templ.min.js
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```

```
/*!
 * jQuery JavaScript Library v1.4.3
 * http://jquery.com/
 *
 * Copyright 2010, John Resig
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 * http://jquery.org/license
 *
 * Includes Sizzle.js
 * http://sizzlejs.com/
 * Copyright 2010, The Dojo Foundation
 * Released under the MIT, BSD, and GPL Licenses.
 *
 * Date: Thu Oct 14 23:10:06 2010 -0400
 */
```

Found in path(s):

```
* /opt/cola/permits/1230279813_1637354508.37/0/coverage-3-6-tar-gz/coverage-3.6/coverage/htmlfiles/jquery-1.4.3.min.js
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Found in path(s):

```
* /opt/cola/permits/1230279813_1637354508.37/0/coverage-3-6-tar-gz/coverage-3.6/coverage/__init__.py
```

1.55 boost 1.72.0

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1.56 libgcrypt 1.8.7

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```
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```

```
<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice
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1.57 traceback2 1.4.0

1.57.1 Available under license :

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Metadata-Version: 1.1

Name: traceback2

Version: 1.4.0

Summary: Backports of the traceback module

Home-page: <https://github.com/testing-cabal/traceback2>

Author: Testing-cabal

Author-email: testing-cabal@lists.launchpad.net

License: UNKNOWN

Description: A backport of traceback to older supported Pythons.

```
>>> import traceback2 as traceback
```

Profit.

Things to be aware of!

In Python 2.x, unlike traceback, traceback2 creates unicode output (because it depends on the linecache2 module).

Exception frame clearing silently does nothing if the interpreter in use does not support it.

traceback2._some_str, which while not an official API is so old its likely in use behaves similarly to the Python3 version - objects where unicode(obj) fails but str(object) works will be shown as b'thestrvaluerepr'.

Platform: UNKNOWN
Classifier: Development Status :: 6 - Mature
Classifier: Intended Audience :: Developers
Classifier: License :: OSI Approved :: Python Software Foundation License
Classifier: Operating System :: OS Independent
Classifier: Programming Language :: Python
Classifier: Programming Language :: Python :: 2
Classifier: Programming Language :: Python :: 2.6
Classifier: Programming Language :: Python :: 2.7
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* /opt/cola/permits/1110814192_1606854315.91/0/traceback2-1-4-0-1-tar-gz/traceback2-1.4.0/traceback2.egg-info/PKG-INFO
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1.58 berkeley-db 5.3.28

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- javaee_web_services_client_1_2.xsd
- javaee_6.xsd
- javaee_web_services_1_3.xsd
- javaee_web_services_client_1_3.xsd
- jsp_2_2.xsd
- web-app_3_0.xsd
- web-common_3_0.xsd
- web-fragment_3_0.xsd
- javaee_7.xsd
- javaee_web_services_1_4.xsd
- javaee_web_services_client_1_4.xsd
- jsp_2_3.xsd
- web-app_3_1.xsd
- web-common_3_1.xsd
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- javaee_5.xsd
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- javaee_web_services_client_1_3.xsd
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- web-common_3_0.xsd
- web-fragment_3_0.xsd
- javaee_7.xsd
- javaee_web_services_1_4.xsd
- javaee_web_services_client_1_4.xsd
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1.63 gc 6.4.0

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1.64 html5lib 0.90

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try:

```
from setuptools import setup
except ImportError:
    from distutils.core import setup
import os
```

long_description="""HTML parser designed to follow the HTML5 specification. The parser is designed to handle all flavours of HTML and parses invalid documents using well-defined error handling rules compatible with the behaviour of major desktop web browsers.

Output is to a tree structure; the current release supports output to DOM, ElementTree and lxml tree formats as well as a simple custom format"""

```
classifiers=[
    'Development Status :: 5 - Production/Stable',
    'Intended Audience :: Developers',
    'License :: OSI Approved :: MIT License',
    'Operating System :: OS Independent',
    'Programming Language :: Python',
    'Topic :: Software Development :: Libraries :: Python Modules',
    'Topic :: Text Processing :: Markup :: HTML'
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```

```
setup(name='html5lib',
      version='0.90',
      url='http://code.google.com/p/html5lib/',
      license="MIT License",
      description='HTML parser based on the HTML5 specification',
      long_description=long_description,
      classifiers=classifiers,
      maintainer='James Graham',
      maintainer_email='james@hoppipolla.co.uk',
      packages=['html5lib'] + ['html5lib.'+name
                              for name in os.listdir(os.path.join('src','html5lib'))
                              if os.path.isdir(os.path.join('src','html5lib',name)) and
                              not name.startswith('.')],
      package_dir = {'html5lib': os.path.join('src', 'html5lib')},
      )
```

Found in path(s):

* /opt/cola/permits/1228366170_1637047411.79/0/html5lib-0-90-tar-gz/html5lib-0.90/setup.py

1.65 docutils 0.15.2

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:Contact: goodger@python.org

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1.70 tomcat-catalina-ha 8.5.32

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1.71 zlib 1.2.5

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/* zlib.h -- interface of the 'zlib' general purpose compression library
version 1.2.11, January 15th, 2017

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1.72 clang 6.0.0

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Version: 6.0.0

Summary: UNKNOWN

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Author: Ethan Smith

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1.73 commons-logging 1.1.1

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1.78 ruby 2.7.2p137

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1.79 python-certifi 2020.12.05

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1.80 libxt 1.0.7-1

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1.81 popt 1.16-2

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1.82 linecache 1.0.0

1.82.1 Available under license :

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Metadata-Version: 1.1

Name: linecache2

Version: 1.0.0

Summary: Backports of the linecache module

Home-page: <https://github.com/testing-cabal/linecache2>

Author: Testing-cabal

Author-email: testing-cabal@lists.launchpad.net

License: UNKNOWN

Description: A backport of linecache to older supported Pythons.

```
>>> import linecache2 as linecache
```

Profit.

Platform: UNKNOWN
Classifier: Development Status :: 6 - Mature
Classifier: Intended Audience :: Developers
Classifier: License :: OSI Approved :: Python Software Foundation License
Classifier: Operating System :: OS Independent
Classifier: Programming Language :: Python
Classifier: Programming Language :: Python :: 2
Classifier: Programming Language :: Python :: 2.6
Classifier: Programming Language :: Python :: 2.7
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Classifier: Programming Language :: Python :: 3.2
Classifier: Programming Language :: Python :: 3.3
Classifier: Programming Language :: Python :: 3.4
Classifier: Topic :: Software Development

Found in path(s):

* /opt/cola/permits/1110814177_1606854861.13/0/linecache2-1-0-0-1-tar-gz/linecache2-1.0.0/linecache2.egg-info/PKG-INFO
* /opt/cola/permits/1110814177_1606854861.13/0/linecache2-1-0-0-1-tar-gz/linecache2-1.0.0/PKG-INFO

1.83 ip-address 1.0.23

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1.84 libsoup 2.55.90

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End

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PCRE2 is a library of functions to support regular expressions whose syntax and semantics are as close as possible to those of the Perl 5 language.

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The basic library functions are written in C and are freestanding. Also included in the distribution is a just-in-time compiler that can be used to optimize pattern matching. This is an optional feature that can be omitted when the library is built.

THE BASIC LIBRARY FUNCTIONS

Written by: Philip Hazel
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@end enumerate

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@heading END OF TERMS AND CONDITIONS
@end iftex
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@page
@heading Appendix: How to Apply These Terms to Your New Programs

If you develop a new program, and you want it to be of the greatest

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```
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@var{one line to give the program's name and a brief idea of what it does.}
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```

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```
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Gnomovision comes with ABSOLUTELY NO WARRANTY; for details type `show w'.
This is free software, and you are welcome to redistribute it
under certain conditions; type `show c' for details.
@end smallexample
```

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@example

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'Gnomovision' (which makes passes at compilers) written by James Hacker.

@var{signature of Ty Coon}, 1 April 1989

Ty Coon, President of Vice

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Eclipse

The following artifacts are EPL.

* org.eclipse.jetty.orbit:org.eclipse.jdt.core

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* org.eclipse.jetty.orbit:javafx.security.auth.message

The following artifacts are EPL and CDDL 1.0.

* org.eclipse.jetty.orbit:javafx.mail.glassfish

Oracle

The following artifacts are CDDL + GPLv2 with classpath exception.
<https://glassfish.dev.java.net/nonav/public/CDDL+GPL.html>

* javax.servlet:javax.servlet-api
* javax.annotation:javax.annotation-api
* javax.transaction:javax.transaction-api
* javax.websocket:javax.websocket-api

Oracle OpenJDK

If ALPN is used to negotiate HTTP/2 connections, then the following artifacts may be included in the distribution or downloaded when ALPN module is selected.

* java.sun.security.ssl

These artifacts replace/modify OpenJDK classes. The modifications are hosted at github and both modified and original are under GPL v2 with classpath exceptions.

<http://openjdk.java.net/legal/gplv2+ce.html>

OW2

The following artifacts are licensed by the OW2 Foundation according to the terms of <http://asm.ow2.org/license.html>

org.ow2.asm:asm-commons

org.ow2.asm:asm

Apache

The following artifacts are ASL2 licensed.

org.apache.taglibs:taglibs-standard-spec

org.apache.taglibs:taglibs-standard-impl

MortBay

The following artifacts are ASL2 licensed. Based on selected classes from following Apache Tomcat jars, all ASL2 licensed.

org.mortbay.jasper:apache-jsp

org.apache.tomcat:tomcat-jasper

org.apache.tomcat:tomcat-juli

org.apache.tomcat:tomcat-jsp-api

org.apache.tomcat:tomcat-el-api

org.apache.tomcat:tomcat-jasper-el

org.apache.tomcat:tomcat-api

org.apache.tomcat:tomcat-util-scan

org.apache.tomcat:tomcat-util

org.mortbay.jasper:apache-el

org.apache.tomcat:tomcat-jasper-el
org.apache.tomcat:tomcat-el-api

Mortbay

The following artifacts are CDDL + GPLv2 with classpath exception.

<https://glassfish.dev.java.net/nonav/public/CDDL+GPL.html>

org.eclipse.jetty.toolchain:jetty-schemas

Assorted

The UnixCrypt.java code implements the one way cryptography used by Unix systems for simple password protection. Copyright 1996 Aki Yoshida, modified April 2001 by Iris Van den Broeke, Daniel Deville.

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1.93 go-cache 2.1.0

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1.94 zlib 1.1.3

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- * /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/GZIPEXception.java
- * /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/GZIPOutputStream.java
- * /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/ZInputStream.java
- * /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/Deflater.java
- * /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/InfBlocks.java

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* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-
jar/com/jcraft/jzlib/DeflaterOutputStream.java
* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/Inflater.java
* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/JZlib.java
* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/GZIPInputStream.java
* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/CRC32.java
* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/GZIPHeader.java
* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/InflaterInputStream.java
* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/Checksum.java
* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/ZOutputStream.java
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* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/Inflate.java
* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/ZStream.java
* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/Adler32.java
* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/Deflate.java
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- * /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/ZStreamException.java
- * /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/InfTree.java
- * /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/Tree.java
- * /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/InfCodes.java

1.95 protobuf 1.28.0

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* /opt/cola/permits/1311986373_1652964744.424501/0/grpc-protobuf-1-28-0-sources-1-jar/io/grpc/protobuf/ProtoUtils.java

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* /opt/cola/permits/1311986373_1652964744.424501/0/grpc-protobuf-1-28-0-sources-1-jar/io/grpc/protobuf/ProtoMethodDescriptorSupplier.java

* /opt/cola/permits/1311986373_1652964744.424501/0/grpc-protobuf-1-28-0-sources-1-jar/io/grpc/protobuf/package-info.java

* /opt/cola/permits/1311986373_1652964744.424501/0/grpc-protobuf-1-28-0-sources-1-jar/io/grpc/protobuf/StatusProto.java

* /opt/cola/permits/1311986373_1652964744.424501/0/grpc-protobuf-1-28-0-sources-1-jar/io/grpc/protobuf/ProtoServiceDescriptorSupplier.java

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*/opt/cola/permits/1311986373_1652964744.424501/0/grpc-protobuf-1-28-0-sources-1-jar/io/grpc/protobuf/ProtoFileDescriptorSupplier.java

1.96 nspr 4.29

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```

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```
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1.104 go-playground-validator 10.2.0

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1.105 jemalloc 3.4.0

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1.106 pcre 10.36

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1.107 file 5.19

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1.108 jetty-util 9.4.16.v20190411

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* org.eclipse.jetty.orbit:org.eclipse.jdt.core

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* org.eclipse.jetty.orbit:javafx.security.auth.message

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* org.eclipse.jetty.orbit:javafx.mail.glassfish

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The following artifacts are CDDL + GPLv2 with classpath exception.
<https://glassfish.dev.java.net/nonav/public/CDDL+GPL.html>

* javax.servlet:javax.servlet-api
* javax.annotation:javax.annotation-api
* javax.transaction:javax.transaction-api
* javax.websocket:javax.websocket-api

Oracle OpenJDK

If ALPN is used to negotiate HTTP/2 connections, then the following artifacts may be included in the distribution or downloaded when ALPN module is selected.

* java.sun.security.ssl

These artifacts replace/modify OpenJDK classes. The modifications are hosted at github and both modified and original are under GPL v2 with classpath exceptions.

<http://openjdk.java.net/legal/gplv2+ce.html>

OW2

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org.ow2.asm:asm-commons

org.ow2.asm:asm

Apache

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org.apache.taglibs:taglibs-standard-spec

org.apache.taglibs:taglibs-standard-impl

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org.mortbay.jasper:apache-jsp

org.apache.tomcat:tomcat-jasper

org.apache.tomcat:tomcat-juli

org.apache.tomcat:tomcat-jsp-api

org.apache.tomcat:tomcat-el-api

org.apache.tomcat:tomcat-jasper-el

org.apache.tomcat:tomcat-api

org.apache.tomcat:tomcat-util-scan
org.apache.tomcat:tomcat-util

org.mortbay.jasper:apache-el
org.apache.tomcat:tomcat-jasper-el
org.apache.tomcat:tomcat-el-api

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The following artifacts are CDDL + GPLv2 with classpath exception.

<https://glassfish.dev.java.net/nonav/public/CDDL+GPL.html>

org.eclipse.jetty.toolchain:jetty-schemas

Assorted

The UnixCrypt.java code implements the one way cryptography used by Unix systems for simple password protection. Copyright 1996 Aki Yoshida, modified April 2001 by Iris Van den Broeke, Daniel Deville.

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1.109 jackson-dataformat-yaml 2.11.2

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Jackson JSON processor

Jackson is a high-performance, Free/Open Source JSON processing library. It was originally written by Tatu Saloranta (tatu.saloranta@iki.fi), and has been in development since 2007.

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In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see <http://www.zope.com>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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1.3 thru 1.5.2	1.2	1995-1999	CNRI	yes
1.6	1.5.2	2000	CNRI	no
2.0	1.6	2000	BeOpen.com	no
1.6.1	1.6	2001	CNRI	yes (2)
2.1	2.0+1.6.1	2001	PSF	no
2.0.1	2.0+1.6.1	2001	PSF	yes
2.1.1	2.1+2.0.1	2001	PSF	yes
2.2	2.1.1	2001	PSF	yes
2.1.2	2.1.1	2002	PSF	yes
2.1.3	2.1.2	2002	PSF	yes
2.2.1	2.2	2002	PSF	yes
2.2.2	2.2.1	2002	PSF	yes
2.2.3	2.2.2	2003	PSF	yes
2.3	2.2.2	2002-2003	PSF	yes
2.3.1	2.3	2002-2003	PSF	yes
2.3.2	2.3.1	2002-2003	PSF	yes
2.3.3	2.3.2	2002-2003	PSF	yes
2.3.4	2.3.3	2004	PSF	yes
2.3.5	2.3.4	2005	PSF	yes
2.4	2.3	2004	PSF	yes
2.4.1	2.4	2005	PSF	yes
2.4.2	2.4.1	2005	PSF	yes
2.4.3	2.4.2	2006	PSF	yes
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2.5	2.4	2006	PSF	yes
2.5.1	2.5	2007	PSF	yes
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2.5.3	2.5.2	2008	PSF	yes
2.6	2.5	2008	PSF	yes
2.6.1	2.6	2008	PSF	yes
2.6.2	2.6.1	2009	PSF	yes
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Julian Seward, Cambridge, UK.

jseward@acm.org

bzip2/libbzip2 version 1.0.2 of 30 December 2001

/*

minibz2

libbz2.dll test program.

by Yoshioka Tsuneo (tsuneo@rr.iij4u.or.jp)

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usage: minibz2 [-d] [-{1,2,..9}] [[srcfilename] destfilename]

*/

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That's all there is to it!

1.115 hk2-api 2.5.0

1.116 lucene-analyzers 3.0.0

1.116.1 Available under license :

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The snowball stemmers in
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Includes lib/servlet-api-2.4.jar from Apache Tomcat

The SmartChineseAnalyzer source code (under contrib/analyzers) was provided by Xiaoping Gao and copyright 2009 by www.imdict.net.

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1.117 meson 0.56.0

1.117.1 Available under license :

```
project('bar', 'c', license: 'Apache')
```

```
executable('bar', 'bar.c')
```

```
license = meson.project_license()[0]  
if license != 'Apache'  
    error("The license should be Apache, but it is: " + license)  
endif
```

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```
* /opt/cola/permits/1110905341_1606871385.15/0/kjd-idna-v2-10-0-ge5c11db-1-tar-gz/kjd-idna-b8defb9/LICENSE.rst
```

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"""

A library to support the Internationalised Domain Names in Applications (IDNA) protocol as specified in RFC 5890 et.al. This new methodology, known as IDNA 2008, can generate materially different results to the previous standard. The library can act as a drop-in replacement for the "encodings.idna" module.

"""

```
import io, sys
```

```
from setuptools import setup
```

```
def main():
```

```
    python_version = sys.version_info[:2]
```

```
    if python_version < (2,7):
```

```
        raise SystemExit("Sorry, Python 2.7 or newer required")
```

```
    package_data = { }
```

```
    exec(open('idna/package_data.py').read(), package_data)
```

```
    arguments = {
```

```
        'name': 'idna',
```

```
        'packages': ['idna'],
```

```
        'version': package_data['__version__'],
```

```
        'description': 'Internationalized Domain Names in Applications (IDNA)',
```

```
        'long_description': io.open("README.rst", encoding="UTF-8").read(),
```

```
        'author': 'Kim Davies',
```

```
        'author_email': 'kim@cynosure.com.au',
```

```
        'license': 'BSD-like',
```

```
        'url': 'https://github.com/kjd/idna',
```

```
        'classifiers': [
```

```
            'Development Status :: 5 - Production/Stable',
```

```
            'Intended Audience :: Developers',
```

```
            'Intended Audience :: System Administrators',
```

```

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'Operating System :: OS Independent',
'Programming Language :: Python',
'Programming Language :: Python :: 2',
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'Programming Language :: Python :: 3.7',
'Programming Language :: Python :: 3.8',
'Programming Language :: Python :: Implementation :: CPython',
'Programming Language :: Python :: Implementation :: PyPy',
'Topic :: Internet :: Name Service (DNS)',
'Topic :: Software Development :: Libraries :: Python Modules',
'Topic :: Utilities',
],
'python_requires': '>=2.7, !=3.0.*, !=3.1.*, !=3.2.*, !=3.3.*',
}

setup(**arguments)

if __name__ == '__main__':
    main()

```

Found in path(s):

* /opt/cola/permits/1110905341_1606871385.15/0/kjd-idna-v2-10-0-ge5c11db-1-tar-gz/kjd-idna-b8defb9/setup.py

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1.122 jackson 1.0.1

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```
* /opt/cola/permits/1146957908_1616828942.14/0/azure-core-serializer-json-jackson-1-0-1-sources-jar/com/azure/core/serializer/json/jackson/package-info.java  
* /opt/cola/permits/1146957908_1616828942.14/0/azure-core-serializer-json-jackson-1-0-1-sources-jar/com/azure/core/serializer/json/jackson/JacksonJsonSerializerBuilder.java  
* /opt/cola/permits/1146957908_1616828942.14/0/azure-core-serializer-json-jackson-1-0-1-sources-jar/com/azure/core/serializer/json/jackson/JacksonJsonSerializerProvider.java  
* /opt/cola/permits/1146957908_1616828942.14/0/azure-core-serializer-json-jackson-1-0-1-sources-jar/module-info.java  
* /opt/cola/permits/1146957908_1616828942.14/0/azure-core-serializer-json-jackson-1-0-1-sources-jar/com/azure/core/serializer/json/jackson/JacksonJsonSerializer.java
```

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Found in path(s):

```
* /opt/cola/permits/1146957908_1616828942.14/0/azure-core-serializer-json-jackson-1-0-1-sources-jar/META-INF/maven/com.azure/azure-core-serializer-json-jackson/pom.xml
```

1.123 python-babel 2.8.0

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1.124 tomcat-annotations-api 8.5.32

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1.125 jinja2 2.11.2

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```
import io
import re

from setuptools import find_packages
from setuptools import setup

with io.open("README.rst", "rt", encoding="utf8") as f:
    readme = f.read()

with io.open("src/jinja2/__init__.py", "rt", encoding="utf8") as f:
    version = re.search(r'__version__ = "(.*?)"', f.read(), re.M).group(1)

setup(
```

```

name="Jinja2",
version=version,
url="https://palletsprojects.com/p/jinja/",
project_urls={
    "Documentation": "https://jinja.palletsprojects.com/",
    "Code": "https://github.com/pallets/jinja",
    "Issue tracker": "https://github.com/pallets/jinja/issues",
},
license="BSD-3-Clause",
author="Armin Ronacher",
author_email="armin.ronacher@active-4.com",
maintainer="Pallets",
maintainer_email="contact@palletsprojects.com",
description="A very fast and expressive template engine.",
long_description=readme,
classifiers=[
    "Development Status :: 5 - Production/Stable",
    "Environment :: Web Environment",
    "Intended Audience :: Developers",
    "License :: OSI Approved :: BSD License",
    "Operating System :: OS Independent",
    "Programming Language :: Python",
    "Programming Language :: Python :: 2",
    "Programming Language :: Python :: 2.7",
    "Programming Language :: Python :: 3",
    "Programming Language :: Python :: 3.5",
    "Programming Language :: Python :: 3.6",
    "Programming Language :: Python :: 3.7",
    "Programming Language :: Python :: 3.8",
    "Programming Language :: Python :: Implementation :: CPython",
    "Programming Language :: Python :: Implementation :: PyPy",
    "Topic :: Internet :: WWW/HTTP :: Dynamic Content",
    "Topic :: Software Development :: Libraries :: Python Modules",
    "Topic :: Text Processing :: Markup :: HTML",
],
packages=find_packages("src"),
package_dir={"": "src"},
include_package_data=True,
python_requires=">=2.7, !=3.0.*, !=3.1.*, !=3.2.*, !=3.3.*, !=3.4.*",
install_requires=["MarkupSafe>=0.23"],
extras_require={"i18n": ["Babel>=0.8"]},
entry_points={"babel.extractors": ["jinja2 = jinja2.ext:babel_extract[i18n]"]},
)

```

Found in path(s):

* /opt/cola/permits/1160880513_1620412497.03/0/jinja2-2-11-2-1-tar-gz/Jinja2-2.11.2/setup.py

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Metadata-Version: 2.1

Name: Jinja2

Version: 2.11.2

Summary: A very fast and expressive template engine.

Home-page: <https://palletsprojects.com/p/jinja/>

Author: Armin Ronacher

Author-email: armin.ronacher@active-4.com

Maintainer: Pallets

Maintainer-email: contact@palletsprojects.com

License: BSD-3-Clause

Project-URL: Documentation, <https://jinja.palletsprojects.com/>

Project-URL: Code, <https://github.com/pallets/jinja>

Project-URL: Issue tracker, <https://github.com/pallets/jinja/issues>

Description: Jinja

=====

Jinja is a fast, expressive, extensible templating engine. Special placeholders in the template allow writing code similar to Python syntax. Then the template is passed data to render the final document.

It includes:

- Template inheritance and inclusion.
- Define and import macros within templates.
- HTML templates can use autoescaping to prevent XSS from untrusted user input.
- A sandboxed environment can safely render untrusted templates.
- AsyncIO support for generating templates and calling async functions.
- I18N support with Babel.
- Templates are compiled to optimized Python code just-in-time and cached, or can be compiled ahead-of-time.
- Exceptions point to the correct line in templates to make debugging easier.
- Extensible filters, tests, functions, and even syntax.

Jinja's philosophy is that while application logic belongs in Python if possible, it shouldn't make the template designer's job difficult by restricting functionality too much.

Installing

Install and update using `pip`:

```
.. code-block:: text
```

```
$ pip install -U Jinja2
```

```
.. _pip: https://pip.pypa.io/en/stable/quickstart/
```

In A Nutshell

```
.. code-block:: jinja
```

```
{% extends "base.html" %}
{% block title %}Members{% endblock %}
{% block content %}
<ul>
{% for user in users %}
```

```
<li><a href="{{ user.url }}">{{ user.username }}</a></li>
{% endfor %}
</ul>
{% endblock %}
```

Links

- Website: <https://palletsprojects.com/p/jinja/>
- Documentation: <https://jinja.palletsprojects.com/>
- Releases: <https://pypi.org/project/Jinja2/>
- Code: <https://github.com/pallets/jinja>
- Issue tracker: <https://github.com/pallets/jinja/issues>
- Test status: https://dev.azure.com/pallets/jinja/_build
- Official chat: <https://discord.gg/t6rrQZH>

Platform: UNKNOWN

Classifier: Development Status :: 5 - Production/Stable

Classifier: Environment :: Web Environment

Classifier: Intended Audience :: Developers

Classifier: License :: OSI Approved :: BSD License

Classifier: Operating System :: OS Independent

Classifier: Programming Language :: Python

Classifier: Programming Language :: Python :: 2

Classifier: Programming Language :: Python :: 2.7

Classifier: Programming Language :: Python :: 3

Classifier: Programming Language :: Python :: 3.5

Classifier: Programming Language :: Python :: 3.6

Classifier: Programming Language :: Python :: 3.7

Classifier: Programming Language :: Python :: 3.8

Classifier: Programming Language :: Python :: Implementation :: CPython

Classifier: Programming Language :: Python :: Implementation :: PyPy

Classifier: Topic :: Internet :: WWW/HTTP :: Dynamic Content

Classifier: Topic :: Software Development :: Libraries :: Python Modules

Classifier: Topic :: Text Processing :: Markup :: HTML

Requires-Python: >=2.7, !=3.0.*, !=3.1.*, !=3.2.*, !=3.3.*, !=3.4.*

Description-Content-Type: text/x-rst

Provides-Extra: i18n

Found in path(s):

* /opt/cola/permits/1160880513_1620412497.03/0/jinja2-2-11-2-1-tar-gz/Jinja2-2.11.2/PKG-INFO

* /opt/cola/permits/1160880513_1620412497.03/0/jinja2-2-11-2-1-tar-gz/Jinja2-2.11.2/src/Jinja2.egg-info/PKG-INFO

1.126 tomcat-i18n-fr 8.5.32

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1.128 python-requests 2.22.0

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1.129 pyopenssl 19.1.0

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1.130 Idns 1.7.1

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/* From openssl 4.3p2 filename openssl-compat/fake-rfc2553.h */

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```
/* from openssh 4.3p2 compat/strncpy.c */
```

```
/*
```

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```

```
*/
```

```
/*
```

```
* FILE: sha2.c
```

```
* AUTHOR: Aaron D. Gifford - http://www.aarongifford.com/
```

```
*
```

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```
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```

```
*
```

```
* Modified by Jelte Jansen to fit in Idns, and not clash with any  
* system-defined SHA code.
```

```
* Changes:
```

```
* - Renamed (external) functions and constants to fit Idns style
```

```
* - Removed _End and _Data functions
```

```
* - Added Idns_shaX(data, len, digest) convenience functions
```

```
* - Removed prototypes of _Transform functions and made those static
```

```
*
```

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*

* \$Id: sha2.c,v 1.1 2001/11/08 00:01:51 adg Exp adg \$

*/

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/* From openssl4.3p2 compat/inet_aton.c */

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1.132 x-text 1.17.9

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1.134 libxml2-python 2.9.10

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1.135 jackson-databind 2.11.1

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1.136 python-certifi 2020.04.05.1

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ca-bundle.crt -- Bundle of CA Root Certificates

Certificate data from Mozilla as of: Thu Nov 3 19:04:19 2011#

This is a bundle of X.509 certificates of public Certificate Authorities (CA). These were automatically extracted from Mozilla's root certificates file (certdata.txt). This file can be found in the mozilla source tree:

<http://mxr.mozilla.org/mozilla/source/security/nss/lib/ckfw/builtins/certdata.txt?raw=1#>

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Just configure this file as the SSLCACertificateFile.#

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@(#) \$RCSfile: certdata.txt,v \$ \$Revision: 1.80 \$ \$Date: 2011/11/03 15:11:58 \$

1.137 urllib3 1.26.16

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1.138 zlib 1.2.1

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```
/* zlib.h -- interface of the 'zlib' general purpose compression library
   version 1.2.1, November 17th, 2003
```

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Jean-loup Gailly Mark Adler
jloup@gzip.org madler@alumni.caltech.edu

The data format used by the zlib library is described by RFCs (Request for Comments) 1950 to 1952 in the files <http://www.ietf.org/rfc/rfc1950.txt>

(zlib format), rfc1951.txt (deflate format) and rfc1952.txt (gzip format).

*/

1.139 pygments 2.5.2

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1.140 httpcomponents-core 4.4.13

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1.141 tcl 8.5.19

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1.142 tomcat-i18n-es 8.5.32

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1.143 libxml2 2.9.10

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1.144 python 3.7.9

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to

BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations, which became Zope Corporation. In 2001, the Python Software Foundation (PSF, see <https://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation was a sponsoring member of the PSF.

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Release	Derived from	Year	Owner	GPL-compatible? (1)
0.9.0 thru 1.2		1991-1995	CWI	yes
1.3 thru 1.5.2	1.2	1995-1999	CNRI	yes
1.6	1.5.2	2000	CNRI	no
2.0	1.6	2000	BeOpen.com	no
1.6.1	1.6	2001	CNRI	yes (2)
2.1	2.0+1.6.1	2001	PSF	no
2.0.1	2.0+1.6.1	2001	PSF	yes
2.1.1	2.1+2.0.1	2001	PSF	yes
2.1.2	2.1.1	2002	PSF	yes
2.1.3	2.1.2	2002	PSF	yes
2.2 and above	2.1.1	2001-now	PSF	yes

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1.146 libnet 1.2

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libnet 1.1.x

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1.147 mesa 17.1.4

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1.148 commons-daemon 1.1.0

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1.149 markupsafe 1.1.1

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 * ~~~~~
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 * C implementation of escaping for better performance. Used instead of
 * the native Python implementation when compiled.
 *
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 */
```

Found in path(s):

```
* /opt/cola/permits/1160876786_1620411589.43/0/sailfishos-mirror-markupsafe-1-1-1-0-g22c946d-1-tar-gz/sailfishos-mirror-markupsafe-22c946d/src/markupsafe/_speedups.c
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* /opt/cola/permits/1160876786_1620411589.43/0/sailfishos-mirror-markupsafe-1-1-1-0-g22c946d-1-tar-gz/sailfishos-mirror-markupsafe-22c946d/LICENSE.rst

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:license: BSD-3-Clause

Found in path(s):

* /opt/cola/permits/1160876786_1620411589.43/0/sailfishos-mirror-markupsafe-1-1-1-0-g22c946d-1-tar-gz/sailfishos-mirror-markupsafe-22c946d/src/markupsafe/_native.py

* /opt/cola/permits/1160876786_1620411589.43/0/sailfishos-mirror-markupsafe-1-1-1-0-g22c946d-1-tar-gz/sailfishos-mirror-markupsafe-22c946d/src/markupsafe/_constants.py

* /opt/cola/permits/1160876786_1620411589.43/0/sailfishos-mirror-markupsafe-1-1-1-0-g22c946d-1-tar-gz/sailfishos-mirror-markupsafe-22c946d/src/markupsafe/__init__.py

* /opt/cola/permits/1160876786_1620411589.43/0/sailfishos-mirror-markupsafe-1-1-1-0-g22c946d-1-tar-gz/sailfishos-mirror-markupsafe-22c946d/src/markupsafe/_compat.py

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Found in path(s):

* /opt/cola/permits/1160876786_1620411589.43/0/sailfishos-mirror-markupsafe-1-1-1-0-g22c946d-1-tar-gz/sailfishos-mirror-markupsafe-22c946d/README.rst

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* /opt/cola/permits/1160876786_1620411589.43/0/sailfishos-mirror-markupsafe-1-1-1-0-g22c946d-1-tar-gz/sailfishos-mirror-markupsafe-22c946d/setup.py

1.150 jaffl 0.5.1

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```
<project xmlns="http://maven.apache.org/POM/4.0.0" xmlns:xsi="http://www.w3.org/2001/XMLSchema-instance"
xsi:schemaLocation="http://maven.apache.org/POM/4.0.0 http://maven.apache.org/maven-v4_0_0.xsd">
  <modelVersion>4.0.0</modelVersion>
  <groupId>org.jruby.extras</groupId>
  <artifactId>jaffl</artifactId>
  <packaging>jar</packaging>
  <version>0.5.12</version>
  <name>jaffl</name>
  <description>An abstracted interface to invoking native functions from java</description>
  <url>http://github.com/wmeissner/jaffl</url>
  <licenses>
    <license>
      <name>The Apache Software License, Version 2.0</name>
      <url>http://www.apache.org/licenses/LICENSE-2.0.txt</url>
      <distribution>repo</distribution>
    </license>
  </licenses>
  <scm>
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    <url>http://github.com/wmeissner/jaffl</url>
  </scm>
  <distributionManagement>
    <repository>
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      <name>JRuby Central Repository</name>
      <url>dav:https://dav.codehaus.org/repository/jruby</url>
    </repository>
    <snapshotRepository>
      <id>codehaus-jruby-snapshot-repository</id>
      <name>JRuby Central Development Repository</name>
      <url>dav:https://dav.codehaus.org/snapshots.repository/jruby</url>
    </snapshotRepository>
  </distributionManagement>
</project>
```

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<repository>
  <id>maven2-repository.dev.java.net</id>
  <name>Java.net Repository for Maven</name>
  <url>http://download.java.net/maven/2/</url>
</repository>
<repository>
  <id>codehaus</id>
  <name>Codehaus Repository</name>
  <releases>
    <enabled>true</enabled>
  </releases>
  <snapshots>
    <enabled>true</enabled>
  </snapshots>
  <url>http://repository.codehaus.org</url>
</repository>
</repositories>
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  <pluginRepository>
    <id>maven2-repository.dev.java.net</id>
    <name>Java.net Repository for Maven</name>
    <url>http://download.java.net/maven/2/</url>
  </pluginRepository>
</pluginRepositories>
<developers>
  <developer>
    <id>wmeissner</id>
    <name>Wayne Meissner</name>
    <email>wmeissner@gmail.com</email>
  </developer>
</developers>
<properties>
  <maven.test.failure.ignore>true</maven.test.failure.ignore>
</properties>
<dependencies>
  <dependency>
    <groupId>junit</groupId>
    <artifactId>junit</artifactId>
    <version>4.5</version>
    <scope>test</scope>
  </dependency>
  <dependency>
    <groupId>org.jruby.extras</groupId>
    <artifactId>jffi</artifactId>
    <version>1.0.6</version>
    <scope>compile</scope>
  </dependency>
  <dependency>
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<version>1.0.6</version>
<scope>runtime</scope>
<classifier>native</classifier>
</dependency>
<dependency>
  <groupId>asm</groupId>
  <artifactId>asm</artifactId>
  <version>3.2</version>
  <scope>compile</scope>
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  <groupId>asm</groupId>
  <artifactId>asm-commons</artifactId>
  <version>3.2</version>
  <scope>compile</scope>
</dependency>
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  <groupId>org.jruby.extras</groupId>
  <artifactId>jnr-x86asm</artifactId>
  <version>1.0.0</version>
  <scope>compile</scope>
</dependency>
</dependencies>
<build>
  <sourceDirectory>src</sourceDirectory>
  <testSourceDirectory>test</testSourceDirectory>
  <plugins>
    <plugin>
      <groupId>org.apache.maven.plugins</groupId>
      <artifactId>maven-compiler-plugin</artifactId>
      <version>2.0.2</version>
      <configuration>
        <source>1.5</source>
        <target>1.5</target>
      </configuration>
    </plugin>
    <plugin>
      <artifactId>maven-antrun-plugin</artifactId>
      <version>1.1</version>
      <executions>
        <execution>
          <phase>test-compile</phase>
          <configuration>
            <tasks>
              <exec dir="{basedir}" executable="make" failonerror="true">
                <arg line="-f libtest/GNUMakefile"/>
              </exec>
            </tasks>
          </configuration>
        </execution>
      </executions>
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  </plugins>
</build>

```

```

    <arg line="BUILD_DIR=${build.directory}"/>
  </exec>
</tasks>
</configuration>
<goals>
  <goal>run</goal>
</goals>
</execution>
</executions>
</plugin>
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  <groupId>org.apache.maven.plugins</groupId>
  <artifactId>maven-surefire-plugin</artifactId>
  <version>2.4.2</version>
  <configuration>
    <systemProperties>
      <property>
        <name>jna.library.path</name>
        <value>${build.directory}</value>
      </property>
    </systemProperties>
  </configuration>
</plugin>
</plugins>
<extensions>
  <extension>
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    <artifactId>wagon-svn</artifactId>
    <version>1.8</version>
  </extension>
  <extension>
    <groupId>org.apache.maven.wagon</groupId>
    <artifactId>wagon-webdav</artifactId>
  </extension>
</extensions>
</build>
<reporting>
  <outputDirectory>build/report</outputDirectory>
</reporting>
</project>

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Found in path(s):

* /opt/cola/permits/1257263866_1675746964.5826902/0/jaffl-0-5-12-jar/META-INF/maven/org.jruby.extras/jaffl/pom.xml

1.151 libsm 1.1.1

1.151.1 Available under license :

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1.152 xerces-j 2.12.0

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1.153 tomcat-jasper-el 8.5.32

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1.154 xz 5.0.5

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1.156 icu 68

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## Commands to generate dependency files  
GEN_DEPS.c= $(CC) -E -MM $(DEFS) $(CPPFLAGS)  
GEN_DEPS.cc= $(CXX) -E -MM $(DEFS) $(CPPFLAGS)  
  
## Flags for position independent code  
SHAREDLIBCFLAGS = -fPIC  
SHAREDLIBCXXFLAGS = -fPIC  
SHAREDLIBCPPFLAGS = -DPIC  
  
## Additional flags when building libraries and with threads  
THREADSCPPFLAGS = -D_REENTRANT  
LIBCPPFLAGS =  
  
## Compiler switch to embed a runtime search path  
LD_RPATH=  
LD_RPATH_PRE= -Wl,-rpath,  
  
## Compiler switch to embed a library name  
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))  
  
## Shared library options  
LD_SOOPTIONS= -Wl,-Bsymbolic
```

```

## Shared object suffix
SO = so
## Non-shared intermediate object suffix
STATIC_O = ao

## Compilation rules
%. $(STATIC_O): $(srcdir)/%.c
$(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<
%.o: $(srcdir)/%.c
$(COMPILE.c) $(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $<

%. $(STATIC_O): $(srcdir)/%.cpp
$(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<
%.o: $(srcdir)/%.cpp
$(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<

## Dependency rules
%.d: $(srcdir)/%.c
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.c) $< \
| sed "s^\($*\)\.o[ :]*^1.o $@ : /g\" > $@; \
[ -s $@ ] || rm -f $@'

%.d: $(srcdir)/%.cpp
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.cc) $< \
| sed "s^\($*\)\.o[ :]*^1.o $@ : /g\" > $@; \
[ -s $@ ] || rm -f $@'

## Versioned libraries rules

%. $(SO).$(SO_TARGET_VERSION_MAJOR): %. $(SO).$(SO_TARGET_VERSION)
$(RM) $@ && ln -s ${<F} $@
%. $(SO): %. $(SO).$(SO_TARGET_VERSION_MAJOR)
$(RM) $@ && ln -s ${*F}. $(SO).$(SO_TARGET_VERSION) $@

## Bind internal references

# LDflags that pkgdata will use
BIR_LDFLAGS= -Wl,-Bsymbolic

# Dependencies [i.e. map files] for the final library
BIR_DEPS=

## Remove shared library 's'
STATIC_PREFIX_WHEN_USED =
STATIC_PREFIX =

```

End BSD-specific setup

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1.158 zstd 1.4.8

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1.159 zstd 1.16.3

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For Blosc - A blocking, shuffling and lossless compression library

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- LZ4 homepage : <http://fastcompression.blogspot.com/p/lz4.html>
- LZ4 source repository : <http://code.google.com/p/lz4/>
ISO C9x compliant stdint.h for Microsoft Visual Studio
Based on ISO/IEC 9899:TC2 Committee draft (May 6, 2005) WG14/N1124

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1.160 distlib 0.3.0

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=====

Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see <http://www.zope.com>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

All Python releases are Open Source (see <http://www.opensource.org> for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

Release	Derived from	Year	Owner	GPL-compatible? (1)
0.9.0 thru 1.2		1991-1995	CWI	yes
1.3 thru 1.5.2	1.2	1995-1999	CNRI	yes
1.6	1.5.2	2000	CNRI	no
2.0	1.6	2000	BeOpen.com	no
1.6.1	1.6	2001	CNRI	yes (2)
2.1	2.0+1.6.1	2001	PSF	no
2.0.1	2.0+1.6.1	2001	PSF	yes
2.1.1	2.1+2.0.1	2001	PSF	yes
2.2	2.1.1	2001	PSF	yes
2.1.2	2.1.1	2002	PSF	yes
2.1.3	2.1.2	2002	PSF	yes
2.2.1	2.2	2002	PSF	yes
2.2.2	2.2.1	2002	PSF	yes
2.2.3	2.2.2	2003	PSF	yes
2.3	2.2.2	2002-2003	PSF	yes
2.3.1	2.3	2002-2003	PSF	yes
2.3.2	2.3.1	2002-2003	PSF	yes
2.3.3	2.3.2	2002-2003	PSF	yes
2.3.4	2.3.3	2004	PSF	yes
2.3.5	2.3.4	2005	PSF	yes
2.4	2.3	2004	PSF	yes
2.4.1	2.4	2005	PSF	yes
2.4.2	2.4.1	2005	PSF	yes
2.4.3	2.4.2	2006	PSF	yes
2.4.4	2.4.3	2006	PSF	yes
2.5	2.4	2006	PSF	yes
2.5.1	2.5	2007	PSF	yes
2.5.2	2.5.1	2008	PSF	yes
2.5.3	2.5.2	2008	PSF	yes
2.6	2.5	2008	PSF	yes

2.6.1	2.6	2008	PSF	yes
2.6.2	2.6.1	2009	PSF	yes
2.6.3	2.6.2	2009	PSF	yes
2.6.4	2.6.3	2009	PSF	yes
2.6.5	2.6.4	2010	PSF	yes
3.0	2.6	2008	PSF	yes
3.0.1	3.0	2009	PSF	yes
3.1	3.0.1	2009	PSF	yes
3.1.1	3.1	2009	PSF	yes
3.1.2	3.1	2010	PSF	yes
3.2	3.1	2010	PSF	yes

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Distutils2 Contributors
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The Distutils2 project was started by Tarek Ziad and is currently maintained by ric Araujo. Many people have contributed to the project.

distlib has started off using some of the code from distutil2.

If you're making a patch, please add your name below in alphabetical order, and welcome into the Fellowship of the Packaging!

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1.167 gmp 5.1.3

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- * HTTPS patch (which inspired HTTPSConnectionPool)

- * erikcederstrand <<http://code.google.com/u/erikcederstrand/>>
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- * Client-verified SSL certificates for HTTPSConnectionPool
- * Response gzip and deflate encoding support
- * Better unicode support for filepost using StringIO buffers

- * btoconnor <brian@btoconnor.net>
- * Non-multipart encoding for POST requests

- * p.dobrogost <<http://code.google.com/u/@WBRSRIBZDhBFXQB6/>>
- * Code review, PEP8 compliance, benchmark fix

- * kennethreitz <me@kennethreitz.com>
- * Bugfixes, suggestions, Requests integration

- * georgemarshall <<http://github.com/georgemarshall>>
- * Bugfixes, Improvements and Test coverage

- * Thomas Kluyver <thomas@kluyver.me.uk>
- * Python 3 support

- * brandon-rhodes <<http://rhodesmill.org/brandon>>

- * Design review, bugfixes, test coverage.

- * studer <theo.studer@gmail.com>
- * IPv6 url support and test coverage

- * Shivaram Lingamneni <slingamn@cs.stanford.edu>
- * Support for explicitly closing pooled connections

- * hartator <hartator@gmail.com>
- * Corrected multipart behavior for params

- * Thomas Weischuh <thomas@t-8ch.de>
- * Support for TLS SNI
- * API unification of ssl_version/cert_reqs
- * SSL fingerprint and alternative hostname verification
- * Bugfixes in testsuite

- * Sune Kirkeby <mig@ibofobi.dk>
- * Optional SNI-support for Python 2 via PyOpenSSL.

- * Marc Schlaich <marc.schlaich@gmail.com>
- * Various bugfixes and test improvements.

- * Bryce Boe <bbzbryce@gmail.com>
- * Correct six.moves conflict
- * Fixed pickle support of some exceptions

- * Boris Figovsky <boris.figovsky@ravello systems.com>
- * Allowed to skip SSL hostname verification

- * Cory Benfield <<http://lukasa.co.uk/about/>>
- * Stream method for Response objects.
- * Return native strings in header values.
- * Generate 'Host' header when using proxies.

- * Jason Robinson <jaywink@basshero.org>
- * Add missing WrappedSocket.fileno method in PyOpenSSL

- * Audrius Butkevicius <audrius.butkevicius@elastichosts.com>
- * Fixed a race condition

- * Stanislav Vitkovskiy <stas.vitkovsky@gmail.com>
- * Added HTTPS (CONNECT) proxy support

- * Stephen Holsapple <sholsapp@gmail.com>
- * Added abstraction for granular control of request fields

- * Martin von Gagern <Martin.vGagern@gmx.net>

- * Support for non-ASCII header parameters

- * Kevin Burke <kev@inburke.com> and Pavel Kirichenko <juanych@yandex-team.ru>
- * Support for separate connect and request timeouts

- * Peter Waller <p@pwaller.net>
- * HTTPResponse.tell() for determining amount received over the wire

- * Nipunn Koorapati <nipunn1313@gmail.com>
- * Ignore default ports when comparing hosts for equality

- * Danilo @dbrgn <http://dbrgn.ch/>
- * Disabled TLS compression by default on Python 3.2+
- * Disabled TLS compression in pyopenssl contrib module
- * Configurable cipher suites in pyopenssl contrib module

- * Roman Bogorodskiy <roman.bogorodskiy@ericsson.com>
- * Account retries on proxy errors

- * Nicolas Delaby <nicolas.delaby@ezeep.com>
- * Use the platform-specific CA certificate locations

- * Josh Schneier <https://github.com/jschneier>
- * HTTPHeaderDict and associated tests and docs
- * Bugfixes, docs, test coverage

- * Tahia Khan <http://tahia.tk/>
- * Added Timeout examples in docs

- * Arthur Grunseid <http://grunseid.com>
- * source_address support and tests (with https://github.com/bui)

- * Ian Cordasco <graffatcolmingov@gmail.com>
- * PEP8 Compliance and Linting
- * Add ability to pass socket options to an HTTP Connection

- * Erik Tollerud <erik.tollerud@gmail.com>
- * Support for standard library io module.

- * Krishna Prasad <kprasad.iitd@gmail.com>
- * Google App Engine documentation

- * Aaron Meurer <asmeurer@gmail.com>
- * Added Url.url, which unparses a Url

- * Evgeny Kapun <abacabadabacaba@gmail.com>
- * Bugfixes

- * Benjamin Meyer <bm_witness@yahoo.com>
- * Security Warning Documentation update for proper capture

- * Shivan Sornarajah <github@sornars.com>
- * Support for using ConnectionPool and PoolManager as context managers.

- * Alex Gaynor <alex.gaynor@gmail.com>
- * Updates to the default SSL configuration

- * Tomas Tomecek <ttomecek@redhat.com>
- * Implemented generator for getting chunks from chunked responses.

- * tlynn <https://github.com/tlynn>
- * Respect the warning preferences at import.

- * David D. Riddle <ddriddle@illinois.edu>
- * IPv6 bugfixes in testsuite

- * Jon Wayne Parrott <jonwayne@google.com>
- * App Engine environment tests.

- * John Krauss <https://github.com/talos>
- * Clues to debugging problems with `cryptography` dependency in docs

- * Disassem <https://github.com/Disassem>
- * Fix pool-default headers not applying for url-encoded requests like GET.

- * James Atherfold <jlatherfold@hotmail.com>
- * Bugfixes relating to cleanup of connections during errors.

- * Christian Pedersen <https://github.com/chripede>
- * IPv6 HTTPS proxy bugfix

- * Jordan Moldow <https://github.com/jmoldow>
- * Fix low-level exceptions leaking from ``HTTPResponse.stream()``.
- * Bugfix for ``ConnectionPool.urlopen(release_conn=False)``.
- * Creation of ``HTTPConnectionPool.ResponseCls``.

- * Predrag Gruevski <https://github.com/obi1kenobi>
- * Made cert digest comparison use a constant-time algorithm.

- * Adam Talsma <https://github.com/a-tal>
- * Bugfix to ca_cert file paths.

- * Evan Meagher <https://evanmeagher.net>
- * Bugfix related to `memoryview` usage in PyOpenSSL adapter

- * John Vandenberg <jayvdb@gmail.com>

- * Python 2.6 fixes; pyflakes and pep8 compliance

- * Andy Caldwell <andy.m.caldwell@googlemail.com>
 - * Bugfix related to reusing connections in indeterminate states.

- * Ville Skytt <ville.skytta@iki.fi>
 - * Logging efficiency improvements, spelling fixes, Travis config.

- * Shige Takeda <smtakeda@gmail.com>
 - * Started Recipes documentation and added a recipe about handling concatenated gzip data in HTTP response

- * Jesse Shapiro <jesse@jesseshapiro.net>
 - * Various character-encoding fixes/tweaks
 - * Disabling IPv6 DNS when IPv6 connections not supported

- * David Foster <http://dafoster.net/>
 - * Ensure order of request and response headers are preserved.

- * Jeremy Cline <jeremy@jcline.org>
 - * Added connection pool keys by scheme

- * Aviv Palivoda <palaviv@gmail.com>
 - * History list to Retry object.
 - * HTTPResponse contains the last Retry object.

- * Nate Prewitt <nate.prewitt@gmail.com>
 - * Ensure timeouts are not booleans and greater than zero.
 - * Fixed infinite loop in ``stream`` when amt=None.
 - * Added length_remaining to determine remaining data to be read.
 - * Added enforce_content_length to raise exception when incorrect content-length received.

- * Seth Michael Larson <sethmichaellarson@protonmail.com>
 - * Created selectors backport that supports PEP 475.

- * Alexandre Dias <alex.dias@smarkets.com>
 - * Don't retry on timeout if method not in whitelist

- * Moinuddin Quadri <moin18@gmail.com>
 - * Lazily load idna package

- * Tom White <s6yg1ez3@mail2tor.com>
 - * Made SOCKS handler differentiate socks5h from socks5 and socks4a from socks4.

- * Tim Burke <tim.burke@gmail.com>
 - * Stop buffering entire deflate-encoded responses.

- * Tuukka Mustonen <tuukka.mustonen@gmail.com>
 - * Add counter for status_forcelist retries.

* Erik Rose <erik@mozilla.com>

* Bugfix to pyopenssl vendoring

* [Your name or handle] <[email or website]>

* [Brief summary of your changes]

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1.169 packaging 19.0

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1.170 beaker 1.11.0

1.170.1 Available under license :

beaker - Puppet's system level acceptance test harness

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1.171 pkix-ssh 11.0

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1.172 libpcap 0.8.3

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1.173 tomcat-catalina 8.5.32

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1.174 gdb 10.1

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@item

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@end enumerate

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

- a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified

executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

a) Accompany the combined library with a copy of the same work

based on the Library, uncombined with any other library facilities. This must be distributed under the terms of the Sections above.

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Ty Coon, President of Vice

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Version 2, June 1991

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[This is the first released version of the library GPL. It is numbered 2 because it goes with version 2 of the ordinary GPL.]

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The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General

Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation

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- b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.
- c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.
- d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square

root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the

source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

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b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

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Appendix: How to Apply These Terms to Your New Libraries

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1.175 cffi 1.15.1

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- c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the

Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

- a) Accompany the combined library with a copy of the same work based on the Library, uncombined with any other library facilities. This must be distributed under the terms of the Sections above.
- b) Give prominent notice with the combined library of the fact that part of it is a work based on the Library, and explaining where to find the accompanying uncombined form of the same work.

8. You may not copy, modify, sublicense, link with, or distribute the Library except as expressly provided under this License. Any attempt otherwise to copy, modify, sublicense, link with, or distribute the Library is void, and will automatically terminate your rights under this License. However, parties who have received copies, or rights, from you under this License will not have their licenses terminated so long as such parties remain in full compliance.

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To apply these terms, attach the following notices to the library. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

```
<one line to give the library's name and a brief idea of what it does.>  
Copyright (C) <year> <name of author>
```

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Also add information on how to contact you by electronic and paper mail.

You should also get your employer (if you work as a programmer) or your school, if any, to sign a "copyright disclaimer" for the library, if necessary. Here is a sample; alter the names:

Yoyodyne, Inc., hereby disclaims all copyright interest in the library `Frob' (a library for tweaking knobs) written by James Random Hacker.

```
<signature of Ty Coon>, 1 April 1990  
Ty Coon, President of Vice
```

That's all there is to it!

```
@c \input texinfo.tex @c -*-texinfo-*  
@c  
@c %**start of header
```

@c All text is ignored before the setfilename.

@setfilename LGPL.info

@center GNU LESSER GENERAL PUBLIC LICENSE

@center Version 2.1, February 1999

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Copyright @copyright{ } 1991, 1999 Free Software Foundation, Inc.,
59 Temple Place, Suite 330, Boston, MA 02111-1307 USA

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[This is the first released version of the Lesser GPL. It also counts
as the successor of the GNU Library Public License, version 2, hence the
version number 2.1.]

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@center Preamble

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make sure the software is free for all its users.

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it too, but we suggest you first think carefully about whether this
license or the ordinary General Public License is the better strategy to
use in any particular case, based on the explanations below.

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you want it; that you can change the software and use pieces of it in
new free programs; and that you are informed that you can do these
things.

To protect your rights, we need to make restrictions that forbid
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rights. These restrictions translate to certain responsibilities for
you if you distribute copies of the library or if you modify it.

For example, if you distribute copies of the library, whether gratis or

for a fee, you must give the recipients all the rights that we gave you. You must make sure that they, too, receive or can get the source code. If you link other code with the library, you must provide complete object files to the recipients, so that they can relink them with the library after making changes to the library and recompiling it. And you must show them these terms so they know their rights.

We protect your rights with a two-step method: (1) we copyright the library, and (2) we offer you this license, which gives you legal permission to copy, distribute and/or modify the library.

To protect each distributor, we want to make it very clear that there is no warranty for the free library. Also, if the library is modified by someone else and passed on, the recipients should know that what they have is not the original version, so that the original author's reputation will not be affected by problems that might be introduced by others.

Finally, software patents pose a constant threat to the existence of any free program. We wish to make sure that a company cannot effectively restrict the users of a free program by obtaining a restrictive license from a patent holder. Therefore, we insist that any patent license obtained for a version of the library must be consistent with the full freedom of use specified in this license.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License. This license, the GNU Lesser General Public License, applies to certain designated libraries, and is quite different from the ordinary General Public License. We use this license for certain libraries in order to permit linking those libraries into non-free programs.

When a program is linked with a library, whether statically or using a shared library, the combination of the two is legally speaking a combined work, a derivative of the original library. The ordinary General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

We call this license the "Lesser" General Public License because it does less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a ``work based on the library" and a ``work that uses the library". The former contains code derived from the library, whereas the latter must be combined with the library in order to run.

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@center GNU LESSER GENERAL PUBLIC LICENSE

@center TERMS AND CONDITIONS FOR COPYING, DISTRIBUTION AND MODIFICATION

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This License Agreement applies to any software library or other program which contains a notice placed by the copyright holder or other authorized party saying it may be distributed under the terms of this Lesser General Public License (also called ``this License"). Each licensee is addressed as ``you".

A ``library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The ``Library", below, refers to any such software library or work which has been distributed under these terms. A ``work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either

verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

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You may copy and distribute verbatim copies of the Library's complete source code as you receive it, in any medium, provided that you conspicuously and appropriately publish on each copy an appropriate copyright notice and disclaimer of warranty; keep intact all the notices that refer to this License and to the absence of any warranty; and distribute a copy of this License along with the Library.

You may charge a fee for the physical act of transferring a copy, and you may at your option offer warranty protection in exchange for a fee.

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You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

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The modified work must itself be a software library.

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You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

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You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

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If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

@end enumerate

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

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You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2,

instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

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You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a ``work that uses the Library''. Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a ``work that uses the Library'' with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a ``work that uses the library''. The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a ``work that uses the Library'' uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

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As an exception to the Sections above, you may also combine or link a ``work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

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Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable ``work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

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Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the

library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

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Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

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If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

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Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

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For an executable, the required form of the ``work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

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You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

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Accompany the combined library with a copy of the same work based on the Library, uncombined with any other library facilities. This must be distributed under the terms of the Sections above.

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Give prominent notice with the combined library of the fact that part of it is a work based on the Library, and explaining where to find the accompanying uncombined form of the same work.

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1.183 mitchellh-mapstructure 1.3.0

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1.184 tomcat-i18n-ja 8.5.32

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1.185 libdwarf 20161124

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*/

```
/* dsc_type: if 0, then dsc_low is a single discriminant value
   and dsc_high is zero..
   If 1, then dsc_low, dsc_high are a discriminant range
```

All the messy complexity here is so we can have both a set of values read as uleb and as sleb.

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1.186 libdrm 2.4.81

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Found in path(s):

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/amdgpu/amdgpu_vamgr.c

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/tests/amdgpu/uvd_messages.h

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/amdgpu/amdgpu.h

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/amdgpu/amdgpu_cs.c

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/amdgpu/amdgpu_device.c

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```
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* Authors:
*   Kevin E. Martin <martin@valinux.com>
*   Gareth Hughes <gareth@valinux.com>
*   Keith Whitwell <keith@tungstengraphics.com>
*/
```

Found in path(s):

```
*/opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/include/drm/amdgpu_drm.h
```

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```
/* xf86drmRandom.c -- "Minimal Standard" PRNG Implementation
* Created: Mon Apr 19 08:28:13 1999 by faith@precisioninsight.com
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*
* Authors: Rickard E. (Rik) Faith <faith@valinux.com>
*
* DESCRIPTION
*
* This file contains a simple, straightforward implementation of the Park
* & Miller "Minimal Standard" PRNG [PM88, PMS93], which is a Lehmer
* multiplicative linear congruential generator (MLCG) with a period of
* $2^{31}-1$.
*
* This implementation is intended to provide a reliable, portable PRNG
* that is suitable for testing a hash table implementation and for
* implementing skip lists.
*
* FUTURE ENHANCEMENTS
*
* If initial seeds are not selected randomly, two instances of the PRNG
* can be correlated. [Knuth81, pp. 32-33] describes a shuffling technique
* that can eliminate this problem.
*
* If PRNGs are used for simulation, the period of the current
* implementation may be too short. [LE88] discusses methods of combining
* MLCGs to produce much longer periods, and suggests some alternative
* values for A and M. [LE90 and Sch92] also provide information on
* long-period PRNGs.
*
* REFERENCES
*
* [Knuth81] Donald E. Knuth. The Art of Computer Programming. Volume 2:
* Seminumerical Algorithms. Reading, Massachusetts: Addison-Wesley, 1981.
*
* [LE88] Pierre L'Ecuyer. "Efficient and Portable Combined Random Number
* Generators". CACM 31(6), June 1988, pp. 742-774.
*

- * [LE90] Pierre L'Ecuyer. "Random Numbers for Simulation". CACM 33(10),
- * October 1990, pp. 85-97.
- *
- * [PM88] Stephen K. Park and Keith W. Miller. "Random Number Generators:
- * Good Ones are Hard to Find". CACM 31(10), October 1988, pp. 1192-1201.
- *
- * [Sch92] Bruce Schneier. "Pseudo-Ransom Sequence Generator for 32-Bit
- * CPUs". Dr. Dobbs Journal 17(2), February 1992, pp. 34, 37-38, 40.
- *
- * [PMS93] Stephen K. Park, Keith W. Miller, and Paul K. Stockmeyer. In
- * "Technical Correspondence: Remarks on Choosing and Implementing Random
- * Number Generators". CACM 36(7), July 1993, pp. 105-110.
- *
- */

Found in path(s):

- * /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/random.c
- * /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/xf86drmRandom.c

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- * /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/libdrm_macros.h
- * /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/tegra/openclose.c

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```
*/opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/tests/etnaviv/write_bmp.h
*/opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/tests/etnaviv/write_bmp.c
```

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Found in path(s):

```
*/opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/man/drmModeGetResources.xml
*/opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/man/drm.xml
*/opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/man/drm-
memory.xml
*/opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/man/drmAvailable.xml
*/opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/man/drmHandleEvent.xml
*/opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/man/drm-kms.xml
```

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/* xf86drmSL.c -- Skip list support

* Created: Mon May 10 09:28:13 1999 by faith@precisioninsight.com

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*
* Authors: Rickard E. (Rik) Faith <faith@valinux.com>

* DESCRIPTION

* This file contains a straightforward skip list implementation.n

* FUTURE ENHANCEMENTS

* REFERENCES

* [Pugh90] William Pugh. Skip Lists: A Probabilistic Alternative to
* Balanced Trees. CACM 33(6), June 1990, pp. 668-676.

*/

Found in path(s):

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/xf86drmSL.c

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/libkms/libkms.h
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/libkms/dumb.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/libkms/intel.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/kmstest/main.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/libkms/internal.h
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/libkms/radeon.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/libkms/vmwgfx.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/libkms/nouveau.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/libkms/api.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/libkms/linux.c

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* Authors:

* Jerome Glisse <j.glisse@gmail.com>

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/radeon/rbo.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/tests/radeon/radeon_ttm.c

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/radeon/rbo.h

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* DRM based mode setting test program

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AC_PREREQ([2.63])

AC_INIT([libdrm],

[2.4.81],

[https://bugs.freedesktop.org/enter_bug.cgi?product=DRI],

[libdrm])

```

AC_CONFIG_HEADERS([config.h])
AC_CONFIG_SRCDIR([Makefile.am])
AC_CONFIG_MACRO_DIR([m4])
AC_CONFIG_AUX_DIR([build-aux])
PKG_PROG_PKG_CONFIG

# Require xorg-macros minimum of 1.12 for XORG_WITH_XSLTPROC
m4_ifndef([XORG_MACROS_VERSION],
  [m4_fatal([must install xorg-macros 1.12 or later before running autoconf/autogen]])
XORG_MACROS_VERSION(1.12)
XORG_WITH_XSLTPROC
XORG_MANPAGE_SECTIONS

AM_INIT_AUTOMAKE([1.10 foreign dist-bzip2])

# Enable quiet compiles on automake 1.11.
m4_ifdef([AM_SILENT_RULES], [AM_SILENT_RULES([yes]])

# Check for programs
AC_PROG_CC
AC_PROG_CC_C99

if test "x$ac_cv_prog_cc_c99" = xno; then
  AC_MSG_ERROR([Building libdrm requires C99 enabled compiler])
fi

AC_USE_SYSTEM_EXTENSIONS
AC_SYS_LARGEFILE
AC_FUNC_ALLOCA

save_CFLAGS="$CFLAGS"
export CFLAGS="$CFLAGS -Werror"
AC_HEADER_MAJOR
CFLAGS="$save_CFLAGS"

AC_CHECK_HEADERS([sys/sysctl.h sys/select.h])

# Initialize libtool
LT_PREREQ([2.2])
LT_INIT([disable-static])

dnl pthread-stubs is mandatory on BSD platforms, due to the nature of the
dnl project. Even then there's a notable issue as described in the project README
case "$host_os" in
linux* | cygwin* | darwin* | solaris* | *-gnu* | gnu*)
  pthread_stubs_possible="no"
  ;;

```

```

*)
  pthread_stubs_possible="yes"
  ;;
esac

if test "x$pthread_stubs_possible" = xyes; then
  PKG_CHECK_MODULES(PTHREADSTUBS, pthread-stubs >= 0.4)
  AC_SUBST(PTHREADSTUBS_CFLAGS)
  AC_SUBST(PTHREADSTUBS_LIBS)
fi

pkgconfigdir=${libdir}/pkgconfig
AC_SUBST(pkgconfigdir)
AC_ARG_ENABLE([udev],
  [AS_HELP_STRING([--enable-udev],
    [Enable support for using udev instead of mknod (default: disabled)]),
  [UDEV=$enableval], [UDEV=no])

AC_ARG_ENABLE([libkms],
  [AS_HELP_STRING([--disable-libkms],
    [Disable KMS mm abstraction library (default: auto, enabled on supported platforms)]),
  [LIBKMS=$enableval], [LIBKMS=auto])

AC_ARG_ENABLE([intel],
  [AS_HELP_STRING([--disable-intel],
    [Enable support for intel's KMS API (default: auto, enabled on x86)]),
  [INTEL=$enableval], [INTEL=auto])

AC_ARG_ENABLE([radeon],
  [AS_HELP_STRING([--disable-radeon],
    [Enable support for radeon's KMS API (default: auto)]),
  [RADEON=$enableval], [RADEON=auto])

AC_ARG_ENABLE([amdgpu],
  [AS_HELP_STRING([--disable-amdgpu],
    [Enable support for amdgpu's KMS API (default: auto)]),
  [AMDGPU=$enableval], [AMDGPU=auto])

AC_ARG_ENABLE([nouveau],
  [AS_HELP_STRING([--disable-nouveau],
    [Enable support for nouveau's KMS API (default: auto)]),
  [NOUVEAU=$enableval], [NOUVEAU=auto])

AC_ARG_ENABLE([vmwgfx],
  [AS_HELP_STRING([--disable-vmwgfx],
    [Enable support for vmwgfx's KMS API (default: yes)]),
  [VMWGFX=$enableval], [VMWGFX=yes])

```

```
AC_ARG_ENABLE(omap-experimental-api,
  AS_HELP_STRING([--enable-omap-experimental-api],
    [Enable support for OMAP's experimental API (default: disabled)]),
  [OMAP=$enableval], [OMAP=no])
```

```
AC_ARG_ENABLE(exynos-experimental-api,
  AS_HELP_STRING([--enable-exynos-experimental-api],
    [Enable support for EXYNOS's experimental API (default: disabled)]),
  [EXYNOS=$enableval], [EXYNOS=no])
```

```
AC_ARG_ENABLE(freedreno,
  AS_HELP_STRING([--disable-freedreno],
    [Enable support for freedreno's KMS API (default: auto, enabled on arm)]),
  [FREEDRENO=$enableval], [FREEDRENO=auto])
```

```
AC_ARG_ENABLE(freedreno-kgsl,
  AS_HELP_STRING([--enable-freedreno-kgsl],
    [Enable support for freedreno's to use downstream android kernel API (default: disabled)]),
  [FREEDRENO_KGSL=$enableval], [FREEDRENO_KGSL=no])
```

```
AC_ARG_ENABLE(tegra-experimental-api,
  AS_HELP_STRING([--enable-tegra-experimental-api],
    [Enable support for Tegra's experimental API (default: disabled)]),
  [TEGRA=$enableval], [TEGRA=no])
```

```
AC_ARG_ENABLE(vc4,
  AS_HELP_STRING([--disable-vc4],
    [Enable support for vc4's API (default: auto, enabled on arm)]),
  [VC4=$enableval], [VC4=auto])
```

```
AC_ARG_ENABLE(etnaviv-experimental-api,
  AS_HELP_STRING([--enable-etnaviv-experimental-api],
    [Enable support for etnaviv's experimental API (default: disabled)]),
  [ETNAVIV=$enableval], [ETNAVIV=no])
```

```
AC_ARG_ENABLE(install-test-programs,
  AS_HELP_STRING([--enable-install-test-programs],
    [Install test programs (default: no)]),
  [INSTALL_TESTS=$enableval], [INSTALL_TESTS=no])
```

```
dnl =====
```

```
dnl check compiler flags
```

```
AC_DEFUN([LIBDRM_CC_TRY_FLAG], [
  AC_MSG_CHECKING([whether $CC supports $1])
```

```
libdrm_save_CFLAGS="$CFLAGS"
CFLAGS="$CFLAGS $1"
```

```
AC_COMPILE_IFELSE([AC_LANG_SOURCE([ ])], [libdrm_cc_flag=yes], [libdrm_cc_flag=no])
CFLAGS="$libdrm_save_CFLAGS"
```

```
if test "x$libdrm_cc_flag" = "xyes"; then
  ifelse([$2], , :, [$2])
else
  ifelse([$3], , :, [$3])
fi
AC_MSG_RESULT([$libdrm_cc_flag])
])
```

dnl We use clock_gettime to check for timeouts in drmWaitVBlank

```
AC_CHECK_FUNCS([clock_gettime], [CLOCK_LIB=],
  [AC_CHECK_LIB([rt], [clock_gettime], [CLOCK_LIB=-lrt],
    [AC_MSG_ERROR([Couldn't find clock_gettime])])])
AC_SUBST([CLOCK_LIB])
```

```
AC_CHECK_FUNCS([open_memstream], [HAVE_OPEN_MEMSTREAM=yes])
```

dnl Use lots of warning flags with with gcc and compatible compilers

dnl Note: if you change the following variable, the cache is automatically
dnl skipped and all flags rechecked. So there's no need to do anything
dnl else. If for any reason you need to force a recheck, just change
dnl MAYBE_WARN in an ignorable way (like adding whitespace)

```
MAYBE_WARN="-Wall -Wextra \  
-Wsign-compare -Werror-implicit-function-declaration \  
-Wpointer-arith -Wwrite-strings -Wstrict-prototypes \  
-Wmissing-prototypes -Wmissing-declarations -Wnested-externs \  
-Wpacked -Wswitch-enum -Wmissing-format-attribute \  
-Wstrict-aliasing=2 -Winit-self \  
-Wdeclaration-after-statement -Wold-style-definition \  
-Wno-unused-parameter \  
-Wno-attributes -Wno-long-long -Winline -Wshadow \  
-Wno-missing-field-initializers"
```

```
# invalidate cached value if MAYBE_WARN has changed  
if test "x$libdrm_cv_warn_maybe" != "x$MAYBE_WARN"; then  
  unset libdrm_cv_warn_cflags  
fi  
AC_CACHE_CHECK([for supported warning flags], libdrm_cv_warn_cflags, [  
  echo  
  WARN_CFLAGS=""
```

```
# Some warning options are not supported by all versions of  
# gcc, so test all desired options against the current
```

```

# compiler.
#
# Note that there are some order dependencies
# here. Specifically, an option that disables a warning will
# have no net effect if a later option then enables that
# warnings, (perhaps implicitly). So we put some grouped
# options (-Wall and -Wextra) up front and the -Wno options
# last.

for W in $MAYBE_WARN; do
LIBDRM_CC_TRY_FLAG([$W], [WARN_CFLAGS="$WARN_CFLAGS $W"])
done

libdrm_cv_warn_cflags=$WARN_CFLAGS
libdrm_cv_warn_maybe=$MAYBE_WARN

AC_MSG_CHECKING([which warning flags were supported])
WARN_CFLAGS="$libdrm_cv_warn_cflags"

# Check for atomic intrinsics
AC_CACHE_CHECK([for native atomic primitives], drm_cv_atomic_primitives, [
drm_cv_atomic_primitives="none"

AC_LINK_IFELSE([AC_LANG_PROGRAM([[
int atomic_add(int *i) { return __sync_add_and_fetch (i, 1); }
int atomic_cmpxchg(int *i, int j, int k) { return __sync_val_compare_and_swap (i, j, k); }
]], [[]]), [drm_cv_atomic_primitives="Intel"], [])

if test "x$drm_cv_atomic_primitives" = "xnone"; then
AC_CHECK_HEADER([atomic_ops.h], drm_cv_atomic_primitives="libatomic-ops")
fi

# atomic functions defined in <atomic.h> & libc on Solaris
if test "x$drm_cv_atomic_primitives" = "xnone"; then
AC_CHECK_FUNC([atomic_cas_uint], drm_cv_atomic_primitives="Solaris")
fi
])

if test "x$drm_cv_atomic_primitives" = xIntel; then
AC_DEFINE(HAVE_LIBDRM_ATOMIC_PRIMITIVES, 1,
[Enable if your compiler supports the Intel __sync_* atomic primitives])
fi
if test "x$drm_cv_atomic_primitives" = "xlibatomic-ops"; then
AC_DEFINE(HAVE_LIB_ATOMIC_OPS, 1, [Enable if you have libatomic-ops-dev installed])
fi

dnl Print out the appropriate message considering the value set be the
dnl respective in $1.

```

```

dnl $1 - value to be evaluated. Eg. $INTEL, $NOUVEAU, ...
dnl $2 - libdrm shortname. Eg. intel, freedreno, ...
dnl $3 - GPU name/brand. Eg. Intel, NVIDIA Tegra, ...
dnl $4 - Configure switch. Eg. intel, omap-experimental-api, ...
AC_DEFUN([LIBDRM_ATOMICS_NOT_FOUND_MSG], [
  case "x$1" in
    xyes) AC_MSG_ERROR([libdrm_$2 depends upon atomic operations, which were not found for your compiler/cpu.
Try compiling with -march=native, or install the libatomics-op-dev package, or, failing both of those, disable
support for $3 GPUs by passing --disable-$4 to ./configure]) ;;
    xauto) AC_MSG_WARN([Disabling $2. It depends on atomic operations, which were not found for your
compiler/cpu. Try compiling with -march=native, or install the libatomics-op-dev package.]) ;;
    *) ;;
  esac
])

```

```

if test "x$drm_cv_atomic_primitives" = "xnone"; then
  LIBDRM_ATOMICS_NOT_FOUND_MSG($INTEL, intel, Intel, intel)
  INTEL=no

```

```

  LIBDRM_ATOMICS_NOT_FOUND_MSG($RADEON, radeon, Radeon, radeon)
  RADEON=no

```

```

  LIBDRM_ATOMICS_NOT_FOUND_MSG($AMDGPU, amdgpu, AMD, amdgpu)
  AMDGPU=no

```

```

  LIBDRM_ATOMICS_NOT_FOUND_MSG($NOUVEAU, nouveau, NVIDIA, nouveau)
  NOUVEAU=no

```

```

  LIBDRM_ATOMICS_NOT_FOUND_MSG($OMAP, omap, OMAP, omap-experimental-api)
  OMAP=no

```

```

  LIBDRM_ATOMICS_NOT_FOUND_MSG($FREEDRENO, freedreno, Qualcomm Adreno, freedreno)
  FREEDRENO=no

```

```

  LIBDRM_ATOMICS_NOT_FOUND_MSG($TEGRA, tegra, NVIDIA Tegra, tegra-experimental-api)
  TEGRA=no

```

```

  LIBDRM_ATOMICS_NOT_FOUND_MSG($ETNAVIV, etnaviv, Vivante, etnaviv-experimental-api)
  ETNAVIV=no

```

```

else
  if test "x$INTEL" = xauto; then
    case $host_cpu in
      i?86|x86_64) INTEL=yes ;;
      *) INTEL=no ;;
    esac
  fi
  if test "x$RADEON" = xauto; then
    RADEON=yes

```

```

fi
if test "x$AMDGPU" = xauto; then
    AMDGPU=yes
fi
if test "x$NOUVEAU" = xauto; then
    NOUVEAU=yes
fi
if test "x$FREEDRENO" = xauto; then
    case $host_cpu in
        arm*|aarch64) FREEDRENO=yes ;;
        *) FREEDRENO=no ;;
    esac
fi
if test "x$VC4" = xauto; then
    case $host_cpu in
        arm*|aarch64) VC4=yes ;;
        *) VC4=no ;;
    esac
fi
fi

if test "x$INTEL" != "xno"; then
    PKG_CHECK_MODULES(PCIAACCESS, [pciaccess >= 0.10])
fi
AC_SUBST(PCIAACCESS_CFLAGS)
AC_SUBST(PCIAACCESS_LIBS)

if test "x$UDEV" = xyes; then
    AC_DEFINE(UDEV, 1, [Have UDEV support])
fi

AC_CANONICAL_HOST
if test "x$LIBKMS" = xauto ; then
    case $host_os in
        linux*) LIBKMS="yes" ;;
        freebsd* | kfreebsd*-gnu)
            LIBKMS="yes" ;;
        dragonfly*) LIBKMS="yes" ;;
        *) LIBKMS="no" ;;
    esac
fi

AM_CONDITIONAL(HAVE_LIBKMS, [test "x$LIBKMS" = xyes])

AM_CONDITIONAL(HAVE_INTEL, [test "x$INTEL" = xyes])
if test "x$INTEL" = xyes; then
    AC_DEFINE(HAVE_INTEL, 1, [Have intel support])
fi

```

```
AM_CONDITIONAL(HAVE_VMWGFX, [test "x$VMWGFX" = xyes])
if test "x$VMWGFX" = xyes; then
  AC_DEFINE(HAVE_VMWGFX, 1, [Have vmwgfx kernel headers])
fi
```

```
AM_CONDITIONAL(HAVE_NOUVEAU, [test "x$NOUVEAU" = xyes])
if test "x$NOUVEAU" = xyes; then
  AC_DEFINE(HAVE_NOUVEAU, 1, [Have nouveau (nvidia) support])
fi
```

```
AM_CONDITIONAL(HAVE_OMAP, [test "x$OMAP" = xyes])
if test "x$OMAP" = xyes; then
  AC_DEFINE(HAVE_OMAP, 1, [Have OMAP support])
fi
```

```
AM_CONDITIONAL(HAVE_EXYNOS, [test "x$EXYNOS" = xyes])
if test "x$EXYNOS" = xyes; then
  AC_DEFINE(HAVE_EXYNOS, 1, [Have EXYNOS support])
fi
```

```
AM_CONDITIONAL(HAVE_FREEDRENO, [test "x$FREEDRENO" = xyes])
if test "x$FREEDRENO" = xyes; then
  AC_DEFINE(HAVE_FREEDRENO, 1, [Have freedreno support])
fi
```

```
if test "x$FREEDRENO_KGSL" = xyes; then
  if test "x$FREEDRENO" != xyes; then
    AC_MSG_ERROR([Cannot enable freedreno KGSL interface if freedreno is disabled])
  fi
fi
```

```
AM_CONDITIONAL(HAVE_FREEDRENO_KGSL, [test "x$FREEDRENO_KGSL" = xyes])
if test "x$FREEDRENO_KGSL" = xyes; then
  AC_DEFINE(HAVE_FREEDRENO_KGSL, 1, [Have freedreno support for KGSL kernel interface])
fi
```

```
AM_CONDITIONAL(HAVE_RADEON, [test "x$RADEON" = xyes])
if test "x$RADEON" = xyes; then
  AC_DEFINE(HAVE_RADEON, 1, [Have radeon support])
fi
```

```
if test "x$AMDGPU" != xno; then
  # Detect cunit library
  PKG_CHECK_MODULES([CUNIT], [cunit >= 2.1], [have_cunit=yes], [have_cunit=no])
  # If pkg-config does not find cunit, check it using AC_CHECK_LIB. We
  # do this because Debian (Ubuntu) lacks pkg-config file for cunit.
  # fixed in 2.1-2.dfsg-3: http://anonscm.debian.org/cgiit/collab-maint/cunit.git/commit/?h=debian
  if test "x${have_cunit}" = "xno"; then
```

```

AC_CHECK_LIB([cunit], [CU_initialize_registry], [have_cunit=yes], [have_cunit=no])
if test "x${have_cunit}" = "xyes"; then
    CUNIT_LIBS="-lcunit"
    CUNIT_CFLAGS=""
    AC_SUBST([CUNIT_LIBS])
    AC_SUBST([CUNIT_CFLAGS])
fi
fi
else
    have_cunit=no
fi
AM_CONDITIONAL(HAVE_CUNIT, [test "x$have_cunit" != "xno"])

AM_CONDITIONAL(HAVE_AMDGPU, [test "x$AMDGPU" = xyes])
if test "x$AMDGPU" = xyes; then
    AC_DEFINE(HAVE_AMDGPU, 1, [Have amdgpu support])

    AC_DEFINE(HAVE_CUNIT, [test "x$have_cunit" != "xno"], [Enable CUNIT Have amdgpu support])

    if test "x$have_cunit" = "xno"; then
        AC_MSG_WARN([Could not find cunit library. Disabling amdgpu tests])
    fi
fi

AM_CONDITIONAL(HAVE_TEGRA, [test "x$TEGRA" = xyes])
if test "x$TEGRA" = xyes; then
    AC_DEFINE(HAVE_TEGRA, 1, [Have Tegra support])
fi

AM_CONDITIONAL(HAVE_VC4, [test "x$VC4" = xyes])
if test "x$VC4" = xyes; then
    AC_DEFINE(HAVE_VC4, 1, [Have VC4 support])
fi

AM_CONDITIONAL(HAVE_ETNAVIV, [test "x$ETNAVIV" = xyes])
if test "x$ETNAVIV" = xyes; then
    AC_DEFINE(HAVE_ETNAVIV, 1, [Have etnaviv support])
fi

AM_CONDITIONAL(HAVE_INSTALL_TESTS, [test "x$INSTALL_TESTS" = xyes])
if test "x$INSTALL_TESTS" = xyes; then
    AC_DEFINE(HAVE_INSTALL_TESTS, 1, [Install test programs])
fi

AC_ARG_ENABLE([cairo-tests],
    [AS_HELP_STRING([--enable-cairo-tests],
        [Enable support for Cairo rendering in tests (default: auto)]),
    [CAIRO=$enableval], [CAIRO=auto])

```

```

if test "x$CAIRO" != xno; then
    PKG_CHECK_MODULES(CAIRO, cairo, [HAVE_CAIRO=yes], [HAVE_CAIRO=no])
fi
AC_MSG_CHECKING([whether to enable Cairo tests])
if test "x$CAIRO" = xauto; then
    CAIRO="$HAVE_CAIRO"
fi
if test "x$CAIRO" = xyes; then
    if ! test "x$HAVE_CAIRO" = xyes; then
        AC_MSG_ERROR([Cairo support required but not present])
    fi
    AC_DEFINE(HAVE_CAIRO, 1, [Have Cairo support])
fi
AC_MSG_RESULT([$CAIRO])
AM_CONDITIONAL(HAVE_CAIRO, [test "x$CAIRO" = xyes])

# xsltproc for docbook manpages
AC_ARG_ENABLE([manpages],
    AS_HELP_STRING([--enable-manpages], [enable manpages @<:@default=auto@:>@]),
    [MANS=$enableval], [MANS=auto])
AM_CONDITIONAL([BUILD_MANPAGES], [test "x$XSLTPROC" != "x" -a "x$MANS" != "xno"])

# check for offline man-pages stylesheet
AC_MSG_CHECKING([for docbook manpages stylesheet])
MANPAGES_STYLESHEET="http://docbook.sourceforge.net/release/xsl/current/manpages/docbook.xsl"
AC_PATH_PROGS_FEATURE_CHECK([XSLTPROC_TMP], [xsltproc],
    AS_IF(["$ac_path_XSLTPROC_TMP" --nonet "$MANPAGES_STYLESHEET" > /dev/null
2>&1`],
        [HAVE_MANPAGES_STYLESHEET=yes]))
if test "x$HAVE_MANPAGES_STYLESHEET" = "xyes"; then
    AC_SUBST(MANPAGES_STYLESHEET)
    AC_MSG_RESULT([yes])
else
    AC_MSG_RESULT([no])
fi
AM_CONDITIONAL([HAVE_MANPAGES_STYLESHEET], [test "x$HAVE_MANPAGES_STYLESHEET" =
"xyes"])

AC_ARG_ENABLE(valgrind,
    [AS_HELP_STRING([--enable-valgrind],
        [Build libdrm with valgrind support (default: auto)]),
    [VALGRIND=$enableval], [VALGRIND=auto])
if test "x$VALGRIND" != xno; then
    PKG_CHECK_MODULES(VLGRIND, [valgrind], [have_valgrind=yes], [have_valgrind=no])
fi
AC_MSG_CHECKING([whether to enable Valgrind support])
if test "x$VALGRIND" = xauto; then
    VALGRIND="$have_valgrind"

```

fi

if test "x\$VALGRIND" = "xyes"; then

if ! test "x\$have_valgrind" = xyes; then

AC_MSG_ERROR([Valgrind support required but not present])

fi

AC_DEFINE([HAVE_VALGRIND], 1, [Use valgrind intrinsics to suppress false warnings])

fi

AC_MSG_RESULT([\$VALGRIND])

AC_ARG_WITH([kernel-source],

[AS_HELP_STRING([--with-kernel-source],

[specify path to linux kernel source]),

[kernel_source="\$with_kernel_source"])

AC_SUBST(kernel_source)

AC_MSG_CHECKING([whether \$CC supports __attribute__(("hidden"))])

AC_LINK_IFELSE([AC_LANG_PROGRAM([

int foo_hidden(void) __attribute__((visibility("hidden")));

]), HAVE_ATTRIBUTE_VISIBILITY="yes"; AC_MSG_RESULT([yes]), AC_MSG_RESULT([no]));

if test "x\$HAVE_ATTRIBUTE_VISIBILITY" = xyes; then

AC_DEFINE(HAVE_VISIBILITY, 1, [Compiler supports __attribute__(("hidden"))])

fi

AC_SUBST(WARN_CFLAGS)

AC_CONFIG_FILES([

Makefile

libkms/Makefile

libkms/libkms.pc

intel/Makefile

intel/libdrm_intel.pc

radeon/Makefile

radeon/libdrm_radeon.pc

amdgpu/Makefile

amdgpu/libdrm_amdgpu.pc

nouveau/Makefile

nouveau/libdrm_nouveau.pc

omap/Makefile

omap/libdrm_omap.pc

exynos/Makefile

exynos/libdrm_exynos.pc

freedreno/Makefile

freedreno/libdrm_freedreno.pc

tegra/Makefile

tegra/libdrm_tegra.pc

vc4/Makefile

vc4/libdrm_vc4.pc
etnaviv/Makefile
etnaviv/libdrm_etnaviv.pc
tests/Makefile
tests/modeprint/Makefile
tests/modetest/Makefile
tests/kms/Makefile
tests/kmstest/Makefile
tests/proptest/Makefile
tests/radeon/Makefile
tests/amdgpu/Makefile
tests/vbltest/Makefile
tests/exynos/Makefile
tests/tegra/Makefile
tests/nouveau/Makefile
tests/etnaviv/Makefile
tests/util/Makefile
man/Makefile
libdrm.pc])
AC_OUTPUT

```
echo ""  
echo "$PACKAGE_STRING will be compiled with:"  
echo ""  
echo " libkms    $LIBKMS"  
echo " Intel API  $INTEL"  
echo " vmwgfx API  $VMWGFY"  
echo " Radeon API  $RADEON"  
echo " AMDGPU API  $AMDGPU"  
echo " Nouveau API $NOUVEAU"  
echo " OMAP API    $OMAP"  
echo " EXYNOS API  $EXYNOS"  
echo " Freedreno API $FREEDRENO (kgs!l: $FREEDRENO_KGSL)"  
echo " Tegra API    $TEGRA"  
echo " VC4 API     $VC4"  
echo " Etnaviv API  $ETNAVIV"  
echo ""
```

Found in path(s):

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/configure.ac
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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/tests/amdgpu/amdgpu_test.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/util_math.h
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/tests/amdgpu/basic_tests.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/tests/amdgpu/amdgpu_test.h
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/tests/amdgpu/cs_tests.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/tests/amdgpu/bo_tests.c

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/intel/mm.c

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/include/drm/drm.h

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/amdgpu/util_hash_table.h

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* Authors:

* Christian Gmeiner <christian.gmeiner@gmail.com>

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/etnaviv/etnaviv_device.c

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/etnaviv/etnaviv_bo.c

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/xf86drmMode.h

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/radeon/radeon_bo_gem.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/radeon/radeon_bo_gem.h

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- cmdstream.xml (12621 bytes, from 2016-09-06 14:44:16)
- copyright.xml (1597 bytes, from 2016-09-06 14:44:16)
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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/exynos/exynos_drm.c

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/freedreno/msm/msm_priv.h

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/freedreno/msm/msm_device.c

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/freedreno/msm/msm_ringbuffer.c

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/freedreno/kgsl/kgsl_device.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/freedreno/kgsl/kgsl_bo.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/freedreno/kgsl/kgsl_priv.h
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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/freedreno/kgsl/kgsl_pipe.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/freedreno/kgsl/kgsl_ringbuffer.c

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*

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*

* Rewritten by:

* Gareth Hughes <gareth@valinux.com>

*/

Found in path(s):

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/include/drm/mga_drm.h

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/* drmsl.c -- Skip list test

* Created: Mon May 10 09:28:13 1999 by faith@precisioninsight.com

*

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*

* Authors: Rickard E. (Rik) Faith <faith@valinux.com>

*

* DESCRIPTION

*

* This file contains a straightforward skip list implementation.n

*

* FUTURE ENHANCEMENTS

*

* REFERENCES

*

* [Pugh90] William Pugh. Skip Lists: A Probabilistic Alternative to
* Balanced Trees. CACM 33(6), June 1990, pp. 668-676.

*

*/

Found in path(s):

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/drmsl.c

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*

* Authors:

* Chris Wilson <chris@chris-wilson.co.uk>

*

*/

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/xf86atomic.h

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/util_double_list.h

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/* exynos_drm.h

*

* Copyright (c) 2011 Samsung Electronics Co., Ltd.

* Authors:

* Inki Dae <inki.dae@samsung.com>

* Joonyoung Shim <jy0922.shim@samsung.com>

* Seung-Woo Kim <sw0312.kim@samsung.com>

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/exynos/exynos_drm.h

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/vc4/vc4_packet.h
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/vc4/vc4_qpu_defines.h

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* Authors: Rickard E. (Rik) Faith <faith@valinux.com>

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/xf86drmRandom.h

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/xf86drmHash.h

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/* exynos.c

*

* Copyright 2009 Samsung Electronics Co., Ltd.

* Authors:

* SooChan Lim <sc1.lim@samsung.com>

* Sangjin LEE <lsj119@samsung.com>

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/intel/test_decode.c

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* DRM based mode setting test program

* Copyright (C) 2013 Red Hat

* Author: Rob Clark <robdclark@gmail.com>

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/* xf86drmHash.c -- Small hash table support for integer -> integer mapping

* Created: Sun Apr 18 09:35:45 1999 by faith@precisioninsight.com

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*

* Authors: Rickard E. (Rik) Faith <faith@valinux.com>

*

* DESCRIPTION

*

* This file contains a straightforward implementation of a fixed-sized

* hash table using self-organizing linked lists [Knuth73, pp. 398-399] for
* collision resolution. There are two potentially interesting things
* about this implementation:

*

* 1) The table is power-of-two sized. Prime sized tables are more
* traditional, but do not have a significant advantage over power-of-two
* sized table, especially when double hashing is not used for collision
* resolution.

*

* 2) The hash computation uses a table of random integers [Hanson97,
* pp. 39-41].

*

* FUTURE ENHANCEMENTS

*

* With a table size of 512, the current implementation is sufficient for a
* few hundred keys. Since this is well above the expected size of the
* tables for which this implementation was designed, the implementation of
* dynamic hash tables was postponed until the need arises. A common (and
* naive) approach to dynamic hash table implementation simply creates a
* new hash table when necessary, rehashes all the data into the new table,
* and destroys the old table. The approach in [Larson88] is superior in
* two ways: 1) only a portion of the table is expanded when needed,
* distributing the expansion cost over several insertions, and 2) portions
* of the table can be locked, enabling a scalable thread-safe
* implementation.

*

* REFERENCES

*

* [Hanson97] David R. Hanson. C Interfaces and Implementations:
* Techniques for Creating Reusable Software. Reading, Massachusetts:
* Addison-Wesley, 1997.

*

* [Knuth73] Donald E. Knuth. The Art of Computer Programming. Volume 3:
* Sorting and Searching. Reading, Massachusetts: Addison-Wesley, 1973.

*

* [Larson88] Per-Ake Larson. "Dynamic Hash Tables". CACM 31(4), April
* 1988, pp. 446-457.

*

*/

Found in path(s):

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/hash.c

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/xf86drmHash.c

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/freedreno/freedreno_ringbuffer.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/freedreno/freedreno_bo.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/freedreno/freedreno_pipe.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/freedreno/freedreno_drmif.h
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/freedreno/freedreno_priv.h
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/freedreno/freedreno_device.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/freedreno/freedreno_ringbuffer.h

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/util/pattern.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/util/kms.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/util/pattern.h
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/util/kms.h
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/util/common.h
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/util/format.c

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* Authors:

* Eric Anholt <eric@anholt.net>

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2.4.81/intel/intel_bufmgr_priv.h

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/nouveau/bufctx.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/nouveau/nouveau.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/nouveau/pushbuf.c

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* Authors:

* Gareth Hughes <gareth@valinux.com>

* Kevin E. Martin <martin@valinux.com>

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/omap/omap_drmif.h

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/omap/omap_drm.c

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/exynos/fimg2d_reg.h

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/exynos/exynos_fimg2d_test.c

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/exynos/exynos_fimg2d.c

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/tests/modetest/buffers.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/intel/intel_bufmgr_fake.c

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* Authors:

* Christian Gmeiner <christian.gmeiner@gmail.com>

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/etnaviv/etnaviv_bo_cache_test.c

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/etnaviv/etnaviv_bo_cache.c

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/* radeon_drm.h -- Public header for the radeon driver -*- linux-c -*-

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*   Kevin E. Martin <martin@valinux.com>
*   Gareth Hughes <gareth@valinux.com>
*   Keith Whitwell <keith@tungstengraphics.com>
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Found in path(s):

```
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/include/drm/radeon_drm.h
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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/etnaviv/etnaviv_drmif.h
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/etnaviv/etnaviv_priv.h
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/etnaviv/etnaviv_2d_test.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/etnaviv/etnaviv_pipe.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/etnaviv/etnaviv_cmd_stream.c

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/freedreno/msm/msm_drm.h

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2.4.81/include/drm/virtgpu_drm.h

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/amdgpu/frame.h
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/tests/amdgpu/vce_ib.h
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/kms/kms-steal-
crtc.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/kms/kms-
universal-planes.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/kms/libkms-test-
plane.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/kms/libkms-test-
screen.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/kms/libkms-test-
framebuffer.c

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/kms/libkms-test-device.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/kms/libkms-test-crtc.c
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/kms/libkms-test.h

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tegra/tegra.h
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tegra/tegra.c

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/radeon/bof.c
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* Authors:

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* Frank C. Earl <fearl@airmail.net>
* Leif Delgass <ldelgass@retinalburn.net>

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/exynos/exynos_fimg2d_event.c

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* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/etnaviv/state.xml.h

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/etnaviv/state_2d.xml.h

No license file was found, but licenses were detected in source scan.

/* savage_drm.h -- Public header for the savage driver

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Found in path(s):

* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/include/drm/savage_drm.h

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Found in path(s):

*/opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/include/drm/drm_mode.h

1.187 zlib 1.2.8

1.187.1 Available under license :

/* zlib.h -- interface of the 'zlib' general purpose compression library
version 1.2.11, January 15th, 2017

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1.188 alabaster 0.7.6

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1.189 cairo 1.16.0

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1.190 stunnel 5.57

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1.191 cryptography 2.9.2

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1.192 jwalterweatherman 1.1.0

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1.193 zlib 1.2.11

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1.194 json-c 0.13

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1.195 tomcat-el-api 8.5.32

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* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-  
jar/com/sun/xml/bind/Messages_de.properties  
* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-  
jar/com/sun/xml/bind/Messages_zh_TW.properties  
* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-  
jar/com/sun/xml/bind/Messages_zh_CN.properties  
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jar/com/sun/xml/bind/Messages_it.properties  
* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-  
jar/com/sun/xml/bind/Messages_pt_BR.properties  
* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-  
jar/com/sun/xml/bind/Messages_ko.properties  
* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-  
jar/com/sun/xml/bind/Messages_ja.properties  
* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-  
jar/com/sun/xml/bind/Messages_fr.properties  
* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-  
jar/com/sun/xml/bind/Messages_es.properties
```

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* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-  
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* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/marshaller/Messages_zh_CN.properties
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* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/v2/Messages_es.properties
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* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/marshaller/Messages.properties
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* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/v2/Messages_fr.properties
* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/marshaller/Messages_es.properties
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jar/com/sun/xml/bind/marshaller/NoEscapeHandler.java
* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-
jar/com/sun/xml/bind/v2/model/impl/ModelBuilderI.java
* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-
jar/com/sun/xml/bind/v2/model/nav/package-info.java
* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-
jar/com/sun/xml/bind/v2/model/util/ArrayInfoUtil.java

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- * /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/v2/model/nav/GenericArrayTypeImpl.java
- * /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/annotation/XmlIsSet.java
- * /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/v2/model/core/ElementPropertyInfo.java
- * /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/v2/ClassFactory.java
- * /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/v2/model/core/ValuePropertyInfo.java
- * /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/v2/model/nav/ReflectionNavigator.java
- * /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/v2/model/nav/TypeVisitor.java

* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/marshaller/Messages.java
* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/v2/model/core/TypeInfoSet.java
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* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/unmarshaller/InfosetScanner.java
* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/v2/model/core/LeafInfo.java
* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/v2/model/core/ReferencePropertyInfo.java
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* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/v2/Messages.java
* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/api/EventListener.java
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* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/v2/model/core/Element.java
* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/v2/model/core/MapPropertyInfo.java
* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/v2/model/core/BuiltinLeafInfo.java
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* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/v2/model/annotation/Locatable.java
* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/v2/model/core/WildcardMode.java
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* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-
jar/com/sun/xml/bind/v2/model/core/WildcardTypeInfo.java
* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-
jar/com/sun/xml/bind/v2/model/core/package-info.java
* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-
jar/com/sun/xml/bind/annotation/XmlLocation.java
* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-
jar/com/sun/xml/bind/v2/model/core/ID.java

No license file was found, but licenses were detected in source scan.

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- */

Found in path(s):

* /opt/cola/permits/1334904536_1653676449.1070127/0/jaxb-core-2-3-0-sources-10-jar/com/sun/xml/bind/v2/util/XmlFactory.java

1.199 ecj 3.12.3

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1.200 libdaemon 0.14

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1.206 llvm 11.0.1

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; RUN: llc < %s -mtriple=aarch64-- | FileCheck %s

; A shuffle mask with all undef elements is always legal.

```
define <4 x i32> @PR41535(<2 x i32> %p1, <2 x i32> %p2) {
; CHECK-LABEL: PR41535:
; CHECK:      // %bb.0:
; CHECK-NEXT:  ext v0.8b, v0.8b, v1.8b, #4
; CHECK-NEXT:  mov v0.d[1], v0.d[0]
; CHECK-NEXT:  ret
%cat1 = shufflevector <2 x i32> %p1, <2 x i32> undef, <4 x i32> <i32 undef, i32 1, i32 undef, i32 undef>
%cat2 = shufflevector <2 x i32> %p2, <2 x i32> undef, <4 x i32> <i32 0, i32 undef, i32 undef, i32 undef>
%r = shufflevector <4 x i32> %cat1, <4 x i32> %cat2, <4 x i32> <i32 undef, i32 undef, i32 1, i32 4>
ret <4 x i32> %r
}
```

; RUN: llc < %s -mtriple=s390x-linux-gnu -mcpu=zEC12 -verify-machineinstrs | FileCheck %s

;

; Test that early if conversion produces LOCR with operands of the right

; register classes.

```
define void @autogen_SD4739(i8*) {
; CHECK-NOT: Expected a GR32Bit register, but got a GRX32Bit register
BB:
```

```
%L34 = load i8, i8* %0
```

```
%Cmp56 = icmp sgt i8 undef, %L34
```

```
br label %CF246
```

```
CF246:                                ; preds = %CF246, %BB
```

```
%S1163 = select i1 %Cmp56, i8 %L34, i8 undef
```

```
br i1 undef, label %CF246, label %CF248
```

```
CF248:                                ; preds = %CF248, %CF246
```

```
store i8 %S1163, i8* %0
```

```
br label %CF248
```

```
}
```

=====
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```
; RUN: llc -mtriple=aarch64-apple-ios %s -o - | FileCheck %s
```

```
define <16 x double> @test_sitofp_fixed(<16 x i32> %in) {  
; CHECK-LABEL: test_sitofp_fixed:
```

```
; First, extend each i32 to i64
```

```
; CHECK-DAG: sshll2.2d [[BLOCK0_HI:v[0-9]+]], v0, #0
```

```
; CHECK-DAG: sshll2.2d [[BLOCK1_HI:v[0-9]+]], v1, #0
```

```
; CHECK-DAG: sshll2.2d [[BLOCK2_HI:v[0-9]+]], v2, #0
```

```
; CHECK-DAG: sshll2.2d [[BLOCK3_HI:v[0-9]+]], v3, #0
```

```
; CHECK-DAG: sshll.2d [[BLOCK0_LO:v[0-9]+]], v0, #0
```

```
; CHECK-DAG: sshll.2d [[BLOCK1_LO:v[0-9]+]], v1, #0
```

```
; CHECK-DAG: sshll.2d [[BLOCK2_LO:v[0-9]+]], v2, #0
```

```
; CHECK-DAG: sshll.2d [[BLOCK3_LO:v[0-9]+]], v3, #0
```

```
; Next, convert each to double.
```

```
; CHECK-DAG: scvtf.2d v0, [[BLOCK0_LO]]
```

```
; CHECK-DAG: scvtf.2d v1, [[BLOCK0_HI]]
```

```
; CHECK-DAG: scvtf.2d v2, [[BLOCK1_LO]]
```

```
; CHECK-DAG: scvtf.2d v3, [[BLOCK1_HI]]
```

```
; CHECK-DAG: scvtf.2d v4, [[BLOCK2_LO]]
```

```
; CHECK-DAG: scvtf.2d v5, [[BLOCK2_HI]]
```

```
; CHECK-DAG: scvtf.2d v6, [[BLOCK3_LO]]
```

```
; CHECK-DAG: scvtf.2d v7, [[BLOCK3_HI]]
```

```
; CHECK: ret
```

```
%flt = sitofp <16 x i32> %in to <16 x double>
```

```
%res = fdiv <16 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0,  
double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0,  
double 64.0>
```

```
ret <16 x double> %res
```

```
}
```

```
; This one is small enough to satisfy isSimple, but still illegally large.
```

```
define <4 x double> @test_sitofp_fixed_shortish(<4 x i64> %in) {
```

```
; CHECK-LABEL: test_sitofp_fixed_shortish:
```

```
; CHECK-DAG: scvtf.2d v0, v0
```

```
; CHECK-DAG: scvtf.2d v1, v1
```

```
; CHECK: ret
```

```
%flt = sitofp <4 x i64> %in to <4 x double>
```

```
%res = fdiv <4 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0>
```

```
ret <4 x double> %res
```

```

}
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=x86_64-linux-android -mattr=+mmx -enable-legalize-types-checking | FileCheck %s
;
; D31946
; Check that we dont end up with the ""LLVM ERROR: Cannot select" error.
; Additionally ensure that the output code actually put fp128 values in SSE registers.

```

```

declare fp128 @llvm.fabs.f128(fp128)
declare fp128 @llvm.copysign.f128(fp128, fp128)

```

```

define fp128 @TestSelect(fp128 %a, fp128 %b) {
; CHECK-LABEL: TestSelect:
; CHECK:      # %bb.0:
; CHECK-NEXT: pushq %rbx
; CHECK-NEXT: .cfi_def_cfa_offset 16
; CHECK-NEXT: subq $32, %rsp
; CHECK-NEXT: .cfi_def_cfa_offset 48
; CHECK-NEXT: .cfi_offset %rbx, -16
; CHECK-NEXT: movaps %xmm1, {[[-0-9]+]}(%r{[sb]})p # 16-byte Spill
; CHECK-NEXT: movaps %xmm0, (%rsp) # 16-byte Spill
; CHECK-NEXT: callq __gttf2
; CHECK-NEXT: movl %eax, %ebx
; CHECK-NEXT: movaps (%rsp), %xmm0 # 16-byte Reload
; CHECK-NEXT: movaps {[[-0-9]+]}(%r{[sb]})p, %xmm1 # 16-byte Reload
; CHECK-NEXT: callq __subtf3
; CHECK-NEXT: testl %ebx, %ebx
; CHECK-NEXT: jg .LBB0_2
; CHECK-NEXT: # %bb.1:
; CHECK-NEXT: xorps %xmm0, %xmm0
; CHECK-NEXT: .LBB0_2:
; CHECK-NEXT: addq $32, %rsp
; CHECK-NEXT: .cfi_def_cfa_offset 16
; CHECK-NEXT: popq %rbx
; CHECK-NEXT: .cfi_def_cfa_offset 8
; CHECK-NEXT: retq
%cmp = fcmp ogt fp128 %a, %b
%sub = fsub fp128 %a, %b
%res = select i1 %cmp, fp128 %sub, fp128 0xL00000000000000000000000000000000
ret fp128 %res
}

```

```

define fp128 @TestFabs(fp128 %a) {
; CHECK-LABEL: TestFabs:
; CHECK:      # %bb.0:
; CHECK-NEXT: andps {[*]}(%rip), %xmm0
; CHECK-NEXT: retq
%res = call fp128 @llvm.fabs.f128(fp128 %a)

```

```

ret fp128 %res
}

define fp128 @TestCopysign(fp128 %a, fp128 %b) {
; CHECK-LABEL: TestCopysign:
; CHECK:      # %bb.0:
; CHECK-NEXT: andps {{.*}}(%rip), %xmm1
; CHECK-NEXT: andps {{.*}}(%rip), %xmm0
; CHECK-NEXT: orps %xmm1, %xmm0
; CHECK-NEXT: retq
%res = call fp128 @llvm.copysign.f128(fp128 %a, fp128 %b)
ret fp128 %res
}

```

```

define fp128 @TestFneg(fp128 %a) {
; CHECK-LABEL: TestFneg:
; CHECK:      # %bb.0:
; CHECK-NEXT: pushq %rax
; CHECK-NEXT: .cfi_def_cfa_offset 16
; CHECK-NEXT: movaps %xmm0, %xmm1
; CHECK-NEXT: callq __multf3
; CHECK-NEXT: xorps {{.*}}(%rip), %xmm0
; CHECK-NEXT: popq %rax
; CHECK-NEXT: .cfi_def_cfa_offset 8
; CHECK-NEXT: retq
%mul = fmul fp128 %a, %a
%res = fsub fp128 0xL00000000000000008000000000000000, %mul
ret fp128 %res
}

```

=====
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```
; RUN: opt %s -inline -S | FileCheck %s
```

```
define internal void @innerSmall() "min-legal-vector-width"="128" {  
  ret void  
}
```

```
define internal void @innerLarge() "min-legal-vector-width"="512" {  
  ret void
```

```
}
```

```
define internal void @innerNoAttribute() {  
  ret void  
}
```

; We should not add an attribute during inlining. No attribute means unknown.
; Inlining doesn't change the fact that we don't know anything about this
; function.

```
define void @outerNoAttribute() {  
  call void @innerLarge()  
  ret void  
}
```

```
define void @outerConflictingAttributeSmall() "min-legal-vector-width"="128" {  
  call void @innerLarge()  
  ret void  
}
```

```
define void @outerConflictingAttributeLarge() "min-legal-vector-width"="512" {  
  call void @innerSmall()  
  ret void  
}
```

; We should remove the attribute after inlining since the callee's
; vector width requirements are unknown.

```
define void @outerAttribute() "min-legal-vector-width"="128" {  
  call void @innerNoAttribute()  
  ret void  
}
```

```
; CHECK: define void @outerNoAttribute() {  
; CHECK: define void @outerConflictingAttributeSmall() #0  
; CHECK: define void @outerConflictingAttributeLarge() #0  
; CHECK: define void @outerAttribute() {  
; CHECK: attributes #0 = { "min-legal-vector-width"="512" }
```

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```
; RUN: opt -consthoist -S -o - %s | FileCheck %s
target datalayout = "e-m:e-p:32:32-i64:64-v128:64:128-a:0:32-n32-S64"
target triple = "thumbv6m-none--musleabi"
```

```
; Check that for i8 type, the maximum legal offset is 31.
; Also check that a constant used as value to be stored rather than
; pointer in a store instruction is hoisted.
; CHECK: foo_i8
; CHECK-DAG: %[[C1:const[0-9]?]] = bitcast i32 805874720 to i32
; CHECK-DAG: %[[C2:const[0-9]?]] = bitcast i32 805874688 to i32
; CHECK-DAG: %[[C3:const[0-9]?]] = bitcast i32 805873720 to i32
; CHECK-DAG: %[[C4:const[0-9]?]] = bitcast i32 805873688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to i8*
; CHECK-NEXT: %1 = load volatile i8, i8* %0
; CHECK-NEXT: %[[M1:const_mat[0-9]?]] = add i32 %[[C2]], 4
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to i8*
; CHECK-NEXT: %3 = load volatile i8, i8* %2
; CHECK-NEXT: %[[M2:const_mat[0-9]?]] = add i32 %[[C2]], 31
; CHECK-NEXT: %4 = inttoptr i32 %[[M2]] to i8*
; CHECK-NEXT: %5 = load volatile i8, i8* %4
; CHECK-NEXT: %6 = inttoptr i32 %[[C1]] to i8*
; CHECK-NEXT: %7 = load volatile i8, i8* %6
; CHECK-NEXT: %[[M3:const_mat[0-9]?]] = add i32 %[[C1]], 7
; CHECK-NEXT: %8 = inttoptr i32 %[[M3]] to i8*
; CHECK-NEXT: %9 = load volatile i8, i8* %8
; CHECK-NEXT: %10 = inttoptr i32 %[[C4]] to i8*
; CHECK-NEXT: store i8 %9, i8* %10
; CHECK-NEXT: %[[M4:const_mat[0-9]?]] = add i32 %[[C4]], 31
; CHECK-NEXT: %11 = inttoptr i32 %[[M4]] to i8*
; CHECK-NEXT: store i8 %7, i8* %11
; CHECK-NEXT: %12 = inttoptr i32 %[[C3]] to i8*
; CHECK-NEXT: store i8 %5, i8* %12
```

```

; CHECK-NEXT: %[[M5:const_mat[0-9]?]] = add i32 %[[C3]], 7
; CHECK-NEXT: %13 = inttoptr i32 %[[M5]] to i8*
; CHECK-NEXT: store i8 %3, i8* %13
; CHECK-NEXT: %[[M6:const_mat[0-9]?]] = add i32 %[[C1]], 80
; CHECK-NEXT: %14 = inttoptr i32 %[[M6]] to i8*
; CHECK-NEXT: store i8* %14, i8** @goo

```

```
@goo = global i8* undef
```

```

define void @foo_i8() {
entry:
%0 = load volatile i8, i8* inttoptr (i32 805874688 to i8*)
%1 = load volatile i8, i8* inttoptr (i32 805874692 to i8*)
%2 = load volatile i8, i8* inttoptr (i32 805874719 to i8*)
%3 = load volatile i8, i8* inttoptr (i32 805874720 to i8*)
%4 = load volatile i8, i8* inttoptr (i32 805874727 to i8*)
store i8 %4, i8* inttoptr(i32 805873688 to i8*)
store i8 %3, i8* inttoptr(i32 805873719 to i8*)
store i8 %2, i8* inttoptr(i32 805873720 to i8*)
store i8 %1, i8* inttoptr(i32 805873727 to i8*)
store i8* inttoptr(i32 805874800 to i8*), i8** @goo
ret void
}

```

```

; Check that for i16 type, the maximum legal offset is 62.
; CHECK: foo_i16
; CHECK-DAG: %[[C1:const[0-9]?]] = bitcast i32 805874752 to i32
; CHECK-DAG: %[[C2:const[0-9]?]] = bitcast i32 805874688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to i16*
; CHECK-NEXT: %1 = load volatile i16, i16* %0, align 2
; CHECK-NEXT: %[[M1:const_mat[0-9]?]] = add i32 %[[C2]], 4
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to i16*
; CHECK-NEXT: %3 = load volatile i16, i16* %2, align 2
; CHECK-NEXT: %[[M2:const_mat[0-9]?]] = add i32 %[[C2]], 32
; CHECK-NEXT: %4 = inttoptr i32 %[[M2]] to i16*
; CHECK-NEXT: %5 = load volatile i16, i16* %4, align 2
; CHECK-NEXT: %[[M3:const_mat[0-9]?]] = add i32 %[[C2]], 62
; CHECK-NEXT: %6 = inttoptr i32 %[[M3]] to i16*
; CHECK-NEXT: %7 = load volatile i16, i16* %6, align 2
; CHECK-NEXT: %8 = inttoptr i32 %[[C1]] to i16*
; CHECK-NEXT: %9 = load volatile i16, i16* %8, align 2
; CHECK-NEXT: %[[M4:const_mat[0-9]?]] = add i32 %[[C1]], 22
; CHECK-NEXT: %10 = inttoptr i32 %[[M4]] to i16*
; CHECK-NEXT: %11 = load volatile i16, i16* %10, align 2

```

```

define void @foo_i16() {
entry:
%0 = load volatile i16, i16* inttoptr (i32 805874688 to i16*), align 2

```

```

%1 = load volatile i16, i16* inttoptr (i32 805874692 to i16*), align 2
%2 = load volatile i16, i16* inttoptr (i32 805874720 to i16*), align 2
%3 = load volatile i16, i16* inttoptr (i32 805874750 to i16*), align 2
%4 = load volatile i16, i16* inttoptr (i32 805874752 to i16*), align 2
%5 = load volatile i16, i16* inttoptr (i32 805874774 to i16*), align 2
ret void
}

```

```

; Check that for i32 type, the maximum legal offset is 124.
; CHECK: foo_i32
; CHECK-DAG: %[[C1:const[0-9]?]] = bitcast i32 805874816 to i32
; CHECK-DAG: %[[C2:const[0-9]?]] = bitcast i32 805874688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to i32*
; CHECK-NEXT: %1 = load volatile i32, i32* %0, align 4
; CHECK-NEXT: %[[M1:const_mat[0-9]?]] = add i32 %[[C2]], 4
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to i32*
; CHECK-NEXT: %3 = load volatile i32, i32* %2, align 4
; CHECK-NEXT: %[[M2:const_mat[0-9]?]] = add i32 %[[C2]], 124
; CHECK-NEXT: %4 = inttoptr i32 %[[M2]] to i32*
; CHECK-NEXT: %5 = load volatile i32, i32* %4, align 4
; CHECK-NEXT: %6 = inttoptr i32 %[[C1]] to i32*
; CHECK-NEXT: %7 = load volatile i32, i32* %6, align 4
; CHECK-NEXT: %[[M3:const_mat[0-9]?]] = add i32 %[[C1]], 8
; CHECK-NEXT: %8 = inttoptr i32 %[[M3]] to i32*
; CHECK-NEXT: %9 = load volatile i32, i32* %8, align 4
; CHECK-NEXT: %[[M4:const_mat[0-9]?]] = add i32 %[[C1]], 12
; CHECK-NEXT: %10 = inttoptr i32 %[[M4]] to i32*
; CHECK-NEXT: %11 = load volatile i32, i32* %10, align 4

```

```

define void @foo_i32() {
entry:
%0 = load volatile i32, i32* inttoptr (i32 805874688 to i32*), align 4
%1 = load volatile i32, i32* inttoptr (i32 805874692 to i32*), align 4
%2 = load volatile i32, i32* inttoptr (i32 805874812 to i32*), align 4
%3 = load volatile i32, i32* inttoptr (i32 805874816 to i32*), align 4
%4 = load volatile i32, i32* inttoptr (i32 805874824 to i32*), align 4
%5 = load volatile i32, i32* inttoptr (i32 805874828 to i32*), align 4
ret void
}

```

```

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```
; NOTE: Assertions have been autogenerated by utils/update_test_checks.py UTC_ARGS: --function-signature --scrub-attributes
```

```
; RUN: opt -S -argpromotion < %s | FileCheck %s
```

```
; RUN: opt -S -passes=argpromotion < %s | FileCheck %s
```

```
; Test that we only promote arguments when the caller/callee have compatible
```

```
; function attributes.
```

```
target triple = "x86_64-unknown-linux-gnu"
```

```
; This should promote
```

```
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #0 {
```

```
; CHECK-LABEL: define {{{^@+}}} @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
```

```
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
```

```
; CHECK-NEXT: bb:
```

```
; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
```

```
; CHECK-NEXT: ret void
```

```
;
```

```
bb:
```

```
%tmp = load <8 x i64>, <8 x i64>* %arg1
```

```
store <8 x i64> %tmp, <8 x i64>* %arg
```

```
ret void
```

```
}
```

```
define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg) #0 {
```

```
; CHECK-LABEL: define {{{^@+}}} @avx512_legal512_prefer512_call_avx512_legal512_prefer512
```

```
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
```

```
; CHECK-NEXT: bb:
```

```
; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
```

```
; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
```

```
; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
```

```
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
```

```
; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
```

```
; CHECK-NEXT: call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* [[TMP2]], <8 x i64> [[TMP_VAL]])
```

```
; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
```

```
; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
```

```
; CHECK-NEXT: ret void
```

```
;
```

```
bb:
```

```
%tmp = alloca <8 x i64>, align 32
```

```
%tmp2 = alloca <8 x i64>, align 32
```

```
%tmp3 = bitcast <8 x i64>* %tmp to i8*
```

```
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
```

```
call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x
```

```

i64>* %tmp)
  %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
  store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
  ret void
}

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #1 {
; CHECK-LABEL: define {[^@]+}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
; CHECK-NEXT: ret void
;
bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #1 {
; CHECK-LABEL: define {[^@]+}@avx512_legal512_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
; CHECK-NEXT: call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x
i64>* [[TMP2]], <8 x i64> [[TMP_VAL]])
; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT: ret void
;
bb:
  %tmp = alloca <8 x i64>, align 32
  %tmp2 = alloca <8 x i64>, align 32
  %tmp3 = bitcast <8 x i64>* %tmp to i8*
  call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
  call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
  %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
  store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
  ret void
}

```

```

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #1 {
; CHECK-LABEL: define {{{^@}+}}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
; CHECK-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg) #0 {
; CHECK-LABEL: define {{{^@}+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
; CHECK-NEXT: call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x
i64>* [[TMP2]], <8 x i64> [[TMP_VAL]])
; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT: ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

```

```

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #0 {
; CHECK-LABEL: define {{{^@}+}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
; CHECK-NEXT: bb:

```

```

; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
; CHECK-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg) #1 {
; CHECK-LABEL: define {{{^@+}}} @avx512_legal512_prefer256_call_avx512_legal512_prefer512
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
; CHECK-NEXT: call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x
i64>* [[TMP2]], <8 x i64> [[TMP_VAL]])
; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT: ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #1 {
; CHECK-LABEL: define {{{^@+}}} @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64>* readonly [[ARG1:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]]
; CHECK-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]]
; CHECK-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1

```

```

store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #2 {
; CHECK-LABEL: define {{{^@}+}}@avx512_legal256_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT: call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x
i64>* [[TMP2]], <8 x i64>* [[TMP]])
; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT: ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #2 {
; CHECK-LABEL: define {{{^@}+}}@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64>* readonly [[ARG1:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]]
; CHECK-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]]
; CHECK-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg) #1 {
; CHECK-LABEL: define {{{^@}+}}@avx512_legal512_prefer256_call_avx512_legal256_prefer256

```

```

; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT: call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x
i64>* [[TMP2]], <8 x i64>* [[TMP]])
; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT: ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should promote
define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8
x i64>* readonly %arg1) #3 {
; CHECK-LABEL: define {{{^@+}}}@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
; CHECK-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg) #4 {
; CHECK-LABEL: define {{{^@+}}}@avx2_legal256_prefer256_call_avx2_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]

```

```

; CHECK-NEXT: call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>*
[[TMP2]], <8 x i64> [[TMP_VAL]])
; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT: ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>*
%tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

```

; This should promote

```

define internal fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg, <8
x i64>* readonly %arg1) #4 {
; CHECK-LABEL: define {{{^@+}}}@callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
; CHECK-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

```

```

define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg) #3 {
; CHECK-LABEL: define {{{^@+}}}@avx2_legal512_prefer256_call_avx2_legal256_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
; CHECK-NEXT: call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>*
[[TMP2]], <8 x i64> [[TMP_VAL]])
; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT: ret void
;

```

```

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>*
%tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

```

; If the arguments are scalar, its ok to promote.

```

define internal i32 @scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32* %X, i32*
%Y) #2 {
; CHECK-LABEL: define
{[^[^@]+]}@scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (i32 [[X_VAL:%.*]], i32 [[Y_VAL:%.*]])
; CHECK-NEXT: [[C:%.*]] = add i32 [[X_VAL]], [[Y_VAL]]
; CHECK-NEXT: ret i32 [[C]]
;
%A = load i32, i32* %X
%B = load i32, i32* %Y
%C = add i32 %A, %B
ret i32 %C
}

```

```

define i32 @scalar_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32* %B) #2 {
; CHECK-LABEL: define {[^[^@]+]}@scalar_avx512_legal256_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (i32* [[B:%.*]])
; CHECK-NEXT: [[A:%.*]] = alloca i32
; CHECK-NEXT: store i32 1, i32* [[A]]
; CHECK-NEXT: [[A_VAL:%.*]] = load i32, i32* [[A]]
; CHECK-NEXT: [[B_VAL:%.*]] = load i32, i32* [[B]]
; CHECK-NEXT: [[C:%.*]] = call i32
@scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32 [[A_VAL]], i32 [[B_VAL]])
; CHECK-NEXT: ret i32 [[C]]
;
%A = alloca i32
store i32 1, i32* %A
%C = call i32 @scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32* %A, i32* %B)
ret i32 %C
}

```

; If the arguments are scalar, its ok to promote.

```

define internal i32 @scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32* %X, i32*
%Y) #2 {
; CHECK-LABEL: define

```

```

{{{^@+}}@scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256
; CHECK-SAME: (i32 [[X_VAL:%.*]], i32 [[Y_VAL:%.*]])
; CHECK-NEXT:  [[C:%.*]] = add i32 [[X_VAL]], [[Y_VAL]]
; CHECK-NEXT:  ret i32 [[C]]
;
%A = load i32, i32* %X
%B = load i32, i32* %Y
%C = add i32 %A, %B
ret i32 %C
}

define i32 @scalar_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32* %B) #2 {
; CHECK-LABEL: define {{{^@+}}@scalar_avx512_legal512_prefer256_call_avx512_legal256_prefer256
; CHECK-SAME: (i32* [[B:%.*]])
; CHECK-NEXT:  [[A:%.*]] = alloca i32
; CHECK-NEXT:  store i32 1, i32* [[A]]
; CHECK-NEXT:  [[A_VAL:%.*]] = load i32, i32* [[A]]
; CHECK-NEXT:  [[B_VAL:%.*]] = load i32, i32* [[B]]
; CHECK-NEXT:  [[C:%.*]] = call i32
@scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32 [[A_VAL]], i32 [[B_VAL]])
; CHECK-NEXT:  ret i32 [[C]]
;
%A = alloca i32
store i32 1, i32* %A
%C = call i32 @scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32* %A, i32* %B)
ret i32 %C
}

```

```

; Function Attrs: argmemonly nounwind
declare void @llvm.memset.p0i8.i64(i8* nocapture writeonly, i8, i64, i1) #5

```

```

attributes #0 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="512" }
attributes #1 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #2 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #3 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #4 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #5 = { argmemonly nounwind }

```

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; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py

; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 -mattr=prefer-256-bit | FileCheck %s --check-prefixes=CHECK,CHECK-AVX512

; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 -mattr=prefer-256-bit,avx512vbmi | FileCheck %s --check-prefixes=CHECK,CHECK-VBMI

; Make sure CPUs default to prefer-256-bit. avx512vnni isn't interesting as it just adds an isel peephole for vpmaddwd+vpadd

; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 | FileCheck %s --check-prefixes=CHECK,CHECK-AVX512

; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=cascadelake | FileCheck %s --check-prefixes=CHECK,CHECK-AVX512

; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=cooperlake | FileCheck %s --check-prefixes=CHECK,CHECK-AVX512

; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=cannonlake | FileCheck %s --check-prefixes=CHECK,CHECK-VBMI

; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=icelake-client | FileCheck %s --check-prefixes=CHECK,CHECK-VBMI

; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=icelake-server | FileCheck %s --check-prefixes=CHECK,CHECK-VBMI

; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=tigerlake | FileCheck %s --check-prefixes=CHECK,CHECK-VBMI

; This file primarily contains tests for specific places in X86ISelLowering.cpp that needed be made aware of the legalizer not allowing 512-bit vectors due to prefer-256-bit even though AVX512 is enabled.

```
define void @add256(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="256" {
```

```
; CHECK-LABEL: add256:
```

```
; CHECK:      # %bb.0:
```

```
; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
```

```
; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
```

```
; CHECK-NEXT:  vpadd 32(%rsi), %ymm1, %ymm1
```

```
; CHECK-NEXT:  vpadd (%rsi), %ymm0, %ymm0
```

```
; CHECK-NEXT:  vmovdqa %ymm0, (%rdx)
```

```
; CHECK-NEXT:  vmovdqa %ymm1, 32(%rdx)
```

```
; CHECK-NEXT:  vzeroupper
```

```
; CHECK-NEXT:  retq
```



```

12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
%even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32
13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
%ret = add <16 x i32> %odd, %even
store <16 x i32> %ret, <16 x i32>* %CPtr
ret void
}

```

```

define void @pmaddwd_32_512(<32 x i16>* %APtr, <32 x i16>* %BPtr, <16 x i32>* %CPtr) "min-legal-vector-
width"="512" {
; CHECK-LABEL: pmaddwd_32_512:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT:  vpmaddwd (%rsi), %zmm0, %zmm0
; CHECK-NEXT:  vmovdqa64 %zmm0, (%rdx)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%A = load <32 x i16>, <32 x i16>* %APtr
%B = load <32 x i16>, <32 x i16>* %BPtr
%a = sext <32 x i16> %A to <32 x i32>
%b = sext <32 x i16> %B to <32 x i32>
%m = mul nsw <32 x i32> %a, %b
%odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32
12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
%even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32
13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
%ret = add <16 x i32> %odd, %even
store <16 x i32> %ret, <16 x i32>* %CPtr
ret void
}

```

```

define void @psubus_64i8_max_256(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-
width"="256" {
; CHECK-LABEL: psubus_64i8_max_256:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:  vpsubusb 32(%rsi), %ymm1, %ymm1
; CHECK-NEXT:  vpsubusb (%rsi), %ymm0, %ymm0
; CHECK-NEXT:  vmovdqa %ymm0, (%rdx)
; CHECK-NEXT:  vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%x = load <64 x i8>, <64 x i8>* %xptr
%y = load <64 x i8>, <64 x i8>* %yptr
%cmp = icmp ult <64 x i8> %x, %y
%max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
%res = sub <64 x i8> %max, %y

```

```

store <64 x i8> %res, <64 x i8>* %zptr
ret void
}

```

```

define void @psubus_64i8_max_512(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-
width"="512" {
; CHECK-LABEL: psubus_64i8_max_512:
; CHECK:      # %bb.0:
; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT: vpsubusb (%rsi), %zmm0, %zmm0
; CHECK-NEXT: vmovdqa64 %zmm0, (%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <64 x i8>, <64 x i8>* %xptr
%y = load <64 x i8>, <64 x i8>* %yptr
%cmp = icmp ult <64 x i8> %x, %y
%max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
%res = sub <64 x i8> %max, %y
store <64 x i8> %res, <64 x i8>* %zptr
ret void
}

```

```

define i32 @_Z9test_charPcS_i_256(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-
width"="256" {
; CHECK-LABEL: _Z9test_charPcS_i_256:
; CHECK:      # %bb.0: # %entry
; CHECK-NEXT: movl %edx, %eax
; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT: xorl %ecx, %ecx
; CHECK-NEXT: vpxor %xmm1, %xmm1, %xmm1
; CHECK-NEXT: vpxor %xmm2, %xmm2, %xmm2
; CHECK-NEXT: .p2align 4, 0x90
; CHECK-NEXT: .LBB8_1: # %vector.body
; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
; CHECK-NEXT: vpmovsxbw 16(%rdi,%rcx), %ymm3
; CHECK-NEXT: vpmovsxbw (%rdi,%rcx), %ymm4
; CHECK-NEXT: vpmovsxbw 16(%rsi,%rcx), %ymm5
; CHECK-NEXT: vpmaddwd %ymm3, %ymm5, %ymm3
; CHECK-NEXT: vpadd %ymm2, %ymm3, %ymm2
; CHECK-NEXT: vpmovsxbw (%rsi,%rcx), %ymm3
; CHECK-NEXT: vpmaddwd %ymm4, %ymm3, %ymm3
; CHECK-NEXT: vpadd %ymm1, %ymm3, %ymm1
; CHECK-NEXT: addq $32, %rcx
; CHECK-NEXT: cmpq %rcx, %rax
; CHECK-NEXT: jne .LBB8_1
; CHECK-NEXT: # %bb.2: # %middle.block
; CHECK-NEXT: vpadd %ymm0, %ymm1, %ymm1
; CHECK-NEXT: vpadd %ymm0, %ymm2, %ymm0

```

```

; CHECK-NEXT: vpaddd %ymm0, %ymm1, %ymm0
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT: vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vpshufd { {. *#+ } } xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT: vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vpshufd { {. *#+ } } xmm1 = xmm0[1,1,2,3]
; CHECK-NEXT: vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vmovd %xmm0, %eax
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq

```

entry:

```

%3 = zext i32 %2 to i64
br label %vector.body

```

vector.body:

```

%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%4 = getelementptr inbounds i8, i8* %0, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mul nsw <32 x i32> %9, %6
%11 = add nsw <32 x i32> %10, %vec.phi
%index.next = add i64 %index, 32
%12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body

```

middle.block:

```

%rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20,
i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef>
%bin.rdx1 = add <32 x i32> %11, %rdx.shuf1
%rdx.shuf = shufflevector <32 x i32> %bin.rdx1, <32 x i32> undef, <32 x i32> <i32 8, i32 9, i32 10, i32 11, i32
12, i32 13, i32 14, i32 15, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx = add <32 x i32> %bin.rdx1, %rdx.shuf
%rdx.shuf15 = shufflevector <32 x i32> %bin.rdx, <32 x i32> undef, <32 x i32> <i32 4, i32 5, i32 6, i32 7, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx32 = add <32 x i32> %bin.rdx, %rdx.shuf15
%rdx.shuf17 = shufflevector <32 x i32> %bin.rdx32, <32 x i32> undef, <32 x i32> <i32 2, i32 3, i32 undef, i32

```

```

undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx18 = add <32 x i32> %bin.rdx32, %rdx.shuf17
%rdx.shuf19 = shufflevector <32 x i32> %bin.rdx18, <32 x i32> undef, <32 x i32> <i32 1, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx20 = add <32 x i32> %bin.rdx18, %rdx.shuf19
%13 = extractelement <32 x i32> %bin.rdx20, i32 0
ret i32 %13
}

```

```

define i32 @_Z9test_charPcS_i_512(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-
width"="512" {

```

```

; CHECK-LABEL: _Z9test_charPcS_i_512:
; CHECK:    # %bb.0: # %entry
; CHECK-NEXT:  movl %edx, %eax
; CHECK-NEXT:  vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT:  xorl %ecx, %ecx
; CHECK-NEXT:  vpxor %xmm1, %xmm1, %xmm1
; CHECK-NEXT:  .p2align 4, 0x90
; CHECK-NEXT: .LBB9_1: # %vector.body
; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
; CHECK-NEXT: vpmovsxbw (%rdi,%rcx), %zmm2
; CHECK-NEXT: vpmovsxbw (%rsi,%rcx), %zmm3
; CHECK-NEXT: vpmaddwd %zmm2, %zmm3, %zmm2
; CHECK-NEXT: vpadd %zmm1, %zmm2, %zmm1
; CHECK-NEXT: addq $32, %rcx
; CHECK-NEXT: cmpq %rcx, %rax
; CHECK-NEXT: jne .LBB9_1
; CHECK-NEXT: # %bb.2: # %middle.block
; CHECK-NEXT: vpadd %zmm0, %zmm1, %zmm0
; CHECK-NEXT: vextracti64x4 $1, %zmm0, %ymm1
; CHECK-NEXT: vpadd %zmm1, %zmm0, %zmm0
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT: vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vpsltd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT: vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vpsltd {{.*#+}} xmm1 = xmm0[1,1,2,3]
; CHECK-NEXT: vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vmovd %xmm0, %eax
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
entry:
%3 = zext i32 %2 to i64
br label %vector.body

```

vector.body:

```
%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%4 = getelementptr inbounds i8, i8* %0, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mul nsw <32 x i32> %9, %6
%11 = add nsw <32 x i32> %10, %vec.phi
%index.next = add i64 %index, 32
%12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body
```

middle.block:

```
%rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20,
i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef>
%bin.rdx1 = add <32 x i32> %11, %rdx.shuf1
%rdx.shuf = shufflevector <32 x i32> %bin.rdx1, <32 x i32> undef, <32 x i32> <i32 8, i32 9, i32 10, i32 11, i32
12, i32 13, i32 14, i32 15, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx = add <32 x i32> %bin.rdx1, %rdx.shuf
%rdx.shuf15 = shufflevector <32 x i32> %bin.rdx, <32 x i32> undef, <32 x i32> <i32 4, i32 5, i32 6, i32 7, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx32 = add <32 x i32> %bin.rdx, %rdx.shuf15
%rdx.shuf17 = shufflevector <32 x i32> %bin.rdx32, <32 x i32> undef, <32 x i32> <i32 2, i32 3, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx18 = add <32 x i32> %bin.rdx32, %rdx.shuf17
%rdx.shuf19 = shufflevector <32 x i32> %bin.rdx18, <32 x i32> undef, <32 x i32> <i32 1, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx20 = add <32 x i32> %bin.rdx18, %rdx.shuf19
%13 = extractelement <32 x i32> %bin.rdx20, i32 0
ret i32 %13
}
```

@a = global [1024 x i8] zeroinitializer, align 16

@b = global [1024 x i8] zeroinitializer, align 16

```
define i32 @sad_16i8_256() "min-legal-vector-width"="256" {
; CHECK-LABEL: sad_16i8_256:
; CHECK:      # %bb.0: # %entry
; CHECK-NEXT:  vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT:  movq $-1024, %rax # imm = 0xFC00
; CHECK-NEXT:  vpxor %xmm1, %xmm1, %xmm1
; CHECK-NEXT:  .p2align 4, 0x90
; CHECK-NEXT:  .LBB10_1: # %vector.body
; CHECK-NEXT:  # =>This Inner Loop Header: Depth=1
; CHECK-NEXT:  vmovdqu a+1024(%rax), %xmm2
; CHECK-NEXT:  vpsadbw b+1024(%rax), %xmm2, %xmm2
; CHECK-NEXT:  vpaddd %ymm1, %ymm2, %ymm1
; CHECK-NEXT:  addq $4, %rax
; CHECK-NEXT:  jne .LBB10_1
; CHECK-NEXT:  # %bb.2: # %middle.block
; CHECK-NEXT:  vpaddd %ymm0, %ymm1, %ymm0
; CHECK-NEXT:  vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT:  vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT:  vpshufd { {.*#+} } xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT:  vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT:  vpshufd { {.*#+} } xmm1 = xmm0[1,1,2,3]
; CHECK-NEXT:  vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT:  vmovd %xmm0, %eax
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
entry:
br label %vector.body
```

vector.body:

```
%index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
%vec.phi = phi <16 x i32> [ zeroinitializer, %entry ], [ %10, %vector.body ]
%0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
%1 = bitcast i8* %0 to <16 x i8>*
%wide.load = load <16 x i8>, <16 x i8>* %1, align 4
%2 = zext <16 x i8> %wide.load to <16 x i32>
%3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
%4 = bitcast i8* %3 to <16 x i8>*
%wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4
%5 = zext <16 x i8> %wide.load1 to <16 x i32>
%6 = sub nsw <16 x i32> %2, %5
%7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1>
%8 = sub nsw <16 x i32> zeroinitializer, %6
%9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
%10 = add nsw <16 x i32> %9, %vec.phi
%index.next = add i64 %index, 4
```

```

%11 = icmp eq i64 %index.next, 1024
br i1 %11, label %middle.block, label %vector.body

middle.block:
%rdx.shuf = shufflevector <16 x i32> %10, <16 x i32> undef, <16 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32
13, i32 14, i32 15, i32 undef, i32 undef>
%bin.rdx = add <16 x i32> %10, %rdx.shuf
%rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> <i32 4, i32 5, i32 6, i32 7, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef>
%bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> <i32 2, i32 3, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef>
%bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> <i32 1, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef>
%bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
ret i32 %12
}

```

```

define i32 @sad_16i8_512() "min-legal-vector-width"="512" {
; CHECK-LABEL: sad_16i8_512:
; CHECK:      # %bb.0: # %entry
; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT: movq $-1024, %rax # imm = 0xFC00
; CHECK-NEXT: .p2align 4, 0x90
; CHECK-NEXT: .LBB11_1: # %vector.body
; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
; CHECK-NEXT: vmovdqu a+1024(%rax), %xmm1
; CHECK-NEXT: vpsadbw b+1024(%rax), %xmm1, %xmm1
; CHECK-NEXT: vpadd %zmm0, %zmm1, %zmm0
; CHECK-NEXT: addq $4, %rax
; CHECK-NEXT: jne .LBB11_1
; CHECK-NEXT: # %bb.2: # %middle.block
; CHECK-NEXT: vextracti64x4 $1, %zmm0, %ymm1
; CHECK-NEXT: vpadd %zmm1, %zmm0, %zmm0
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT: vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vpslufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT: vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vpslufd {{.*#+}} xmm1 = xmm0[1,1,2,3]
; CHECK-NEXT: vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vmovd %xmm0, %eax
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq

```

entry:

br label %vector.body

vector.body:

%index = phi i64 [0, %entry], [%index.next, %vector.body]

%vec.phi = phi <16 x i32> [zeroinitializer, %entry], [%10, %vector.body]

%0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index

%1 = bitcast i8* %0 to <16 x i8>*

%wide.load = load <16 x i8>, <16 x i8>* %1, align 4

%2 = zext <16 x i8> %wide.load to <16 x i32>

%3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index

%4 = bitcast i8* %3 to <16 x i8>*

%wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4

%5 = zext <16 x i8> %wide.load1 to <16 x i32>

%6 = sub nsw <16 x i32> %2, %5

%7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1>

%8 = sub nsw <16 x i32> zeroinitializer, %6

%9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8

%10 = add nsw <16 x i32> %9, %vec.phi

%index.next = add i64 %index, 4

%11 = icmp eq i64 %index.next, 1024

br i1 %11, label %middle.block, label %vector.body

middle.block:

%rdx.shuf = shufflevector <16 x i32> %10, <16 x i32> undef, <16 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef>

%bin.rdx = add <16 x i32> %10, %rdx.shuf

%rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> <i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef>

%bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2

%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> <i32 2, i32 3, i32 undef, i32 undef>

%bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3

%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> <i32 1, i32 undef, i32 undef>

%bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4

%12 = extractelement <16 x i32> %bin.rdx4, i32 0

ret i32 %12

}

define void @sbto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {

; CHECK-LABEL: sbto16f32_256:

; CHECK: # %bb.0:

; CHECK-NEXT: vpmovw2m %ymm0, %k0

```

; CHECK-NEXT: kshiftrw $8, %k0, %k1
; CHECK-NEXT: vpmovm2d %k1, %ymm0
; CHECK-NEXT: vcvtdq2ps %ymm0, %ymm0
; CHECK-NEXT: vpmovm2d %k0, %ymm1
; CHECK-NEXT: vcvtdq2ps %ymm1, %ymm1
; CHECK-NEXT: vmovaps %ymm1, (%rdi)
; CHECK-NEXT: vmovaps %ymm0, 32(%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @sbto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: sbto16f32_512:
; CHECK: # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: vpmovm2d %k0, %zmm0
; CHECK-NEXT: vcvtdq2ps %zmm0, %zmm0
; CHECK-NEXT: vmovaps %zmm0, (%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @sbto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: sbto16f64_256:
; CHECK: # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: kshiftrw $8, %k0, %k1
; CHECK-NEXT: vpmovm2d %k1, %ymm0
; CHECK-NEXT: vcvtdq2pd %xmm0, %ymm1
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm0
; CHECK-NEXT: vcvtdq2pd %xmm0, %ymm0
; CHECK-NEXT: vpmovm2d %k0, %ymm2
; CHECK-NEXT: vcvtdq2pd %xmm2, %ymm3
; CHECK-NEXT: vextracti128 $1, %ymm2, %xmm2
; CHECK-NEXT: vcvtdq2pd %xmm2, %ymm2
; CHECK-NEXT: vmovaps %ymm2, 32(%rdi)
; CHECK-NEXT: vmovaps %ymm3, (%rdi)
; CHECK-NEXT: vmovaps %ymm0, 96(%rdi)
; CHECK-NEXT: vmovaps %ymm1, 64(%rdi)
; CHECK-NEXT: vzeroupper

```

```

; CHECK-NEXT:   retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

define void @sbto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: sbto16f64_512:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vpmovw2m %ymm0, %k0
; CHECK-NEXT:  vpmovm2d %k0, %zmm0
; CHECK-NEXT:  vcvt dq2pd %ymm0, %zmm1
; CHECK-NEXT:  vextracti64x4 $1, %zmm0, %ymm0
; CHECK-NEXT:  vcvt dq2pd %ymm0, %zmm0
; CHECK-NEXT:  vmovaps %zmm0, 64(%rdi)
; CHECK-NEXT:  vmovaps %zmm1, (%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

define void @ubto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: ubto16f32_256:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vpmovw2m %ymm0, %k0
; CHECK-NEXT:  kshiftrw $8, %k0, %k1
; CHECK-NEXT:  vpmovm2d %k1, %ymm0
; CHECK-NEXT:  vpsrld $31, %ymm0, %ymm0
; CHECK-NEXT:  vcvt dq2ps %ymm0, %ymm0
; CHECK-NEXT:  vpmovm2d %k0, %ymm1
; CHECK-NEXT:  vpsrld $31, %ymm1, %ymm1
; CHECK-NEXT:  vcvt dq2ps %ymm1, %ymm1
; CHECK-NEXT:  vmovaps %ymm1, (%rdi)
; CHECK-NEXT:  vmovaps %ymm0, 32(%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @ubto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: ubto16f32_512:

```

```

; CHECK:    # %bb.0:
; CHECK-NEXT:  vpmovw2m %ymm0, %k0
; CHECK-NEXT:  vpmovm2d %k0, %zmm0
; CHECK-NEXT:  vpsrld $31, %zmm0, %zmm0
; CHECK-NEXT:  vcvtdq2ps %zmm0, %zmm0
; CHECK-NEXT:  vmovaps %zmm0, (%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

```

```

define void @ubto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {

```

```

; CHECK-LABEL: ubto16f64_256:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vpmovw2m %ymm0, %k0
; CHECK-NEXT:  kshiftrw $8, %k0, %k1
; CHECK-NEXT:  vpmovm2d %k1, %ymm0
; CHECK-NEXT:  vpsrld $31, %ymm0, %ymm0
; CHECK-NEXT:  vcvtdq2pd %xmm0, %ymm1
; CHECK-NEXT:  vextracti128 $1, %ymm0, %xmm0
; CHECK-NEXT:  vcvtdq2pd %xmm0, %ymm0
; CHECK-NEXT:  vpmovm2d %k0, %ymm2
; CHECK-NEXT:  vpsrld $31, %ymm2, %ymm2
; CHECK-NEXT:  vcvtdq2pd %xmm2, %ymm3
; CHECK-NEXT:  vextracti128 $1, %ymm2, %xmm2
; CHECK-NEXT:  vcvtdq2pd %xmm2, %ymm2
; CHECK-NEXT:  vmovaps %ymm2, 32(%rdi)
; CHECK-NEXT:  vmovaps %ymm3, (%rdi)
; CHECK-NEXT:  vmovaps %ymm0, 96(%rdi)
; CHECK-NEXT:  vmovaps %ymm1, 64(%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

```

```

define void @ubto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {

```

```

; CHECK-LABEL: ubto16f64_512:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vpmovw2m %ymm0, %k0
; CHECK-NEXT:  vpmovm2d %k0, %zmm0
; CHECK-NEXT:  vpsrld $31, %zmm0, %zmm0
; CHECK-NEXT:  vcvtdq2pd %ymm0, %zmm1

```

```

; CHECK-NEXT: vextracti64x4 $1, %zmm0, %ymm0
; CHECK-NEXT: vcvtqd2pd %ymm0, %zmm0
; CHECK-NEXT: vmovaps %zmm0, 64(%rdi)
; CHECK-NEXT: vmovaps %zmm1, (%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

```

```

define <16 x i16> @test_16f32toub_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="256" {

```

```

; CHECK-LABEL: test_16f32toub_256:
; CHECK: # %bb.0:
; CHECK-NEXT: vcvtps2dq (%rdi), %ymm1
; CHECK-NEXT: vpslld $31, %ymm1, %ymm1
; CHECK-NEXT: vpmovd2m %ymm1, %k0
; CHECK-NEXT: vcvtps2dq 32(%rdi), %ymm1
; CHECK-NEXT: vpslld $31, %ymm1, %ymm1
; CHECK-NEXT: vpmovd2m %ymm1, %k1
; CHECK-NEXT: kunpckbw %k0, %k1, %k1
; CHECK-NEXT: vmovdqu16 %ymm0, %ymm0 {%k1} {z}
; CHECK-NEXT: retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptoui <16 x float> %a to <16 x i1>
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select
}

```

```

define <16 x i16> @test_16f32toub_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {

```

```

; CHECK-LABEL: test_16f32toub_512:
; CHECK: # %bb.0:
; CHECK-NEXT: vcvtps2dq (%rdi), %zmm1
; CHECK-NEXT: vpslld $31, %zmm1, %zmm1
; CHECK-NEXT: vpmovd2m %zmm1, %k1
; CHECK-NEXT: vmovdqu16 %ymm0, %ymm0 {%k1} {z}
; CHECK-NEXT: retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptoui <16 x float> %a to <16 x i1>
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select
}

```

```

define <16 x i16> @test_16f32tosb_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="256" {

```

```

; CHECK-LABEL: test_16f32tosb_256:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vcvttps2dq (%rdi), %ymm1
; CHECK-NEXT:  vpmovd2m %ymm1, %k0
; CHECK-NEXT:  vcvttps2dq 32(%rdi), %ymm1
; CHECK-NEXT:  vpmovd2m %ymm1, %k1
; CHECK-NEXT:  kunpckbw %k0, %k1, %k1
; CHECK-NEXT:  vmovdqu16 %ymm0, %ymm0 {%k1} {z}
; CHECK-NEXT:  retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptosi <16 x float> %a to <16 x i1>
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select
}

define <16 x i16> @test_16f32tosb_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {
; CHECK-LABEL: test_16f32tosb_512:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vcvttps2dq (%rdi), %zmm1
; CHECK-NEXT:  vpmovd2m %zmm1, %k1
; CHECK-NEXT:  vmovdqu16 %ymm0, %ymm0 {%k1} {z}
; CHECK-NEXT:  retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptosi <16 x float> %a to <16 x i1>
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select
}

define void @mul256(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="256" {
; CHECK-AVX512-LABEL: mul256:
; CHECK-AVX512:    # %bb.0:
; CHECK-AVX512-NEXT:  vmovdqa (%rdi), %ymm0
; CHECK-AVX512-NEXT:  vmovdqa 32(%rdi), %ymm1
; CHECK-AVX512-NEXT:  vmovdqa (%rsi), %ymm2
; CHECK-AVX512-NEXT:  vmovdqa 32(%rsi), %ymm3
; CHECK-AVX512-NEXT:  vpunpckhbw {{.*#+}} %ymm4 =
ymm3[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-AVX512-NEXT:  vpunpckhbw {{.*#+}} %ymm5 =
ymm1[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-AVX512-NEXT:  vpmullw %ymm4, %ymm5, %ymm4
; CHECK-AVX512-NEXT:  vmovdqa {{.*#+}} %ymm5 =
[255,255,255,255,255,255,255,255,255,255,255,255,255,255,255]
; CHECK-AVX512-NEXT:  vpand %ymm5, %ymm4, %ymm4
; CHECK-AVX512-NEXT:  vpunpcklbw {{.*#+}} %ymm3 =
ymm3[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-AVX512-NEXT:  vpunpcklbw {{.*#+}} %ymm1 =
ymm1[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]

```

```

; CHECK-AVX512-NEXT: vpmullw %ymm3, %ymm1, %ymm1
; CHECK-AVX512-NEXT: vpand %ymm5, %ymm1, %ymm1
; CHECK-AVX512-NEXT: vpackuswb %ymm4, %ymm1, %ymm1
; CHECK-AVX512-NEXT: vpunpckhbw {{.*#+}} ymm3 =
ymm2[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-AVX512-NEXT: vpunpckhbw {{.*#+}} ymm4 =
ymm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-AVX512-NEXT: vpmullw %ymm3, %ymm4, %ymm3
; CHECK-AVX512-NEXT: vpand %ymm5, %ymm3, %ymm3
; CHECK-AVX512-NEXT: vpunpcklbw {{.*#+}} ymm2 =
ymm2[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-AVX512-NEXT: vpunpcklbw {{.*#+}} ymm0 =
ymm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-AVX512-NEXT: vpmullw %ymm2, %ymm0, %ymm0
; CHECK-AVX512-NEXT: vpand %ymm5, %ymm0, %ymm0
; CHECK-AVX512-NEXT: vpackuswb %ymm3, %ymm0, %ymm0
; CHECK-AVX512-NEXT: vmovdqa %ymm0, (%rdx)
; CHECK-AVX512-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-AVX512-NEXT: vzeroupper
; CHECK-AVX512-NEXT: retq
;
; CHECK-VBMI-LABEL: mul256:
; CHECK-VBMI: # %bb.0:
; CHECK-VBMI-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-VBMI-NEXT: vmovdqa 32(%rdi), %ymm1
; CHECK-VBMI-NEXT: vmovdqa (%rsi), %ymm2
; CHECK-VBMI-NEXT: vmovdqa 32(%rsi), %ymm3
; CHECK-VBMI-NEXT: vpunpckhbw {{.*#+}} ymm4 =
ymm3[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-VBMI-NEXT: vpunpckhbw {{.*#+}} ymm5 =
ymm1[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-VBMI-NEXT: vpmullw %ymm4, %ymm5, %ymm4
; CHECK-VBMI-NEXT: vpunpcklbw {{.*#+}} ymm3 =
ymm3[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-VBMI-NEXT: vpunpcklbw {{.*#+}} ymm1 =
ymm1[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-VBMI-NEXT: vpmullw %ymm3, %ymm1, %ymm1
; CHECK-VBMI-NEXT: vmovdqa {{.*#+}} ymm3 =
[0,2,4,6,8,10,12,14,32,34,36,38,40,42,44,46,16,18,20,22,24,26,28,30,48,50,52,54,56,58,60,62]
; CHECK-VBMI-NEXT: vpermt2b %ymm4, %ymm3, %ymm1
; CHECK-VBMI-NEXT: vpunpckhbw {{.*#+}} ymm4 =
ymm2[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-VBMI-NEXT: vpunpckhbw {{.*#+}} ymm5 =
ymm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-VBMI-NEXT: vpmullw %ymm4, %ymm5, %ymm4
; CHECK-VBMI-NEXT: vpunpcklbw {{.*#+}} ymm2 =
ymm2[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-VBMI-NEXT: vpunpcklbw {{.*#+}} ymm0 =

```



```

zmm1[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,4
2,42,43,43,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63]
; CHECK-VBMI-NEXT:  vpunpckhbw {{.*#+}} zmm3 =
zmm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,4
2,42,43,43,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63]
; CHECK-VBMI-NEXT:  vmullw %zmm2, %zmm3, %zmm2
; CHECK-VBMI-NEXT:  vpunpcklbw {{.*#+}} zmm1 =
zmm1[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23,32,32,33,33,34,34,35,35,36,3
6,37,37,38,38,39,39,48,48,49,49,50,50,51,51,52,52,53,53,54,54,55,55]
; CHECK-VBMI-NEXT:  vpunpcklbw {{.*#+}} zmm0 =
zmm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23,32,32,33,33,34,34,35,35,36,3
6,37,37,38,38,39,39,48,48,49,49,50,50,51,51,52,52,53,53,54,54,55,55]
; CHECK-VBMI-NEXT:  vmullw %zmm1, %zmm0, %zmm0
; CHECK-VBMI-NEXT:  vmovdqa64 {{.*#+}} zmm1 =
[0,2,4,6,8,10,12,14,64,66,68,70,72,74,76,78,16,18,20,22,24,26,28,30,80,82,84,86,88,90,92,94,32,34,36,38,40,42,44,
46,96,98,100,102,104,106,108,110,48,50,52,54,56,58,60,62,112,114,116,118,120,122,124,126]
; CHECK-VBMI-NEXT:  vpermi2b %zmm2, %zmm0, %zmm1
; CHECK-VBMI-NEXT:  vmovdqa64 %zmm1, (%rdx)
; CHECK-VBMI-NEXT:  vzeroupper
; CHECK-VBMI-NEXT:  retq
%d = load <64 x i8>, <64 x i8>* %a
%e = load <64 x i8>, <64 x i8>* %b
%f = mul <64 x i8> %d, %e
store <64 x i8> %f, <64 x i8>* %c
ret void
}

```

; This threw an assertion at one point.

```

define <4 x i32> @mload_v4i32(<4 x i32> %trigger, <4 x i32>* %addr, <4 x i32> %dst) "min-legal-vector-
width"="256" {
; CHECK-LABEL: mload_v4i32:
; CHECK:      # %bb.0:
; CHECK-NEXT:  vptestnmd %xmm0, %xmm0, %k1
; CHECK-NEXT:  vpblendmd (%rdi), %xmm1, %xmm0 {%k1}
; CHECK-NEXT:  retq
%mask = icmp eq <4 x i32> %trigger, zeroinitializer
%res = call <4 x i32> @llvm.masked.load.v4i32.p0v4i32(<4 x i32>* %addr, i32 4, <4 x i1> %mask, <4 x i32>
%dst)
ret <4 x i32> %res
}
declare <4 x i32> @llvm.masked.load.v4i32.p0v4i32(<4 x i32>*, i32, <4 x i1>, <4 x i32>)

```

```

define <16 x i32> @trunc_v16i64_v16i32(<16 x i64>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v16i64_v16i32:
; CHECK:      # %bb.0:
; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:  vmovdqa 64(%rdi), %ymm2

```

```

; CHECK-NEXT: vmovdqa 96(%rdi), %ymm3
; CHECK-NEXT: vpmovqd %ymm0, %xmm0
; CHECK-NEXT: vpmovqd %ymm1, %xmm1
; CHECK-NEXT: vinserti128 $1, %xmm1, %ymm0, %ymm0
; CHECK-NEXT: vpmovqd %ymm2, %xmm1
; CHECK-NEXT: vpmovqd %ymm3, %xmm2
; CHECK-NEXT: vinserti128 $1, %xmm2, %ymm1, %ymm1
; CHECK-NEXT: retq
%a = load <16 x i64>, <16 x i64>* %x
%b = trunc <16 x i64> %a to <16 x i32>
ret <16 x i32> %b
}

define <16 x i8> @trunc_v16i64_v16i8(<16 x i64>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v16i64_v16i8:
; CHECK:    # %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT: vmovdqa 64(%rdi), %ymm2
; CHECK-NEXT: vmovdqa 96(%rdi), %ymm3
; CHECK-NEXT: vpmovqb %ymm3, %xmm3
; CHECK-NEXT: vpmovqb %ymm2, %xmm2
; CHECK-NEXT: vpunpckldq {{.*#+}} xmm2 = xmm2[0],xmm3[0],xmm2[1],xmm3[1]
; CHECK-NEXT: vpmovqb %ymm1, %xmm1
; CHECK-NEXT: vpmovqb %ymm0, %xmm0
; CHECK-NEXT: vpunpckldq {{.*#+}} xmm0 = xmm0[0],xmm1[0],xmm0[1],xmm1[1]
; CHECK-NEXT: vpunpckldq {{.*#+}} xmm0 = xmm0[0],xmm2[0]
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%a = load <16 x i64>, <16 x i64>* %x
%b = trunc <16 x i64> %a to <16 x i8>
ret <16 x i8> %b
}

define <16 x i8> @trunc_v16i32_v16i8(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v16i32_v16i8:
; CHECK:    # %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT: vpmovdb %ymm1, %xmm1
; CHECK-NEXT: vpmovdb %ymm0, %xmm0
; CHECK-NEXT: vpunpckldq {{.*#+}} xmm0 = xmm0[0],xmm1[0]
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%a = load <16 x i32>, <16 x i32>* %x
%b = trunc <16 x i32> %a to <16 x i8>
ret <16 x i8> %b
}

```

```

define <8 x i8> @trunc_v8i64_v8i8(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v8i64_v8i8:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:  vpmovqb %ymm1, %xmm1
; CHECK-NEXT:  vpmovqb %ymm0, %xmm0
; CHECK-NEXT:  vpunpckldq {{.*#+}} xmm0 = xmm0[0],xmm1[0],xmm0[1],xmm1[1]
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%a = load <8 x i64>, <8 x i64>* %x
%b = trunc <8 x i64> %a to <8 x i8>
ret <8 x i8> %b
}

```

```

define <8 x i16> @trunc_v8i64_v8i16(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v8i64_v8i16:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:  vpmovqw %ymm1, %xmm1
; CHECK-NEXT:  vpmovqw %ymm0, %xmm0
; CHECK-NEXT:  vpunpckldq {{.*#+}} xmm0 = xmm0[0],xmm1[0]
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%a = load <8 x i64>, <8 x i64>* %x
%b = trunc <8 x i64> %a to <8 x i16>
ret <8 x i16> %b
}

```

```

define <8 x i32> @trunc_v8i64_v8i32_zeroes(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v8i64_v8i32_zeroes:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vpsrlq $48, 32(%rdi), %ymm1
; CHECK-NEXT:  vpsrlq $48, (%rdi), %ymm2
; CHECK-NEXT:  vmovdqa {{.*#+}} ymm0 = [0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30]
; CHECK-NEXT:  vpermi2w %ymm1, %ymm2, %ymm0
; CHECK-NEXT:  retq
%a = load <8 x i64>, <8 x i64>* %x
%b = lshr <8 x i64> %a, <i64 48, i64 48>
%c = trunc <8 x i64> %b to <8 x i32>
ret <8 x i32> %c
}

```

```

define <16 x i16> @trunc_v16i32_v16i16_zeroes(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v16i32_v16i16_zeroes:
; CHECK:    # %bb.0:

```



```

; CHECK-NEXT: vextracti128 $1, %ymm1, %xmm1
; CHECK-NEXT: vpshufd {{.*#+}} xmm3 = xmm1[2,3,0,1]
; CHECK-NEXT: vpmovzxbq {{.*#+}} ymm3 =
xmm3[0],zero,zero,zero,xmm3[1],zero,zero,zero,xmm3[2],zero,zero,zero,xmm3[3],zero,zero,zero
; CHECK-NEXT: vpmovzxbq {{.*#+}} ymm1 =
xmm1[0],zero,zero,zero,xmm1[1],zero,zero,zero,xmm1[2],zero,zero,zero,xmm1[3],zero,zero,zero
; CHECK-NEXT: vpmovzxbq {{.*#+}} ymm0 =
xmm0[0],zero,zero,zero,zero,zero,zero,zero,xmm0[1],zero,zero,zero,zero,zero,zero,zero,xmm0[2],zero,zero,zero,zer
o,zero,zero,zero,xmm0[3],zero,zero,zero,zero,zero,zero,zero
; CHECK-NEXT: vmovdqa %ymm0, (%rdi)
; CHECK-NEXT: vmovdqa %ymm1, 64(%rdi)
; CHECK-NEXT: vmovdqa %ymm3, 96(%rdi)
; CHECK-NEXT: vmovdqa %ymm2, 32(%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%a = zext <16 x i8> %x to <16 x i64>
store <16 x i64> %a, <16 x i64>* %y
ret void
}

```

```

define void @sext_v16i8_v16i64(<16 x i8> %x, <16 x i64>* %y) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: sext_v16i8_v16i64:
; CHECK:    # %bb.0:
; CHECK-NEXT: vpmovsxbw %xmm0, %ymm0
; CHECK-NEXT: vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT: vpmovsxbw %xmm1, %ymm1
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm2
; CHECK-NEXT: vpshufd {{.*#+}} xmm3 = xmm2[2,3,0,1]
; CHECK-NEXT: vpmovsxbw %xmm3, %ymm3
; CHECK-NEXT: vpmovsxbw %xmm0, %ymm0
; CHECK-NEXT: vpmovsxbw %xmm2, %ymm2
; CHECK-NEXT: vmovdqa %ymm2, 64(%rdi)
; CHECK-NEXT: vmovdqa %ymm0, (%rdi)
; CHECK-NEXT: vmovdqa %ymm3, 96(%rdi)
; CHECK-NEXT: vmovdqa %ymm1, 32(%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%a = sext <16 x i8> %x to <16 x i64>
store <16 x i64> %a, <16 x i64>* %y
ret void
}

```

```

define void @vselect_split_v8i16_setcc(<8 x i16> %s, <8 x i16> %t, <8 x i64>* %p, <8 x i64>* %q, <8 x i64>*
%r) "min-legal-vector-width"="256" {
; CHECK-LABEL: vselect_split_v8i16_setcc:
; CHECK:    # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm2
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm3

```

```

; CHECK-NEXT: vpcmpeqw %xmm1, %xmm0, %k1
; CHECK-NEXT: kshiftrb $4, %k1, %k2
; CHECK-NEXT: vmovdqa64 32(%rdi), %ymm3 {%k2}
; CHECK-NEXT: vmovdqa64 (%rdi), %ymm2 {%k1}
; CHECK-NEXT: vmovdqa %ymm2, (%rdx)
; CHECK-NEXT: vmovdqa %ymm3, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <8 x i64>, <8 x i64>* %p
%y = load <8 x i64>, <8 x i64>* %q
%a = icmp eq <8 x i16> %s, %t
%b = select <8 x i1> %a, <8 x i64> %x, <8 x i64> %y
store <8 x i64> %b, <8 x i64>* %r
ret void
}

```

```

define void @vselect_split_v8i32_setcc(<8 x i32> %s, <8 x i32> %t, <8 x i64>* %p, <8 x i64>* %q, <8 x i64>* %r) "min-legal-vector-width"="256" {
; CHECK-LABEL: vselect_split_v8i32_setcc:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm2
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm3
; CHECK-NEXT: vpcmpeqd %ymm1, %ymm0, %k1
; CHECK-NEXT: kshiftrb $4, %k1, %k2
; CHECK-NEXT: vmovdqa64 32(%rdi), %ymm3 {%k2}
; CHECK-NEXT: vmovdqa64 (%rdi), %ymm2 {%k1}
; CHECK-NEXT: vmovdqa %ymm2, (%rdx)
; CHECK-NEXT: vmovdqa %ymm3, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <8 x i64>, <8 x i64>* %p
%y = load <8 x i64>, <8 x i64>* %q
%a = icmp eq <8 x i32> %s, %t
%b = select <8 x i1> %a, <8 x i64> %x, <8 x i64> %y
store <8 x i64> %b, <8 x i64>* %r
ret void
}

```

```

define void @vselect_split_v16i8_setcc(<16 x i8> %s, <16 x i8> %t, <16 x i32>* %p, <16 x i32>* %q, <16 x i32>* %r) "min-legal-vector-width"="256" {
; CHECK-LABEL: vselect_split_v16i8_setcc:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm2
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm3
; CHECK-NEXT: vpcmpeqb %xmm1, %xmm0, %k1
; CHECK-NEXT: kshiftrw $8, %k1, %k2
; CHECK-NEXT: vmovdqa32 32(%rdi), %ymm3 {%k2}
; CHECK-NEXT: vmovdqa32 (%rdi), %ymm2 {%k1}

```

```

; CHECK-NEXT: vmovdqa %ymm2, (%rdx)
; CHECK-NEXT: vmovdqa %ymm3, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <16 x i32>, <16 x i32>* %p
%y = load <16 x i32>, <16 x i32>* %q
%a = icmp eq <16 x i8> %s, %t
%b = select <16 x i1> %a, <16 x i32> %x, <16 x i32> %y
store <16 x i32> %b, <16 x i32>* %r
ret void
}

```

```

define void @vselect_split_v16i16_setcc(<16 x i16> %s, <16 x i16> %t, <16 x i32>* %p, <16 x i32>* %q, <16 x i32>* %r) "min-legal-vector-width"="256" {
; CHECK-LABEL: vselect_split_v16i16_setcc:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm2
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm3
; CHECK-NEXT: vpcmpeqw %ymm1, %ymm0, %k1
; CHECK-NEXT: kshiftrw $8, %k1, %k2
; CHECK-NEXT: vmovdqa32 32(%rdi), %ymm3 {%k2}
; CHECK-NEXT: vmovdqa32 (%rdi), %ymm2 {%k1}
; CHECK-NEXT: vmovdqa %ymm2, (%rdx)
; CHECK-NEXT: vmovdqa %ymm3, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <16 x i32>, <16 x i32>* %p
%y = load <16 x i32>, <16 x i32>* %q
%a = icmp eq <16 x i16> %s, %t
%b = select <16 x i1> %a, <16 x i32> %x, <16 x i32> %y
store <16 x i32> %b, <16 x i32>* %r
ret void
}

```

```

define <16 x i8> @trunc_packus_v16i32_v16i8(<16 x i32>* %p) "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_packus_v16i32_v16i8:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vpackusdw 32(%rdi), %ymm0, %ymm0
; CHECK-NEXT: vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]
; CHECK-NEXT: vpmovuswb %ymm0, %xmm0
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%a = load <16 x i32>, <16 x i32>* %p
%b = icmp slt <16 x i32> %a, <i32 255, i32 255>
%c = select <16 x i1> %b, <16 x i32> %a, <16 x i32> <i32 255, i32 255>

```



```

; CHECK-NEXT:  movq $-5, %rax
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $3, %k0, %k2
; CHECK-NEXT:  kshiftrlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $61, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-9, %rax
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $2, %k0, %k2
; CHECK-NEXT:  kshiftrlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $60, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-17, %rax
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $5, %k0, %k2
; CHECK-NEXT:  kshiftrlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $59, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-33, %rax
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $4, %k0, %k2
; CHECK-NEXT:  kshiftrlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $58, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-65, %rax
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $7, %k0, %k2
; CHECK-NEXT:  kshiftrlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $57, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-129, %rax
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $6, %k0, %k2
; CHECK-NEXT:  kshiftrlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $56, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-257, %rax # imm = 0xFEFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $9, %k0, %k2
; CHECK-NEXT:  kshiftrlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $55, %k2, %k2

```

```

; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-513, %rax # imm = 0xFDFD
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $8, %k0, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $54, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-1025, %rax # imm = 0xFBFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $11, %k0, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $53, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-2049, %rax # imm = 0xF7FF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $10, %k0, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $52, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-4097, %rax # imm = 0xEFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $13, %k0, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $51, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-8193, %rax # imm = 0xDFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $12, %k0, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $50, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-16385, %rax # imm = 0xBFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $15, %k0, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $49, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-32769, %rax # imm = 0xFFFF7FFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $14, %k0, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2

```

```

; CHECK-NEXT: kshiftrq $48, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-65537, %rax # imm = 0xFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $17, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $47, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-131073, %rax # imm = 0xFFFFDFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $16, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $46, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-262145, %rax # imm = 0xFFFFBFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $19, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $45, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-524289, %rax # imm = 0xFFFF7FFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $18, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $44, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-1048577, %rax # imm = 0xFFEFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $21, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $43, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-2097153, %rax # imm = 0xFFDFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $20, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $42, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-4194305, %rax # imm = 0xFFBFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $23, %k0, %k2

```

```

; CHECK-NEXT: kshiflq $63, %k2, %k2
; CHECK-NEXT: kshifrq $41, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-8388609, %rax # imm = 0xFF7FFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshifrd $22, %k0, %k2
; CHECK-NEXT: kshiflq $63, %k2, %k2
; CHECK-NEXT: kshifrq $40, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-16777217, %rax # imm = 0xFEFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshifrd $25, %k0, %k2
; CHECK-NEXT: kshiflq $63, %k2, %k2
; CHECK-NEXT: kshifrq $39, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-33554433, %rax # imm = 0xFDFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshifrd $24, %k0, %k2
; CHECK-NEXT: kshiflq $63, %k2, %k2
; CHECK-NEXT: kshifrq $38, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-67108865, %rax # imm = 0xFBFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshifrd $27, %k0, %k2
; CHECK-NEXT: kshiflq $63, %k2, %k2
; CHECK-NEXT: kshifrq $37, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-134217729, %rax # imm = 0xF7FFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshifrd $26, %k0, %k2
; CHECK-NEXT: kshiflq $63, %k2, %k2
; CHECK-NEXT: kshifrq $36, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-268435457, %rax # imm = 0xEFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshifrd $29, %k0, %k2
; CHECK-NEXT: kshiflq $63, %k2, %k2
; CHECK-NEXT: kshifrq $35, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-536870913, %rax # imm = 0xDFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1

```

```

; CHECK-NEXT: kshiftrd $28, %k0, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $34, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-1073741825, %rax # imm = 0xBFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $31, %k0, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $33, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movabsq $-2147483649, %rax # imm = 0xFFFFFFFF7FFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k2
; CHECK-NEXT: vptestnmb %ymm0, %ymm0, %k1
; CHECK-NEXT: kshiftrd $30, %k0, %k0
; CHECK-NEXT: kshiftrlq $63, %k0, %k0
; CHECK-NEXT: kshiftrq $32, %k0, %k0
; CHECK-NEXT: korq %k0, %k2, %k0
; CHECK-NEXT: movabsq $-4294967297, %rax # imm = 0xFFFFFFFFEFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $1, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $31, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-8589934593, %rax # imm = 0xFFFFFFFFDFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrlq $63, %k1, %k2
; CHECK-NEXT: kshiftrq $30, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-17179869185, %rax # imm = 0xFFFFFFFFBFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $3, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $29, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-34359738369, %rax # imm = 0xFFFFFFFF7FFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $2, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $28, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-68719476737, %rax # imm = 0xFFFFFFFFEFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2

```

```

; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $5, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $27, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-137438953473, %rax # imm = 0xFFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $4, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $26, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-274877906945, %rax # imm = 0xFFFFFFFFBFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $7, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $25, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-549755813889, %rax # imm = 0xFFFFFFFF7FFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $6, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $24, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-1099511627777, %rax # imm = 0xFFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $9, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $23, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-2199023255553, %rax # imm = 0xFFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $8, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $22, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-4398046511105, %rax # imm = 0xFFFFFFFFBFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $11, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $21, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-8796093022209, %rax # imm = 0xFFFFFFFF7FFFFFFFFF

```

```

; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $10, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $20, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-17592186044417, %rax # imm = 0xFFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $13, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $19, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-35184372088833, %rax # imm = 0xFFFFDFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $12, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $18, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-70368744177665, %rax # imm = 0xFFFFBFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $15, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $17, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-140737488355329, %rax # imm = 0xFFFF7FFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $14, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $16, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-281474976710657, %rax # imm = 0xFFFEFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $17, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $15, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-562949953421313, %rax # imm = 0xFFFDFFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $16, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $14, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0

```

```

; CHECK-NEXT: movabsq $-1125899906842625, %rax # imm = 0xFFFFBFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $19, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $13, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-2251799813685249, %rax # imm = 0xFFF7FFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $18, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $12, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-4503599627370497, %rax # imm = 0xFFEFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $21, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $11, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-9007199254740993, %rax # imm = 0xFFDFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $20, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $10, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-18014398509481985, %rax # imm = 0xFFBFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $23, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $9, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-36028797018963969, %rax # imm = 0xFF7FFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $22, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $8, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-72057594037927937, %rax # imm = 0xFEFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $25, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $7, %k2, %k2

```

```

; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-144115188075855873, %rax # imm = 0xFDFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $24, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $6, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-288230376151711745, %rax # imm = 0xFBFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $27, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $5, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-576460752303423489, %rax # imm = 0xF7FFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $26, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $4, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-1152921504606846977, %rax # imm = 0xEFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $29, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $3, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-2305843009213693953, %rax # imm = 0xDFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $28, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $2, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-4611686018427387905, %rax # imm = 0xBF7FFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $31, %k1, %k2
; CHECK-NEXT: kshiftrlq $62, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $30, %k1, %k1
; CHECK-NEXT: kshiftrlq $1, %k0, %k0
; CHECK-NEXT: kshiftrq $1, %k0, %k0
; CHECK-NEXT: kshiftrlq $63, %k1, %k1
; CHECK-NEXT: korq %k1, %k0, %k1
; CHECK-NEXT: vmovdqu8 %ymm1, (%rsi) {%k1}

```

```

; CHECK-NEXT: kshiftrq $32, %k1, %k1
; CHECK-NEXT: vmovdqu8 %ymm0, 32(%rsi) {%k1}
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
entry:
%a = load <64 x i8>, <64 x i8>* %x
%b = icmp eq <64 x i8> %a, zeroinitializer
%shuf = shufflevector <64 x i1> %b, <64 x i1> undef, <64 x i32> <i32 1, i32 0, i32 3, i32 2, i32 5, i32 4, i32 7, i32
6, i32 9, i32 8, i32 11, i32 10, i32 13, i32 12, i32 15, i32 14, i32 17, i32 16, i32 19, i32 18, i32 21, i32 20, i32 23, i32
22, i32 25, i32 24, i32 27, i32 26, i32 29, i32 28, i32 31, i32 30, i32 33, i32 32, i32 35, i32 34, i32 37, i32 36, i32 39,
i32 38, i32 41, i32 40, i32 43, i32 42, i32 45, i32 44, i32 47, i32 46, i32 49, i32 48, i32 51, i32 50, i32 53, i32 52, i32
55, i32 54, i32 57, i32 56, i32 59, i32 58, i32 61, i32 60, i32 63, i32 62>
call void @llvm.masked.store.v64i8.p0v64i8(<64 x i8> %a, <64 x i8>* %y, i32 1, <64 x i1> %shuf)
ret void
}
declare void @llvm.masked.store.v64i8.p0v64i8(<64 x i8>, <64 x i8>*, i32, <64 x i1>)

```

```

@mem64_dst = global i64 0, align 8
@mem64_src = global i64 0, align 8
define i32 @v64i1_inline_asm() "min-legal-vector-width"="256" {
; CHECK-LABEL: v64i1_inline_asm:
; CHECK: # %bb.0:
; CHECK-NEXT: kmovq {{.*}}(%rip), %k0
; CHECK-NEXT: #APP
; CHECK-NEXT: #NO_APP
; CHECK-NEXT: kmovq %k0, {{.*}}(%rip)
; CHECK-NEXT: movl -{[0-9]+}(%rsp), %eax
; CHECK-NEXT: retq
%1 = alloca i32, align 4
%2 = load i64, i64* @mem64_src, align 8
%3 = call i64 @asm "", "=k,k,~{dirflag},~{fpsr},~{flags}"(i64 %2)
store i64 %3, i64* @mem64_dst, align 8
%4 = load i32, i32* %1, align 4
ret i32 %4
}

```

```

define void @cmp_v8i64_sext(<8 x i64>* %xptr, <8 x i64>* %yptr, <8 x i64>* %zptr) "min-legal-vector-
width"="256" {
; CHECK-LABEL: cmp_v8i64_sext:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm1
; CHECK-NEXT: vpcmpgtq 32(%rdi), %ymm1, %ymm1
; CHECK-NEXT: vpcmpgtq (%rdi), %ymm0, %ymm0
; CHECK-NEXT: vmovdqa %ymm0, (%rdx)
; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq

```

```

%x = load <8 x i64>, <8 x i64>* %xptr
%y = load <8 x i64>, <8 x i64>* %yptr
%cmp = icmp slt <8 x i64> %x, %y
%ext = sext <8 x i1> %cmp to <8 x i64>
store <8 x i64> %ext, <8 x i64>* %zptr
ret void
}

```

```

define void @cmp_v8i64_zext(<8 x i64>* %xptr, <8 x i64>* %yptr, <8 x i64>* %zptr) "min-legal-vector-width"="256" {
; CHECK-LABEL: cmp_v8i64_zext:
; CHECK:      # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm1
; CHECK-NEXT: vpcmpgtq 32(%rdi), %ymm1, %ymm1
; CHECK-NEXT: vpcmpgtq (%rdi), %ymm0, %ymm0
; CHECK-NEXT: vpsrlq $63, %ymm1, %ymm1
; CHECK-NEXT: vpsrlq $63, %ymm0, %ymm0
; CHECK-NEXT: vmovdqa %ymm0, (%rdx)
; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <8 x i64>, <8 x i64>* %xptr
%y = load <8 x i64>, <8 x i64>* %yptr
%cmp = icmp slt <8 x i64> %x, %y
%ext = zext <8 x i1> %cmp to <8 x i64>
store <8 x i64> %ext, <8 x i64>* %zptr
ret void
}

```

```

define <16 x i8> @var_rotate_v16i8(<16 x i8> %a, <16 x i8> %b) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: var_rotate_v16i8:
; CHECK:      # %bb.0:
; CHECK-NEXT: vmovdqa {{.*#+}} xmm2 = [8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8]
; CHECK-NEXT: vpsubb %xmm1, %xmm2, %xmm2
; CHECK-NEXT: vpmovzxbw {{.*#+}} ymm1 =
xmm1[0],zero,xmm1[1],zero,xmm1[2],zero,xmm1[3],zero,xmm1[4],zero,xmm1[5],zero,xmm1[6],zero,xmm1[7],zer
o,xmm1[8],zero,xmm1[9],zero,xmm1[10],zero,xmm1[11],zero,xmm1[12],zero,xmm1[13],zero,xmm1[14],zero,xm
m1[15],zero
; CHECK-NEXT: vpmovzxbw {{.*#+}} ymm0 =
xmm0[0],zero,xmm0[1],zero,xmm0[2],zero,xmm0[3],zero,xmm0[4],zero,xmm0[5],zero,xmm0[6],zero,xmm0[7],zer
o,xmm0[8],zero,xmm0[9],zero,xmm0[10],zero,xmm0[11],zero,xmm0[12],zero,xmm0[13],zero,xmm0[14],zero,xm
m0[15],zero
; CHECK-NEXT: vpsllvw %ymm1, %ymm0, %ymm1
; CHECK-NEXT: vpmovzxbw {{.*#+}} ymm2 =
xmm2[0],zero,xmm2[1],zero,xmm2[2],zero,xmm2[3],zero,xmm2[4],zero,xmm2[5],zero,xmm2[6],zero,xmm2[7],zer
o,xmm2[8],zero,xmm2[9],zero,xmm2[10],zero,xmm2[11],zero,xmm2[12],zero,xmm2[13],zero,xmm2[14],zero,xm
m2[15],zero

```



```

ymm0[0],ymm2[0],ymm0[1],ymm2[1],ymm0[2],ymm2[2],ymm0[3],ymm2[3],ymm0[4],ymm2[4],ymm0[5],ymm2[
5],ymm0[6],ymm2[6],ymm0[7],ymm2[7],ymm0[16],ymm2[16],ymm0[17],ymm2[17],ymm0[18],ymm2[18],ymm0[
19],ymm2[19],ymm0[20],ymm2[20],ymm0[21],ymm2[21],ymm0[22],ymm2[22],ymm0[23],ymm2[23]
; CHECK-AVX512-NEXT: vpsllvw {{.*}}(%rip), %ymm0, %ymm0
; CHECK-AVX512-NEXT: vpsrlw $8, %ymm0, %ymm0
; CHECK-AVX512-NEXT: vpackuswb %ymm3, %ymm0, %ymm0
; CHECK-AVX512-NEXT: vpor %ymm0, %ymm1, %ymm0
; CHECK-AVX512-NEXT: retq
;
; CHECK-VBMI-LABEL: constant_rotate_v32i8:
; CHECK-VBMI: # %bb.0:
; CHECK-VBMI-NEXT: vpsllw $4, %ymm0, %ymm1
; CHECK-VBMI-NEXT: vpand {{.*}}(%rip), %ymm1, %ymm1
; CHECK-VBMI-NEXT: vbroadcasti128 {{.*#+}} ymm2 =
[8192,24640,41088,57536,57344,41152,24704,8256,8192,24640,41088,57536,57344,41152,24704,8256]
; CHECK-VBMI-NEXT: # ymm2 = mem[0,1,0,1]
; CHECK-VBMI-NEXT: vpbldvb %ymm2, %ymm1, %ymm0, %ymm1
; CHECK-VBMI-NEXT: vpsllw $2, %ymm1, %ymm3
; CHECK-VBMI-NEXT: vpand {{.*}}(%rip), %ymm3, %ymm3
; CHECK-VBMI-NEXT: vpaddb %ymm2, %ymm2, %ymm2
; CHECK-VBMI-NEXT: vpbldvb %ymm2, %ymm3, %ymm1, %ymm1
; CHECK-VBMI-NEXT: vpaddb %ymm1, %ymm1, %ymm3
; CHECK-VBMI-NEXT: vpaddb %ymm2, %ymm2, %ymm2
; CHECK-VBMI-NEXT: vpbldvb %ymm2, %ymm3, %ymm1, %ymm1
; CHECK-VBMI-NEXT: vpxor %xmm2, %xmm2, %xmm2
; CHECK-VBMI-NEXT: vpunpckhbw {{.*#+}} ymm3 =
ymm0[8],ymm2[8],ymm0[9],ymm2[9],ymm0[10],ymm2[10],ymm0[11],ymm2[11],ymm0[12],ymm2[12],ymm0[13
],ymm2[13],ymm0[14],ymm2[14],ymm0[15],ymm2[15],ymm0[24],ymm2[24],ymm0[25],ymm2[25],ymm0[26],ym
m2[26],ymm0[27],ymm2[27],ymm0[28],ymm2[28],ymm0[29],ymm2[29],ymm0[30],ymm2[30],ymm0[31],ymm2[
31]
; CHECK-VBMI-NEXT: vpsllvw {{.*}}(%rip), %ymm3, %ymm3
; CHECK-VBMI-NEXT: vpunpcklbw {{.*#+}} ymm0 =
ymm0[0],ymm2[0],ymm0[1],ymm2[1],ymm0[2],ymm2[2],ymm0[3],ymm2[3],ymm0[4],ymm2[4],ymm0[5],ymm2[
5],ymm0[6],ymm2[6],ymm0[7],ymm2[7],ymm0[16],ymm2[16],ymm0[17],ymm2[17],ymm0[18],ymm2[18],ymm0[
19],ymm2[19],ymm0[20],ymm2[20],ymm0[21],ymm2[21],ymm0[22],ymm2[22],ymm0[23],ymm2[23]
; CHECK-VBMI-NEXT: vpsllvw {{.*}}(%rip), %ymm0, %ymm0
; CHECK-VBMI-NEXT: vmovdqa {{.*#+}} ymm2 =
[1,3,5,7,9,11,13,15,33,35,37,39,41,43,45,47,17,19,21,23,25,27,29,31,49,51,53,55,57,59,61,63]
; CHECK-VBMI-NEXT: vpermi2b %ymm3, %ymm0, %ymm2
; CHECK-VBMI-NEXT: vpor %ymm2, %ymm1, %ymm0
; CHECK-VBMI-NEXT: retq
%shl = shl <32 x i8> %a, <i8 0, i8 1, i8 2, i8 3, i8 4, i8 5, i8 6, i8 7, i8 8, i8 7, i8 6, i8 5, i8 4, i8 3, i8 2, i8 1, i8 0, i8
1, i8 2, i8 3, i8 4, i8 5, i8 6, i8 7, i8 8, i8 7, i8 6, i8 5, i8 4, i8 3, i8 2, i8 1>
%lshr = lshr <32 x i8> %a, <i8 8, i8 7, i8 6, i8 5, i8 4, i8 3, i8 2, i8 1, i8 0, i8 1, i8 2, i8 3, i8 4, i8 5, i8 6, i8 7, i8 8,
i8 7, i8 6, i8 5, i8 4, i8 3, i8 2, i8 1, i8 0, i8 1, i8 2, i8 3, i8 4, i8 5, i8 6, i8 7>
%or = or <32 x i8> %shl, %lshr
ret <32 x i8> %or
}

```


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```
add_lldb_library(lldbPluginObjectContainerBSDArchive PLUGIN
ObjectContainerBSDArchive.cpp
```

```
LINK_LIBS
```

```
  lldbCore
```

```
  lldbHost
```

```
  lldbSymbol
```

```
LINK_COMPONENTS
```

```
  Support
```

```
)
```

```
# People who have agreed to one of the CLAs and can contribute patches.
```

```
# The AUTHORS file lists the copyright holders; this file
# lists people. For example, Google employees are listed here
# but not in AUTHORS, because Google holds the copyright.
#
```

```
# Names should be added to this file only after verifying that
# the individual or the individual's organization has agreed to
# the appropriate Contributor License Agreement, found here:
#
```

```
# https://developers.google.com/open-source/cla/individual
```

```
# https://developers.google.com/open-source/cla/corporate
```

```
#
```

```
# The agreement for individuals can be filled out on the web.
```

```
#
```

```
# When adding J Random Contributor's name to this file,
# either J's name or J's organization's name should be
# added to the AUTHORS file, depending on whether the
```

individual or corporate CLA was used.

#

Names should be added to this file as:

Name <email address>

#

Please keep the list sorted.

Albert Pretorius <pretoalb@gmail.com>
Arne Beer <arne@twobeer.de>
Billy Robert O'Neal III <billy.oneal@gmail.com> <bion@microsoft.com>
Chris Kennelly <ckennelly@google.com> <ckennelly@ckennelly.com>
Christopher Seymour <chris.j.seymour@hotmail.com>
Cyrille Fauchaux <cyrille.fauchaux@gmail.com>
David Coeurjolly <david.coeurjolly@liris.cnrs.fr>
Deniz Evrenci <denizevrenci@gmail.com>
Dominic Hamon <dma@stripysock.com> <dominic@google.com>
Dominik Czarnota <dominik.b.czarnota@gmail.com>
Eric Fiselier <eric@efcs.ca>
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Matt Clarkson <mattyclarkson@gmail.com>
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Ori Livneh <ori.livneh@gmail.com>
Pascal Leroy <phl@google.com>
Paul Redmond <paul.redmond@gmail.com>
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Roman Lebedev <lebedev.ri@gmail.com>
Shuo Chen <chenshuo@chenshuo.com>
Tobias Ulvgrd <tobias.ulvgard@dirac.se>
Tom Madams <tom.ej.madams@gmail.com> <tmadams@google.com>

Yixuan Qiu <yixuanq@gmail.com>
Yusuke Suzuki <utatane.tea@gmail.com>
Zbigniew Skowron <zbychs@gmail.com>
; RUN: llc -march=hexagon < %s
; REQUIRES: asserts

; The two loads based on %struct.0, loading two different data types
; cause LSR to assume type "void" for the memory type. This would then
; cause an assert in isLegalAddressingMode. Make sure we no longer crash.

target triple = "hexagon"

%struct.0 = type { i8*, i8, %union.anon.0 }
%union.anon.0 = type { i8* }

define hidden fastcc void @fred() unnamed_addr #0 {
entry:

br i1 undef, label %while.end, label %while.body.lr.ph

while.body.lr.ph: ; preds = %entry
br label %while.body

while.body: ; preds = %exit.2, %while.body.lr.ph
%lshr.iv = phi %struct.0* [%cgep22, %exit.2], [undef, %while.body.lr.ph]
switch i32 undef, label %exit [
i32 1, label %sw.bb.i
i32 2, label %sw.bb3.i
]

sw.bb.i: ; preds = %while.body
unreachable

sw.bb3.i: ; preds = %while.body
unreachable

exit: ; preds = %while.body
switch i32 undef, label %exit.2 [
i32 1, label %sw.bb.i17
i32 2, label %sw.bb3.i20
]

sw.bb.i17: ; preds = %exit
%0 = bitcast %struct.0* %lshr.iv to i32*
%1 = load i32, i32* %0, align 4
unreachable

sw.bb3.i20: ; preds = %exit
%2 = bitcast %struct.0* %lshr.iv to i8**

```

%3 = load i8*, i8** %2, align 4
unreachable

exit.2:                                ; preds = %exit
%cgcp22 = getelementptr %struct.0, %struct.0* %lsr.iv, i32 1
br label %while.body

while.end:                              ; preds = %entry
ret void
}

attributes #0 = { nounwind optsize "target-cpu"="hexagonv55" }
; NOTE: Assertions have been autogenerated by utils/update_test_checks.py UTC_ARGS: --function-signature --
scrub-attributes
; RUN: opt -attributor -attributor-manifest-internal -attributor-max-iterations-verify -attributor-annotate-decl-cs -
attributor-max-iterations=4 -S < %s | FileCheck %s --check-
prefixes=CHECK,NOT_CGSCC_NPM,NOT_CGSCC_OPM,NOT_TUNIT_NPM,IS__TUNIT____,IS_____OP
M,IS__TUNIT_OPM
; RUN: opt -aa-pipeline=basic-aa -passes=attributor -attributor-manifest-internal -attributor-max-iterations-verify -
attributor-annotate-decl-cs -attributor-max-iterations=3 -S < %s | FileCheck %s --check-
prefixes=CHECK,NOT_CGSCC_OPM,NOT_CGSCC_NPM,NOT_TUNIT_OPM,IS__TUNIT____,IS_____NP
M,IS__TUNIT_NPM
; RUN: opt -attributor-cgsc -attributor-manifest-internal -attributor-annotate-decl-cs -S < %s | FileCheck %s --
check-
prefixes=CHECK,NOT_TUNIT_NPM,NOT_TUNIT_OPM,NOT_CGSCC_NPM,IS__CGSCC____,IS_____OP
M,IS__CGSCC_OPM
; RUN: opt -aa-pipeline=basic-aa -passes=attributor-cgsc -attributor-manifest-internal -attributor-annotate-decl-cs -
S < %s | FileCheck %s --check-
prefixes=CHECK,NOT_TUNIT_NPM,NOT_TUNIT_OPM,NOT_CGSCC_OPM,IS__CGSCC____,IS_____NP
M,IS__CGSCC_NPM
; Test that we only promote arguments when the caller/callee have compatible
; function attributes.

target triple = "x86_64-unknown-linux-gnu"

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #0 {
;
; NOT_TUNIT_NPM-LABEL: define
{[[@+]}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void
;

```

```

; IS__TUNIT_NPM-LABEL: define
{[^[^@]+]}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias norecurse noreturn writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg) #0 {
;
; IS__TUNIT_OPM-LABEL: define {[^[^@]+]}@avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS__TUNIT_OPM-SAME: (<8 x i64>* norecurse writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* norecurse writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* norecurse noreturn
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* norecurse noreturn readonly align 64
dereferenceable(64) [[TMP]])
; IS__TUNIT_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_OPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define {[^[^@]+]}@avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS__TUNIT_NPM-SAME: (<8 x i64>* norecurse writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* norecurse writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* noalias norecurse
noreturn writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]])
; IS__TUNIT_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64

```

```

; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void
;
; IS__CGSCC_OPM-LABEL: define {{{^@+}} @avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void
;
; IS__CGSCC_NPM-LABEL: define {{{^@+}} @avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly
align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT: ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

```

```

; This should promote

```

```

define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #1 {
;
; NOT_TUNIT_NPM-LABEL: define
{{{^@+}}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void
;
; IS_TUNIT_NPM-LABEL: define
{{{^@+}}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
; IS_TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
; IS_TUNIT_NPM-NEXT: bb:
; IS_TUNIT_NPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS_TUNIT_NPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS_TUNIT_NPM-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #1 {
;
; IS_TUNIT_OPM-LABEL: define {{{^@+}}}@avx512_legal512_prefer256_call_avx512_legal512_prefer256
; IS_TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS_TUNIT_OPM-NEXT: bb:
; IS_TUNIT_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS_TUNIT_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS_TUNIT_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS_TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS_TUNIT_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS_TUNIT_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS_TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS_TUNIT_OPM-NEXT: ret void
;
; IS_TUNIT_NPM-LABEL: define {{{^@+}}}@avx512_legal512_prefer256_call_avx512_legal512_prefer256

```

```

; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]])
; IS__TUNIT_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void
;
; IS__CGSCC_OPM-LABEL: define {{{^@+}}} @avx512_legal512_prefer256_call_avx512_legal512_prefer256
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void
;
; IS__CGSCC_NPM-LABEL: define {{{^@+}}} @avx512_legal512_prefer256_call_avx512_legal512_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly
align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT: ret void
;
bb:

```

```

%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

```

; This should promote

```

define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #1 {

```

```

;
; NOT_TUNIT_NPM-LABEL: define
{[^[^@]+]}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define
{[^[^@]+]}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

```

```

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg) #0 {
;
; IS__TUNIT_OPM-LABEL: define {[^[^@]+]}@avx512_legal512_prefer512_call_avx512_legal512_prefer256
; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32

```

```

; IS__TUNIT_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__TUNIT_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_OPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define {{{^@+}}}@avx512_legal512_prefer512_call_avx512_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]])
; IS__TUNIT_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void
;
; IS__CGSCC_OPM-LABEL: define {{{^@+}}}@avx512_legal512_prefer512_call_avx512_legal512_prefer256
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void
;
; IS__CGSCC_NPM-LABEL: define {{{^@+}}}@avx512_legal512_prefer512_call_avx512_legal512_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32

```

```

; IS__CGSCC_NPM-NEXT:  [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT:  call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT:  call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly
align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT:  ret void
;
bb:
  %tmp = alloca <8 x i64>, align 32
  %tmp2 = alloca <8 x i64>, align 32
  %tmp3 = bitcast <8 x i64>* %tmp to i8*
  call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
  call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
  %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
  store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
  ret void
}

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #0 {
;
; NOT_TUNIT_NPM-LABEL: define
{{{^@+}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT:  [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT:  store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT:  ret void
;
; IS__TUNIT_NPM-LABEL: define
{{{^@+}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT:  [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_NPM-NEXT:  store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT:  [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT:  store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT:  ret void
;
bb:

```

```

%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg) #1 {
;
; IS__TUNIT_OPM-LABEL: define {{{^@}+})@avx512_legal512_prefer256_call_avx512_legal512_prefer512
; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__TUNIT_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_OPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define {{{^@}+})@avx512_legal512_prefer256_call_avx512_legal512_prefer512
; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]])
; IS__TUNIT_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void
;
; IS__CGSCC_OPM-LABEL: define {{{^@}+})@avx512_legal512_prefer256_call_avx512_legal512_prefer512
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void

```

```

@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void
;
; IS__CGSCC_NPM-LABEL: define {{{^@}+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer512
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly
align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT: ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #1 {
;
; NOT_TUNIT_NPM-LABEL: define
{{{^@}+}}@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void
;

```

```

; IS__TUNIT_NPM-LABEL: define
{[^[^@]+]}@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias noreturn noreturn writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>* noalias noreturn noreturn readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #2 {
;
; IS__TUNIT_OPM-LABEL: define {[^[^@]+]}@avx512_legal256_prefer256_call_avx512_legal512_prefer256
; IS__TUNIT_OPM-SAME: (<8 x i64>* noreturn writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* noreturn noreturn writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT: call fastcc void
@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* noreturn noreturn writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noreturn noreturn readonly align 64 dereferenceable(64) [[TMP]])
; IS__TUNIT_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_OPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define {[^[^@]+]}@avx512_legal256_prefer256_call_avx512_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* noreturn writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* noreturn noreturn writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: call fastcc void
@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* noalias noreturn noreturn writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias noreturn noreturn readonly align 64 dereferenceable(64) [[TMP]])
; IS__TUNIT_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void

```

```

;
; IS__CGSCC_OPM-LABEL: define {{^[^@]+}} @avx512_legal256_prefer256_call_avx512_legal512_prefer256
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void
@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* nocapture nfree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nfree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void
;
; IS__CGSCC_NPM-LABEL: define {{^[^@]+}} @avx512_legal256_prefer256_call_avx512_legal512_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT: call fastcc void
@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nfree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nfree nonnull readonly
align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT: ret void
;
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #2 {

```

```

;
; NOT_TUNIT_NPM-LABEL: define
{[^(+)]}@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT:  [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT:  store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT:  ret void
;
; IS_TUNIT_NPM-LABEL: define
{[^(+)]}@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256
; IS_TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; IS_TUNIT_NPM-NEXT: bb:
; IS_TUNIT_NPM-NEXT:  [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; IS_TUNIT_NPM-NEXT:  store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS_TUNIT_NPM-NEXT:  ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg) #1 {
;
; IS_TUNIT_OPM-LABEL: define {[^(+)]}@avx512_legal512_prefer256_call_avx512_legal256_prefer256
; IS_TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS_TUNIT_OPM-NEXT: bb:
; IS_TUNIT_OPM-NEXT:  [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS_TUNIT_OPM-NEXT:  [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS_TUNIT_OPM-NEXT:  [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS_TUNIT_OPM-NEXT:  call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS_TUNIT_OPM-NEXT:  call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS_TUNIT_OPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS_TUNIT_OPM-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS_TUNIT_OPM-NEXT:  ret void
;
; IS_TUNIT_NPM-LABEL: define {[^(+)]}@avx512_legal512_prefer256_call_avx512_legal256_prefer256
; IS_TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS_TUNIT_NPM-NEXT: bb:
; IS_TUNIT_NPM-NEXT:  [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS_TUNIT_NPM-NEXT:  [[TMP2:%.*]] = alloca <8 x i64>, align 32

```

```

; IS__TUNIT_NPM-NEXT:  [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT:  call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT:  call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly
align 64 dereferenceable(64) [[TMP]])
; IS__TUNIT_NPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT:  ret void
;
; IS__CGSCC_OPM-LABEL: define {{[^\@]+}} @avx512_legal512_prefer256_call_avx512_legal256_prefer256
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT:  [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT:  [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT:  [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT:  call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT:  call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT:  ret void
;
; IS__CGSCC_NPM-LABEL: define {{[^\@]+}} @avx512_legal512_prefer256_call_avx512_legal256_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT:  [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:  [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:  [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT:  call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT:  call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly
align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT:  ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)

```

```

call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

```

; This should promote

```

define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8
x i64>* readonly %arg1) #3 {

```

```

;

```

```

; NOT_TUNIT_NPM-LABEL: define

```

```

{[^[^@]+]}@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256

```

```

; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64)

```

```

[[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])

```

```

; NOT_TUNIT_NPM-NEXT: bb:

```

```

; NOT_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64

```

```

; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64

```

```

; NOT_TUNIT_NPM-NEXT: ret void

```

```

;

```

```

; IS__TUNIT_NPM-LABEL: define {[^[^@]+]}@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256

```

```

; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64)

```

```

[[ARG:%.*]], <8 x i64> [[TMP0:%.*]])

```

```

; IS__TUNIT_NPM-NEXT: bb:

```

```

; IS__TUNIT_NPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64

```

```

; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64

```

```

; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64

```

```

; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64

```

```

; IS__TUNIT_NPM-NEXT: ret void

```

```

;

```

```

bb:

```

```

%tmp = load <8 x i64>, <8 x i64>* %arg1

```

```

store <8 x i64> %tmp, <8 x i64>* %arg

```

```

ret void

```

```

}

```

```

define void @avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg) #4 {

```

```

;

```

```

; IS__TUNIT_OPM-LABEL: define {[^[^@]+]}@avx2_legal256_prefer256_call_avx2_legal512_prefer256

```

```

; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])

```

```

; IS__TUNIT_OPM-NEXT: bb:

```

```

; IS__TUNIT_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32

```

```

; IS__TUNIT_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32

```

```

; IS__TUNIT_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*

```

```

; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)

```

```

; IS__TUNIT_OPM-NEXT: call fastcc void

```

```

@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* nocapture nofree nonnull writeonly

```

```

align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64)
[[TMP]])
; IS__TUNIT_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_OPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define {{{^@+}}@avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT: call fastcc void
@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* noalias nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]])
; IS__TUNIT_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void
;
; IS__CGSCC_OPM-LABEL: define {{{^@+}}@avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void
@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* nocapture nofree nonnull writeonly
align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64)
[[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void
;
; IS__CGSCC_NPM-LABEL: define {{{^@+}}@avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT: call fastcc void
@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* noalias nocapture nofree nonnull

```

```

writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT:  ret void
;
bb:
  %tmp = alloca <8 x i64>, align 32
  %tmp2 = alloca <8 x i64>, align 32
  %tmp3 = bitcast <8 x i64>* %tmp to i8*
  call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
  call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>*
%tmp)
  %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
  store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
  ret void
}

; This should promote
define internal fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg, <8
x i64>* readonly %arg1) #4 {
;
; NOT_TUNIT_NPM-LABEL: define
{[[@+]]}@callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT:  [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT:  store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT:  ret void
;
; IS__TUNIT_NPM-LABEL: define {[[@+]]}@callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT:  [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_NPM-NEXT:  store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT:  [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT:  store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT:  ret void
;
bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg) #3 {

```

```

;
; IS__TUNIT_OPM-LABEL: define {{^[^@]+}}@avx2_legal512_prefer256_call_avx2_legal256_prefer256
; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT: call fastcc void
@callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* nocapture nofree nonnull writeonly
align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64)
[[TMP]])
; IS__TUNIT_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_OPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define {{^[^@]+}}@avx2_legal512_prefer256_call_avx2_legal256_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT: call fastcc void
@callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* noalias nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]])
; IS__TUNIT_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void
;
; IS__CGSCC_OPM-LABEL: define {{^[^@]+}}@avx2_legal512_prefer256_call_avx2_legal256_prefer256
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void
@callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* nocapture nofree nonnull writeonly
align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64)
[[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void

```

```

;
; IS__CGSCC_NPM-LABEL: define {{[^\@+]+}}@avx2_legal512_prefer256_call_avx2_legal256_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT: call fastcc void
@callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* noalias nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT: ret void
;
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>*
%tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; Function Attrs: argmemonly nounwind
declare void @llvm.memset.p0i8.i64(i8* nocapture writeonly, i8, i64, i1) #5

attributes #0 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-
width"="512" "prefer-vector-width"="512" }
attributes #1 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-
width"="512" "prefer-vector-width"="256" }
attributes #2 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-
width"="256" "prefer-vector-width"="256" }
attributes #3 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="512"
"prefer-vector-width"="256" }
attributes #4 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="256"
"prefer-vector-width"="256" }
attributes #5 = { argmemonly nounwind }

```

```

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LLVM Team

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```
; RUN: llc -O3 -mtriple=powerpc-unknown-linux-gnu -mcpu=e500 -mattr=spe < %s | FileCheck %s
```

```
; PowerPC SPE is a rare in-tree target that has the FP_TO_SINT node marked  
; as Legal.
```

```
; Verify that fptosi(42.1) isn't simplified when the rounding mode is  
; unknown.  
; Verify that no gross errors happen.  
; CHECK-LABEL: @f20  
; COMMON: cfdtsiz  
define i32 @f20(double %a) strictfp {  
entry:  
  %result = call i32 @llvm.experimental.constrained.fptosi.i32.f64(double 42.1,  
    metadata !"fpexcept.strict")  
    strictfp  
  ret i32 %result  
}
```

```
@llvm.fp.env = thread_local global i8 zeroinitializer, section "llvm.metadata"  
declare i32 @llvm.experimental.constrained.fptosi.i32.f64(double, metadata)
```

```
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```
=====
; NOTE: Assertions have been autogenerated by utils/update_analyze_test_checks.py
; RUN: opt < %s -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx2 | FileCheck %s --check-
prefixes=CHECK,VEC256,AVX
; RUN: opt < %s -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+prefer-256-bit |
FileCheck %s --check-prefixes=CHECK,VEC256,AVX512VL256
; RUN: opt < %s -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,-prefer-256-bit |
FileCheck %s --check-prefixes=CHECK,VEC512,AVX512VL512
; RUN: opt < %s -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -
mattr=+avx512vl,+avx512bw,+avx512dq,+prefer-256-bit | FileCheck %s --check-
prefixes=CHECK,VEC256,SKX256
; RUN: opt < %s -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -
mattr=+avx512vl,+avx512bw,+avx512dq,-prefer-256-bit | FileCheck %s --check-
prefixes=CHECK,VEC512,SKX512
```

```
define void @zext256() "min-legal-vector-width"="256" {
; VEC256-LABEL: 'zext256'
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = zext <8 x i16> undef to <8 x
i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x
i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x
i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %D = zext <16 x i16> undef to <16 x
i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x
i16>
; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL512-LABEL: 'zext256'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to
<8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to
<8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to
<16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to
<16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to
<32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX512-LABEL: 'zext256'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x
```

```

i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x
i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x
i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x
i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x
i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
%A = zext <8 x i16> undef to <8 x i64>
%B = zext <8 x i32> undef to <8 x i64>
%C = zext <16 x i8> undef to <16 x i32>
%D = zext <16 x i16> undef to <16 x i32>
%E = zext <32 x i8> undef to <32 x i16>
ret void
}

```

```

define void @zext512() "min-legal-vector-width"="512" {
; AVX-LABEL: 'zext512'
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = zext <8 x i16> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %D = zext <16 x i16> undef to <16 x
i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL256-LABEL: 'zext512'
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to
<8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to
<8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to
<16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to
<16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to
<32 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL512-LABEL: 'zext512'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to
<8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to
<8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to

```

```

<16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to
<16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to
<32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX256-LABEL: 'zext512'
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x
i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x
i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x
i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX512-LABEL: 'zext512'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x
i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x
i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x
i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x
i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x
i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
%A = zext <8 x i16> undef to <8 x i64>
%B = zext <8 x i32> undef to <8 x i64>
%C = zext <16 x i8> undef to <16 x i32>
%D = zext <16 x i16> undef to <16 x i32>
%E = zext <32 x i8> undef to <32 x i16>
ret void
}

define void @sext256() "min-legal-vector-width"="256" {
; VEC256-LABEL: 'sext256'
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = sext <8 x i16> undef to <8 x
i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x
i64>

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; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x
i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = sext <16 x i16> undef to <16 x
i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x
i16>
; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL512-LABEL: 'sext256'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8
x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to
<8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to
<8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to
<16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to
<16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to
<32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX512-LABEL: 'sext256'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x
i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x
i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x
i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x
i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x
i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
%A = sext <8 x i8> undef to <8 x i64>
%B = sext <8 x i16> undef to <8 x i64>
%C = sext <8 x i32> undef to <8 x i64>
%D = sext <16 x i8> undef to <16 x i32>
%E = sext <16 x i16> undef to <16 x i32>
%F = sext <32 x i8> undef to <32 x i16>
ret void
}

define void @sext512() "min-legal-vector-width"="512" {
; AVX-LABEL: 'sext512'

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; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL256-LABEL: 'sext512'
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8
x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to
<8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to
<8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to
<16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to
<16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to
<32 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL512-LABEL: 'sext512'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8
x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to
<8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to
<8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to
<16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to
<16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to
<32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX256-LABEL: 'sext512'
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x
i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x
i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x
i32>

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; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x
i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX512-LABEL: 'sext512'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x
i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x
i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x
i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x
i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x
i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
%A = sext <8 x i8> undef to <8 x i64>
%B = sext <8 x i16> undef to <8 x i64>
%C = sext <8 x i32> undef to <8 x i64>
%D = sext <16 x i8> undef to <16 x i32>
%E = sext <16 x i16> undef to <16 x i32>
%F = sext <32 x i8> undef to <32 x i16>
ret void
}

define void @trunc256() "min-legal-vector-width"="256" {
; AVX-LABEL: 'trunc256'
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = trunc <8 x i64> undef to <8 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %B = trunc <8 x i64> undef to <8 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %C = trunc <8 x i64> undef to <8 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %D = trunc <16 x i32> undef to <16 x
i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %E = trunc <16 x i32> undef to <16 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %F = trunc <32 x i16> undef to <32 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL256-LABEL: 'trunc256'
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = trunc <8 x i64> undef to
<8 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %B = trunc <8 x i64> undef to
<8 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %C = trunc <8 x i64> undef
to <8 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %D = trunc <16 x i32> undef
to <16 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %E = trunc <16 x i32> undef to

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<16 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %F = trunc <32 x i16> undef to
<32 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL512-LABEL: 'trunc256'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = trunc <8 x i64> undef to
<8 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %B = trunc <8 x i64> undef to
<8 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %C = trunc <8 x i64> undef to
<8 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %D = trunc <16 x i32> undef
to <16 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %E = trunc <16 x i32> undef to
<16 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 8 for instruction: %F = trunc <32 x i16> undef to
<32 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX256-LABEL: 'trunc256'
; SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = trunc <8 x i64> undef to <8 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %B = trunc <8 x i64> undef to <8 x
i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %C = trunc <8 x i64> undef to <8 x
i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %D = trunc <16 x i32> undef to <16 x
i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %E = trunc <16 x i32> undef to <16 x
i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %F = trunc <32 x i16> undef to <32 x
i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX512-LABEL: 'trunc256'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = trunc <8 x i64> undef to <8 x
i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %B = trunc <8 x i64> undef to <8 x
i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %C = trunc <8 x i64> undef to <8 x
i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %D = trunc <16 x i32> undef to <16 x
i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %E = trunc <16 x i32> undef to <16 x
i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %F = trunc <32 x i16> undef to <32 x
i8>

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; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
%A = trunc <8 x i64> undef to <8 x i32>
%B = trunc <8 x i64> undef to <8 x i16>
%C = trunc <8 x i64> undef to <8 x i8>
%D = trunc <16 x i32> undef to <16 x i16>
%E = trunc <16 x i32> undef to <16 x i8>
%F = trunc <32 x i16> undef to <32 x i8>
ret void
}

define i32 @zext256_vXi1() "min-legal-vector-width"="256" {
; AVX-LABEL: 'zext256_vXi1'
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;

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; AVX512VL256-LABEL: 'zext256_vXi1'
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 12 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 24 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 12 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 25 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 50 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; AVX512VL512-LABEL: 'zext256_vXi1'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>

; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 19 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX256-LABEL: 'zext256_vXi1'
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = zext <4 x i1> undef to <4 x

i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX512-LABEL: 'zext256_vXi1'
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>

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; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = zext <4 x i1> undef to <4 x
i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = zext <8 x i1> undef to <8 x
i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = zext <16 x i1> undef to <16
x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = zext <32 x i1> undef to <32
x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V64i8 = zext <64 x i1> undef to <64
x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
%V2i64 = zext <2 x i1> undef to <2 x i64>
%V4i64 = zext <4 x i1> undef to <4 x i64>
%V8i64 = zext <8 x i1> undef to <8 x i64>

%V2i32 = zext <2 x i1> undef to <2 x i32>
%V4i32 = zext <4 x i1> undef to <4 x i32>
%V8i32 = zext <8 x i1> undef to <8 x i32>
%V16i32 = zext <16 x i1> undef to <16 x i32>

%V2i16 = zext <2 x i1> undef to <2 x i16>
%V4i16 = zext <4 x i1> undef to <4 x i16>
%V8i16 = zext <8 x i1> undef to <8 x i16>
%V16i16 = zext <16 x i1> undef to <16 x i16>
%V32i16 = zext <32 x i1> undef to <32 x i16>

%V2i8 = zext <2 x i1> undef to <2 x i8>
%V4i8 = zext <4 x i1> undef to <4 x i8>
%V8i8 = zext <8 x i1> undef to <8 x i8>
%V16i8 = zext <16 x i1> undef to <16 x i8>
%V32i8 = zext <32 x i1> undef to <32 x i8>
%V64i8 = zext <64 x i1> undef to <64 x i8>

ret i32 undef
}

define i32 @sext256_vXi1() "min-legal-vector-width"="256" {
; AVX-LABEL: 'sext256_vXi1'
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = sext <2 x i1> undef to <2 x
i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i64 = sext <4 x i1> undef to <4 x
i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V8i64 = sext <8 x i1> undef to <8 x
i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = sext <2 x i1> undef to <2 x

```

i32>

; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>

; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>

; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>

; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16

; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>

; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>

; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>

; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>

; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>

; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8

; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>

; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>

; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>

; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>

; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>

; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>

; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef

;

; AVX512VL256-LABEL: 'sext256_vXi1'

; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64

; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>

; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>

; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>

; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32

; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>

; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>

; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>

; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>

; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16

; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 10 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 20 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 10 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 21 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 42 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; AVX512VL512-LABEL: 'sext256_vXi1'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i16 = sext <8 x i1> undef

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to <8 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i16 = sext <16 x i1>
undef to <16 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V32i16 = sext <32 x i1>
undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i8 = sext <2 x i1> undef to
<2 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i8 = sext <4 x i1> undef to
<4 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i8 = sext <8 x i1> undef to
<8 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i8 = sext <16 x i1> undef
to <16 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V32i8 = sext <32 x i1> undef
to <32 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 15 for instruction: %V64i8 = sext <64 x i1>
undef to <64 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX256-LABEL: 'sext256_vXi1'
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef to <2 x
i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i64 = sext <4 x i1> undef to <4 x
i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i64 = sext <8 x i1> undef to <8 x
i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = sext <2 x i1> undef to <2 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = sext <4 x i1> undef to <4 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = sext <8 x i1> undef to <8 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i32 = sext <16 x i1> undef to
<16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = sext <2 x i1> undef to <2 x
i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = sext <4 x i1> undef to <4 x
i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i16 = sext <8 x i1> undef to <8 x
i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = sext <16 x i1> undef to
<16 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V32i16 = sext <32 x i1> undef to
<32 x i16>

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; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX512-LABEL: 'sext256_vXi1'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>

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; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = sext <8 x i1> undef to <8 x
i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i8 = sext <16 x i1> undef to <16
x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i8 = sext <32 x i1> undef to <32
x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V64i8 = sext <64 x i1> undef to <64
x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
%I64 = sext i1 undef to i64
%V2i64 = sext <2 x i1> undef to <2 x i64>
%V4i64 = sext <4 x i1> undef to <4 x i64>
%V8i64 = sext <8 x i1> undef to <8 x i64>

%I32 = sext i1 undef to i32
%V2i32 = sext <2 x i1> undef to <2 x i32>
%V4i32 = sext <4 x i1> undef to <4 x i32>
%V8i32 = sext <8 x i1> undef to <8 x i32>
%V16i32 = sext <16 x i1> undef to <16 x i32>

%I16 = sext i1 undef to i16
%V2i16 = sext <2 x i1> undef to <2 x i16>
%V4i16 = sext <4 x i1> undef to <4 x i16>
%V8i16 = sext <8 x i1> undef to <8 x i16>
%V16i16 = sext <16 x i1> undef to <16 x i16>
%V32i16 = sext <32 x i1> undef to <32 x i16>

%I8 = sext i1 undef to i8
%V2i8 = sext <2 x i1> undef to <2 x i8>
%V4i8 = sext <4 x i1> undef to <4 x i8>
%V8i8 = sext <8 x i1> undef to <8 x i8>
%V16i8 = sext <16 x i1> undef to <16 x i8>
%V32i8 = sext <32 x i1> undef to <32 x i8>
%V64i8 = sext <64 x i1> undef to <64 x i8>

ret i32 undef
}

define i32 @trunc_vXi1() "min-legal-vector-width"="256" {
; AVX-LABEL: 'trunc_vXi1'
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x
i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x
i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x
i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to

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<16 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V32i64 = trunc <32 x i64> undef to
<32 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 46 for instruction: %V64i64 = trunc <64 x i64> undef to
<64 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x
i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x
i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x
i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = trunc <16 x i32> undef to <16
x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V32i32 = trunc <32 x i32> undef to
<32 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 22 for instruction: %V64i32 = trunc <64 x i32> undef to
<64 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x
i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x
i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x
i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i16 = trunc <16 x i16> undef to <16
x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V32i16 = trunc <32 x i16> undef to <32
x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 18 for instruction: %V64i16 = trunc <64 x i16> undef to
<64 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x
i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x
i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x
i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; AVX512VL256-LABEL: 'trunc_vXi1'
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = trunc <2 x i64>
undef to <2 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = trunc <4 x i64>
undef to <4 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V8i64 = trunc <8 x i64>
undef to <8 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64>

```

```

undef to <16 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 22 for instruction: %V32i64 = trunc <32 x i64>
undef to <32 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 44 for instruction: %V64i64 = trunc <64 x i64>
undef to <64 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = trunc <2 x i32>
undef to <2 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = trunc <4 x i32>
undef to <4 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32>
undef to <8 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = trunc <16 x i32>
undef to <16 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 10 for instruction: %V32i32 = trunc <32 x i32>
undef to <32 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 20 for instruction: %V64i32 = trunc <64 x i32>
undef to <64 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i16 = trunc <2 x i16>
undef to <2 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i16 = trunc <4 x i16>
undef to <4 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i16 = trunc <8 x i16>
undef to <8 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 8 for instruction: %V16i16 = trunc <16 x i16>
undef to <16 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 16 for instruction: %V32i16 = trunc <32 x i16>
undef to <32 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 32 for instruction: %V64i16 = trunc <64 x i16>
undef to <64 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i8 = trunc <2 x i8> undef
to <2 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i8 = trunc <4 x i8> undef
to <4 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i8 = trunc <8 x i8> undef
to <8 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 8 for instruction: %V16i8 = trunc <16 x i8>
undef to <16 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 17 for instruction: %V32i8 = trunc <32 x i8>
undef to <32 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 34 for instruction: %V64i8 = trunc <64 x i8>
undef to <64 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; AVX512VL512-LABEL: 'trunc_vXi1'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = trunc <2 x i64>
undef to <2 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = trunc <4 x i64>
undef to <4 x i1>

```

```

; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i64 = trunc <8 x i64>
undef to <8 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64>
undef to <16 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 22 for instruction: %V32i64 = trunc <32 x i64>
undef to <32 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 44 for instruction: %V64i64 = trunc <64 x i64>
undef to <64 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = trunc <2 x i32>
undef to <2 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = trunc <4 x i32>
undef to <4 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32>
undef to <8 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = trunc <16 x i32>
undef to <16 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V32i32 = trunc <32 x i32>
undef to <32 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 8 for instruction: %V64i32 = trunc <64 x i32>
undef to <64 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i16 = trunc <2 x i16>
undef to <2 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i16 = trunc <4 x i16>
undef to <4 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i16 = trunc <8 x i16>
undef to <8 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i16 = trunc <16 x i16>
undef to <16 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V32i16 = trunc <32 x i16>
undef to <32 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 14 for instruction: %V64i16 = trunc <64 x i16>
undef to <64 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i8 = trunc <2 x i8> undef
to <2 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i8 = trunc <4 x i8> undef
to <4 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i8 = trunc <8 x i8> undef
to <8 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i8 = trunc <16 x i8>
undef to <16 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V32i8 = trunc <32 x i8>
undef to <32 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 15 for instruction: %V64i8 = trunc <64 x i8>
undef to <64 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX256-LABEL: 'trunc_vXi1'
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = trunc <2 x i64> undef to <2

```

x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 47 for instruction: %V64i64 = trunc <64 x i64> undef to <64 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x il>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef

;
; SKX512-LABEL: 'trunc_vXi1'
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 47 for instruction: %V64i64 = trunc <64 x i64> undef to <64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x i1>

```

; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V64i8 = trunc <64 x i8> undef to
<64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
%V2i64 = trunc <2 x i64> undef to <2 x i1>
%V4i64 = trunc <4 x i64> undef to <4 x i1>
%V8i64 = trunc <8 x i64> undef to <8 x i1>
%V16i64 = trunc <16 x i64> undef to <16 x i1>
%V32i64 = trunc <32 x i64> undef to <32 x i1>
%V64i64 = trunc <64 x i64> undef to <64 x i1>

%V2i32 = trunc <2 x i32> undef to <2 x i1>
%V4i32 = trunc <4 x i32> undef to <4 x i1>
%V8i32 = trunc <8 x i32> undef to <8 x i1>
%V16i32 = trunc <16 x i32> undef to <16 x i1>
%V32i32 = trunc <32 x i32> undef to <32 x i1>
%V64i32 = trunc <64 x i32> undef to <64 x i1>

%V2i16 = trunc <2 x i16> undef to <2 x i1>
%V4i16 = trunc <4 x i16> undef to <4 x i1>
%V8i16 = trunc <8 x i16> undef to <8 x i1>
%V16i16 = trunc <16 x i16> undef to <16 x i1>
%V32i16 = trunc <32 x i16> undef to <32 x i1>
%V64i16 = trunc <64 x i16> undef to <64 x i1>

%V2i8 = trunc <2 x i8> undef to <2 x i1>
%V4i8 = trunc <4 x i8> undef to <4 x i1>
%V8i8 = trunc <8 x i8> undef to <8 x i1>
%V16i8 = trunc <16 x i8> undef to <16 x i1>
%V32i8 = trunc <32 x i8> undef to <32 x i1>
%V64i8 = trunc <64 x i8> undef to <64 x i1>

ret i32 undef
}

```

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1.207 jffi 1.0.1

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1.212 ncurses 5.7

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1.213 Idns 1.7.0

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1.214 python-certifi 2019.06.16

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This package contains a modified version of ca-bundle.crt:

ca-bundle.crt -- Bundle of CA Root Certificates

Certificate data from Mozilla as of: Thu Nov 3 19:04:19 2011#

This is a bundle of X.509 certificates of public Certificate Authorities (CA). These were automatically extracted from Mozilla's root certificates file (certdata.txt). This file can be found in the mozilla source tree:

<http://mxr.mozilla.org/mozilla/source/security/nss/lib/ckfw/builtins/certdata.txt?raw=1#>

It contains the certificates in PEM format and therefore can be directly used with curl / libcurl / php_curl, or with an Apache+mod_ssl webserver for SSL client authentication.

Just configure this file as the SSLCACertificateFile.#

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@(#) \$RCSfile: certdata.txt,v \$ \$Revision: 1.80 \$ \$Date: 2011/11/03 15:11:58 \$

1.215 tomcat-jasper 8.5.32

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1.216 recommonmark 0.5.0

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1.217 libyaml 0.2.5

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1.218 jetty-continuation 9.4.16.v20190411

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The following artifacts are EPL.

* org.eclipse.jetty.orbit:org.eclipse.jdt.core

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* org.eclipse.jetty.orbit:javax.security.auth.message

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* org.eclipse.jetty.orbit:javax.mail.glassfish

Oracle

The following artifacts are CDDL + GPLv2 with classpath exception.

<https://glassfish.dev.java.net/nonav/public/CDDL+GPL.html>

* javax.servlet:javax.servlet-api

* javax.annotation:javax.annotation-api

* javax.transaction:javax.transaction-api

* javax.websocket:javax.websocket-api

Oracle OpenJDK

If ALPN is used to negotiate HTTP/2 connections, then the following artifacts may be included in the distribution or downloaded when ALPN module is selected.

* java.sun.security.ssl

These artifacts replace/modify OpenJDK classes. The modifications are hosted at github and both modified and original are under GPL v2 with classpath exceptions.

<http://openjdk.java.net/legal/gplv2+ce.html>

OW2

The following artifacts are licensed by the OW2 Foundation according to the terms of <http://asm.ow2.org/license.html>

org.ow2.asm:asm-commons

org.ow2.asm:asm

Apache

The following artifacts are ASL2 licensed.

org.apache.taglibs:taglibs-standard-spec

org.apache.taglibs:taglibs-standard-impl

MortBay

The following artifacts are ASL2 licensed. Based on selected classes from following Apache Tomcat jars, all ASL2 licensed.

org.mortbay.jasper:apache-jsp

org.apache.tomcat:tomcat-jasper

org.apache.tomcat:tomcat-juli

org.apache.tomcat:tomcat-jsp-api

org.apache.tomcat:tomcat-el-api

org.apache.tomcat:tomcat-jasper-el

org.apache.tomcat:tomcat-api

org.apache.tomcat:tomcat-util-scan

org.apache.tomcat:tomcat-util

org.mortbay.jasper:apache-el

org.apache.tomcat:tomcat-jasper-el

org.apache.tomcat:tomcat-el-api

Mortbay

The following artifacts are CDDL + GPLv2 with classpath exception.

<https://glassfish.dev.java.net/nonav/public/CDDL+GPL.html>

org.eclipse.jetty.toolchain:jetty-schemas

Assorted

The UnixCrypt.java code implements the one way cryptography used by Unix systems for simple password protection. Copyright 1996 Aki Yoshida, modified April 2001 by Iris Van den Broeke, Daniel Deville.

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1.219 commons-lang3 2.6

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1.220 tomcat-jdbc 8.5.32

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1.221 stateless4j 2.5.0

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```
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jar/com/github/oxo42/stateless4j/delegates/FuncBoolean.java
* /opt/cola/permits/1257784248_1643014186.5/0/stateless4j-2-5-0-sources-
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1.222 xalan 2.7.2

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Files: util/AnsiTerm.cpp

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Upstream-Name: ipmiutil

Upstream-Contact: Andy Cress <arcress@users.sourceforge.net>

Source: <http://ipmiutil.sourceforge.net>

Comment: This package was debianized by Andy Cress on 2010-10-29, with Debian package updates by Alex Waite on 2013-06-14

Files: *

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Upstream-Name: ipmiutil

Upstream-Contact: Andy Cress <arcress@users.sourceforge.net>

Source: <http://ipmiutil.sourceforge.net>

<http://www.openmash.org/lxr/source/src/getopt.c?c=gsm>

<http://www.openssl.org/source/openssl-0.9.8x.tar.gz>

Comment: This COPYING.win covers contributed source used in Windows ipmiutil.exe

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1.224 pep517 0.5.0

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1.225 tomcat-jaspic-api 8.5.32

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Found in path(s):

- * /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-jar/javax/security/auth/message/AuthStatus.java
- * /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-jar/javax/security/auth/message/config/AuthConfigProvider.java
- * /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-jar/javax/security/auth/message/config/AuthConfig.java
- * /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-jar/javax/security/auth/message/module/ServerAuthModule.java
- * /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-jar/javax/security/auth/message/config/ServerAuthConfig.java
- * /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-jar/javax/security/auth/message/ServerAuth.java
- * /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-jar/javax/security/auth/message/config/RegistrationListener.java
- * /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-jar/javax/security/auth/message/config/ClientAuthContext.java
- * /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-jar/javax/security/auth/message/config/ServerAuthContext.java
- * /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-jar/javax/security/auth/message/callback/CallerPrincipalCallback.java
- * /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-jar/javax/security/auth/message/MessagePolicy.java
- * /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-jar/javax/security/auth/message/config/AuthConfigFactory.java
- * /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-jar/javax/security/auth/message/AuthException.java
- * /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-jar/javax/security/auth/message/callback/GroupPrincipalCallback.java
- * /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-jar/javax/security/auth/message/config/ClientAuthConfig.java
- * /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-jar/javax/security/auth/message/callback/SecretKeyCallback.java
- * /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-jar/javax/security/auth/message/callback/PasswordValidationCallback.java

```
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/ClientAuth.java
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/module/ClientAuthModule.java
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/MessageInfo.java
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/callback/TrustStoreCallback.java
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/callback/CertStoreCallback.java
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/callback/PrivateKeyCallback.java
```

1.226 python-certifi 2018.01.18

1.226.1 Available under license :

This package contains a modified version of ca-bundle.crt:

ca-bundle.crt -- Bundle of CA Root Certificates

Certificate data from Mozilla as of: Thu Nov 3 19:04:19 2011#

This is a bundle of X.509 certificates of public Certificate Authorities (CA). These were automatically extracted from Mozilla's root certificates file (certdata.txt). This file can be found in the mozilla source tree:

<http://mxr.mozilla.org/mozilla/source/security/nss/lib/ckfw/builtins/certdata.txt?raw=1#>

It contains the certificates in PEM format and therefore can be directly used with curl / libcurl / php_curl, or with an Apache+mod_ssl webserver for SSL client authentication.

Just configure this file as the SSLCACertificateFile.#

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1.227 pathtools 0.1.2

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In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see <http://www.zope.com>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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| Release | Derived from | Year | Owner | GPL-compatible? (1) |
|---------|--------------|------|-------|---------------------|
| 2.5 | 2.4 | 2006 | PSF | yes |
| 2.5.1 | 2.5 | 2007 | PSF | yes |
| 2.5.2 | 2.5.1 | 2008 | PSF | yes |
| 2.5.3 | 2.5.2 | 2008 | PSF | yes |
| 2.6 | 2.5 | 2008 | PSF | yes |
| 2.6.1 | 2.6 | 2008 | PSF | yes |
| 2.6.2 | 2.6.1 | 2009 | PSF | yes |
| 2.6.3 | 2.6.2 | 2009 | PSF | yes |
| 2.6.4 | 2.6.3 | 2009 | PSF | yes |
| 2.6.5 | 2.6.4 | 2010 | PSF | yes |
| 3.0 | 2.6 | 2008 | PSF | yes |
| 3.0.1 | 3.0 | 2009 | PSF | yes |
| 3.1 | 3.0.1 | 2009 | PSF | yes |
| 3.1.1 | 3.1 | 2009 | PSF | yes |
| 3.1.2 | 3.1.1 | 2010 | PSF | yes |
| 3.1.3 | 3.1.2 | 2010 | PSF | yes |
| 3.1.4 | 3.1.3 | 2011 | PSF | yes |
| 3.2 | 3.1 | 2011 | PSF | yes |
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|----------------|--------------|-----------|------------|---------------------|
| 0.9.0 thru 1.2 | | 1991-1995 | CWI | yes |
| 1.3 thru 1.5.2 | 1.2 | 1995-1999 | CNRI | yes |
| 1.6 | 1.5.2 | 2000 | CNRI | no |
| 2.0 | 1.6 | 2000 | BeOpen.com | no |
| 1.6.1 | 1.6 | 2001 | CNRI | yes (2) |
| 2.1 | 2.0+1.6.1 | 2001 | PSF | no |

| | | | | |
|-------|-----------|-----------|-----|-----|
| 2.0.1 | 2.0+1.6.1 | 2001 | PSF | yes |
| 2.1.1 | 2.1+2.0.1 | 2001 | PSF | yes |
| 2.2 | 2.1.1 | 2001 | PSF | yes |
| 2.1.2 | 2.1.1 | 2002 | PSF | yes |
| 2.1.3 | 2.1.2 | 2002 | PSF | yes |
| 2.2.1 | 2.2 | 2002 | PSF | yes |
| 2.2.2 | 2.2.1 | 2002 | PSF | yes |
| 2.2.3 | 2.2.2 | 2003 | PSF | yes |
| 2.3 | 2.2.2 | 2002-2003 | PSF | yes |
| 2.3.1 | 2.3 | 2002-2003 | PSF | yes |
| 2.3.2 | 2.3.1 | 2002-2003 | PSF | yes |
| 2.3.3 | 2.3.2 | 2002-2003 | PSF | yes |
| 2.3.4 | 2.3.3 | 2004 | PSF | yes |
| 2.3.5 | 2.3.4 | 2005 | PSF | yes |
| 2.4 | 2.3 | 2004 | PSF | yes |
| 2.4.1 | 2.4 | 2005 | PSF | yes |
| 2.4.2 | 2.4.1 | 2005 | PSF | yes |
| 2.4.3 | 2.4.2 | 2006 | PSF | yes |
| 2.4.4 | 2.4.3 | 2006 | PSF | yes |
| 2.5 | 2.4 | 2006 | PSF | yes |
| 2.5.1 | 2.5 | 2007 | PSF | yes |
| 2.5.2 | 2.5.1 | 2008 | PSF | yes |
| 2.5.3 | 2.5.2 | 2008 | PSF | yes |
| 2.6 | 2.5 | 2008 | PSF | yes |
| 2.6.1 | 2.6 | 2008 | PSF | yes |
| 2.6.2 | 2.6.1 | 2009 | PSF | yes |
| 2.6.3 | 2.6.2 | 2009 | PSF | yes |
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A. HISTORY OF THE SOFTWARE

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Python was created in the early 1990s by Guido van Rossum at Stichting
Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands
as a successor of a language called ABC. Guido remains Python's
principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for
National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>)
in Reston, Virginia where he released several versions of the
software.

In May 2000, Guido and the Python core development team moved to
BeOpen.com to form the BeOpen PythonLabs team. In October of the same
year, the PythonLabs team moved to Digital Creations (now Zope
Corporation, see <http://www.zope.com>). In 2001, the Python Software
Foundation (PSF, see <http://www.python.org/psf/>) was formed, a
non-profit organization created specifically to own Python-related
Intellectual Property. Zope Corporation is a sponsoring member of
the PSF.

All Python releases are Open Source (see <http://www.opensource.org> for

the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

| Release | Derived from | Year | Owner | GPL-compatible? (1) |
|----------------|--------------|-----------|------------|---------------------|
| 0.9.0 thru 1.2 | | 1991-1995 | CWI | yes |
| 1.3 thru 1.5.2 | 1.2 | 1995-1999 | CNRI | yes |
| 1.6 | 1.5.2 | 2000 | CNRI | no |
| 2.0 | 1.6 | 2000 | BeOpen.com | no |
| 1.6.1 | 1.6 | 2001 | CNRI | yes (2) |
| 2.1 | 2.0+1.6.1 | 2001 | PSF | no |
| 2.0.1 | 2.0+1.6.1 | 2001 | PSF | yes |
| 2.1.1 | 2.1+2.0.1 | 2001 | PSF | yes |
| 2.2 | 2.1.1 | 2001 | PSF | yes |
| 2.1.2 | 2.1.1 | 2002 | PSF | yes |
| 2.1.3 | 2.1.2 | 2002 | PSF | yes |
| 2.2.1 | 2.2 | 2002 | PSF | yes |
| 2.2.2 | 2.2.1 | 2002 | PSF | yes |
| 2.2.3 | 2.2.2 | 2003 | PSF | yes |
| 2.3 | 2.2.2 | 2002-2003 | PSF | yes |
| 2.3.1 | 2.3 | 2002-2003 | PSF | yes |
| 2.3.2 | 2.3.1 | 2002-2003 | PSF | yes |
| 2.3.3 | 2.3.2 | 2002-2003 | PSF | yes |
| 2.3.4 | 2.3.3 | 2004 | PSF | yes |
| 2.3.5 | 2.3.4 | 2005 | PSF | yes |
| 2.4 | 2.3 | 2004 | PSF | yes |
| 2.4.1 | 2.4 | 2005 | PSF | yes |
| 2.4.2 | 2.4.1 | 2005 | PSF | yes |
| 2.4.3 | 2.4.2 | 2006 | PSF | yes |
| 2.4.4 | 2.4.3 | 2006 | PSF | yes |
| 2.5 | 2.4 | 2006 | PSF | yes |
| 2.5.1 | 2.5 | 2007 | PSF | yes |
| 2.5.2 | 2.5.1 | 2008 | PSF | yes |
| 2.5.3 | 2.5.2 | 2008 | PSF | yes |
| 2.6 | 2.5 | 2008 | PSF | yes |
| 2.6.1 | 2.6 | 2008 | PSF | yes |
| 2.6.2 | 2.6.1 | 2009 | PSF | yes |
| 2.6.3 | 2.6.2 | 2009 | PSF | yes |
| 2.6.4 | 2.6.3 | 2009 | PSF | yes |
| 2.6.5 | 2.6.4 | 2010 | PSF | yes |
| 3.0 | 2.6 | 2008 | PSF | yes |
| 3.0.1 | 3.0 | 2009 | PSF | yes |
| 3.1 | 3.0.1 | 2009 | PSF | yes |
| 3.1.1 | 3.1 | 2009 | PSF | yes |
| 3.1.2 | 3.1 | 2010 | PSF | yes |
| 3.2 | 3.1 | 2010 | PSF | yes |

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(2) According to Richard Stallman, 1.6.1 is not GPL-compatible, because its license has a choice of law clause. According to CNRI, however, Stallman's lawyer has told CNRI's lawyer that 1.6.1 is "not incompatible" with the GPL.

Thanks to the many outside volunteers who have worked under Guido's direction to make these releases possible.

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ca-bundle.crt -- Bundle of CA Root Certificates

Certificate data from Mozilla as of: Thu Nov 3 19:04:19 2011#

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<http://mxr.mozilla.org/mozilla/source/security/nss/lib/ckfw/builtins/certdata.txt?raw=1#>

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Just configure this file as the SSLCertificateFile.#

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@(#) \$RCSfile: certdata.txt,v \$ \$Revision: 1.80 \$ \$Date: 2011/11/03 15:11:58 \$

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1.231 cffi 1.14.4

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1.232 ncurses 6.2

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1.233 commons-codec 1.9

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

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- b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.
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b) Convey the object code in, or embodied in, a physical product (including a physical distribution medium), accompanied by a written offer, valid for at least three years and valid for as long as you offer spare parts or customer support for that product model, to give anyone who possesses the object code either (1) a copy of the Corresponding Source for all the software in the product that is covered by this License, on a durable physical medium customarily used for software interchange, for a price no more than your reasonable cost of physically performing this conveying of source, or (2) access to copy the Corresponding Source from a network server at no charge.

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- * /opt/cola/permits/1136493993_1613774733.35/0/asm-all-3-2-sources-2-jar/org/objectweb/asm/xml/ASMContentHandler.java
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1.240 pyjwt 1.5.3

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1.241 tomcat-websocket-api 8.5.32

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1.243 activation 1.1.1

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1.245 pytz 2020.1

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1.246 sphinx-rtd-theme 0.4.3

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1.247 bolt 1.3.1

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1.248 lua 5.3.4

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1.249 glib 2.66.7

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Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a

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When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

- a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that

uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

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d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

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It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

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1.250 gdbm 1.13

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The "source code" for a work means the preferred form of the work for making modifications to it. "Object code" means any non-source form of a work.

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1.251 tcsh 6.20.00

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```

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*/

/*

* config.h -- configure various defines for tcsh

*

* All source files should #include this FIRST.

*

* Edit this to match your system type.

*/

#ifndef _h_config

#define _h_config

/****** System dependant compilation flags *****/

/*

* POSIX This system supports IEEE Std 1003.1-1988 (POSIX).

*/

#undef POSIX

/*

* POSIXJOBS This system supports the optional IEEE Std 1003.1-1988 (POSIX)

* job control facilities.

*/

#undef POSIXJOBS

/*

* VFORK This machine has a vfork().

* It used to be that for job control to work, this define

* was mandatory. This is not the case any more.

* If you think you still need it, but you don't have vfork,

* define this anyway and then do #define vfork fork.

* I do this anyway on a Sun because of yellow pages brain damage,

* [should not be needed under 4.1]

* and on the iris4d cause SGI's fork is sufficiently "virtual"

* that vfork isn't necessary. (Besides, SGI's vfork is weird).

* Note that some machines eg. rs6000 have a vfork, but not

* with the berkeley semantics, so we cannot use it there either.

*/

#define VFORK

```

/*
* BSDJOBS You have BSD-style job control (both process groups and
* a tty that deals correctly
*/
#define BSDJOBS

/*
* BSDTIMES You have BSD-style process time stuff (like rusage)
* This may or may not be true. For example, Apple Unix
* (OREO) has BSDJOBS but not BSDTIMES.
*/
#define BSDTIMES

/*
* BSDLIMIT You have BSD-style resource limit stuff (getrlimit/setrlimit)
*/
#define BSDLIMIT

/*
* TERMIO You have struct termio instead of struct sgtyb.
* This is usually the case for SYSV systems, where
* BSD uses sgtyb. POSIX systems should define this
* anyway, even though they use struct termios.
*/
#undef TERMIO

/*
* SYSVREL Your machine is SYSV based (HPUX, A/UX)
* NOTE: don't do this if you are on a Pyramid -- tcsh is
* built in a BSD universe.
* Set SYSVREL to 1, 2, 3, or 4, depending the version of System V
* you are running. Or set it to 0 if you are not SYSV based
*/
#define SYSVREL 0

/*
* YPBUGS Work around Sun YP bugs that cause expansion of ~username
* to send command output to /dev/null
*/
#undef YPBUGS

/***** local defines *****/

#endif /* _h_config */

```

1.252 clamav 0.104.2

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Version 2, June 1991

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```
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getopt.c - my re-implementation of getopt.

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bzip2/libbzip2 version 1.0.4 of 20 December 2006

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1.254 jackson-mapper-asl 1.0.1

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/* Jackson JSON-processor.
```

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*/
```

Found in path(s):

```
* /opt/cola/permits/1151085999_1617779160.19/0/jackson-mapper-asl-1-0-1-sources-1-  
jar/org/codehaus/jackson/map/MappingJsonFactory.java
```

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1.256 libpcap 1.4.0

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1.257 jsp 2.3.FR

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1.258 tomcat-websocket 8.5.32

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1.259 rfc3986 1.3.2

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Manifest-Version: 1.0

Bundle-Description: istack common utility code

Implementation-Title: istack common utility code runtime

Bundle-License: https://glassfish.java.net/public/CDDL+GPL_1_1.html, h

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Bundle-SymbolicName: com.sun.istack.common-runtime

Implementation-Version: 3.0.5

Built-By: build

Bnd-LastModified: 1486772850426

Bundle-ManifestVersion: 2

Implementation-Vendor-Id: com.sun.istack

Bundle-DocURL: <http://www.oracle.com/>

Bundle-Vendor: Oracle Corporation

Import-Package: javax.activation,javax.xml.namespace,javax.xml.stream,
org.xml.sax,org.xml.sax.helpers

Require-Capability: osgi.ee;filter="(&(osgi.ee=JavaSE)(version=1.7))"

Tool: Bnd-3.2.0.201605172007

Implementation-Vendor: Oracle Corporation

Export-Package: com.sun.istack;uses="javax.activation,javax.xml.strea
m,org.xml.sax,org.xml.sax.helpers";version="3.0.5",com.sun.istack.log
ging;version="3.0.5",com.sun.istack.localization;version="3.0.5"

Bundle-Name: istack common utility code runtime

Bundle-Version: 3.0.5

Implementation-Build-Id: tags/istack-commons-3.0.5-321, 2017-02-11T01:

27:24+0100

Created-By: Apache Maven Bundle Plugin

Build-Jdk: 1.8.0_112

Found in path(s):

* /opt/cola/permits/1334904685_1653676440.7415743/0/istack-commons-runtime-3-0-5-jar/META-INF/MANIFEST.MF

No license file was found, but licenses were detected in source scan.

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Found in path(s):

* /opt/cola/permits/1334904685_1653676440.7415743/0/istack-commons-runtime-3-0-5-jar/META-INF/maven/com.sun.istack/istack-commons-runtime/pom.xml

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```
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```

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*/

/******

*
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*
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include/GL/gl.h :

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```
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include/GL/glxext.h
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- */

Found in path(s):

- * /opt/cola/permits/203480980_1651176812.86/0/hikaricp-java7-2-4-13-sources-
zip/com/zaxxer/hikari/HikariConfig.java
- * /opt/cola/permits/203480980_1651176812.86/0/hikaricp-java7-2-4-13-sources-
zip/com/zaxxer/hikari/util/FastList.java
- * /opt/cola/permits/203480980_1651176812.86/0/hikaricp-java7-2-4-13-sources-
zip/com/zaxxer/hikari/pool/ProxyLeakTask.java
- * /opt/cola/permits/203480980_1651176812.86/0/hikaricp-java7-2-4-13-sources-
zip/com/zaxxer/hikari/pool/ProxyResultSet.java
- * /opt/cola/permits/203480980_1651176812.86/0/hikaricp-java7-2-4-13-sources-
zip/com/zaxxer/hikari/pool/ProxyConnection.java
- * /opt/cola/permits/203480980_1651176812.86/0/hikaricp-java7-2-4-13-sources-
zip/com/zaxxer/hikari/util/DriverDataSource.java
- * /opt/cola/permits/203480980_1651176812.86/0/hikaricp-java7-2-4-13-sources-
zip/com/zaxxer/hikari/pool/ProxyFactory.java
- * /opt/cola/permits/203480980_1651176812.86/0/hikaricp-java7-2-4-13-sources-
zip/com/zaxxer/hikari/util/ConcurrentBag.java
- * /opt/cola/permits/203480980_1651176812.86/0/hikaricp-java7-2-4-13-sources-
zip/com/zaxxer/hikari/util/SuspendResumeLock.java
- * /opt/cola/permits/203480980_1651176812.86/0/hikaricp-java7-2-4-13-sources-
zip/com/zaxxer/hikari/util/JavassistProxyFactory.java

1.263 libunistring 0.9.10-1

1.263.1 Available under license :

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```
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1.267 pcre 10.40

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1.268 bouncycastle-fips 1.0.3

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/**

* Base constructor.

* <p>

* The authorizer attempts to perform matching (including the use of the wildcard) in accordance with RFC 6125.
* </p>
* <p>
* Known suffixes is a list of public domain suffixes that can't be used as wild cards for
* example *.com, or c*c.com, as a dns wildcard could match every/most .com domains if a registrar were issue it.
* If *.com is in the known suffixes list will not be allowed to match.
* </p>
*
* @param knownSuffixes a set of suffixes that cannot be wild-carded, e.g. { ".com", ".net", ".org" }
*/

Found in path(s):

* /opt/ws_local/PERMITS_SQL/1089947271_1601369618.24/0/bcpkix-fips-1-0-3-sources-jar/org/bouncycastle/est/jcajce/JsseDefaultHostnameAuthorizer.java

1.269 nginx 1.12.1

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1.270 c-ares 1.10.0

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1.271 google-api-python-client 1.7.6

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=====

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httplib2 - MIT License

1.272 lucene 3.0.0

1.272.1 Available under license :

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1.273 heimdal 7.7.0

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@heading Vincent Rijmen, Antoon Bosselaers, Paulo Barreto

AES in libcrypto

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rijndael-alg-fst.c

@version 3.0 (December 2000)

Optimised ANSI C code for the Rijndael cipher (now AES)

@author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>

@author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>

@author Paulo Barreto <paulo.barreto@terra.com.br>

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kdc/announce.c

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DES core in libcrypto

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lib/hcrypto/test_dh.c

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1.275 viper 1.6.3

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1.277 html5lib 1.0.1

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1.278 packaging 16.8

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1.279 activation-api 1.1.1

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1.280 python-pip 19.2.3

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A. HISTORY OF THE SOFTWARE

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for

National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see <http://www.zope.com>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

All Python releases are Open Source (see <http://www.opensource.org> for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

| Release | Derived from | Year | Owner | GPL-compatible? (1) |
|----------------|--------------|-----------|------------|---------------------|
| 0.9.0 thru 1.2 | | 1991-1995 | CWI | yes |
| 1.3 thru 1.5.2 | 1.2 | 1995-1999 | CNRI | yes |
| 1.6 | 1.5.2 | 2000 | CNRI | no |
| 2.0 | 1.6 | 2000 | BeOpen.com | no |
| 1.6.1 | 1.6 | 2001 | CNRI | yes (2) |
| 2.1 | 2.0+1.6.1 | 2001 | PSF | no |
| 2.0.1 | 2.0+1.6.1 | 2001 | PSF | yes |
| 2.1.1 | 2.1+2.0.1 | 2001 | PSF | yes |
| 2.2 | 2.1.1 | 2001 | PSF | yes |
| 2.1.2 | 2.1.1 | 2002 | PSF | yes |
| 2.1.3 | 2.1.2 | 2002 | PSF | yes |
| 2.2.1 | 2.2 | 2002 | PSF | yes |
| 2.2.2 | 2.2.1 | 2002 | PSF | yes |
| 2.2.3 | 2.2.2 | 2003 | PSF | yes |
| 2.3 | 2.2.2 | 2002-2003 | PSF | yes |
| 2.3.1 | 2.3 | 2002-2003 | PSF | yes |
| 2.3.2 | 2.3.1 | 2002-2003 | PSF | yes |
| 2.3.3 | 2.3.2 | 2002-2003 | PSF | yes |
| 2.3.4 | 2.3.3 | 2004 | PSF | yes |
| 2.3.5 | 2.3.4 | 2005 | PSF | yes |
| 2.4 | 2.3 | 2004 | PSF | yes |
| 2.4.1 | 2.4 | 2005 | PSF | yes |
| 2.4.2 | 2.4.1 | 2005 | PSF | yes |
| 2.4.3 | 2.4.2 | 2006 | PSF | yes |
| 2.4.4 | 2.4.3 | 2006 | PSF | yes |
| 2.5 | 2.4 | 2006 | PSF | yes |
| 2.5.1 | 2.5 | 2007 | PSF | yes |

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|-------|-------|------|-----|-----|
| 2.5.2 | 2.5.1 | 2008 | PSF | yes |
| 2.5.3 | 2.5.2 | 2008 | PSF | yes |
| 2.6 | 2.5 | 2008 | PSF | yes |
| 2.6.1 | 2.6 | 2008 | PSF | yes |
| 2.6.2 | 2.6.1 | 2009 | PSF | yes |
| 2.6.3 | 2.6.2 | 2009 | PSF | yes |
| 2.6.4 | 2.6.3 | 2009 | PSF | yes |
| 2.6.5 | 2.6.4 | 2010 | PSF | yes |
| 2.7 | 2.6 | 2010 | PSF | yes |

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(2) According to Richard Stallman, 1.6.1 is not GPL-compatible, because its license has a choice of law clause. According to CNRI, however, Stallman's lawyer has told CNRI's lawyer that 1.6.1 is "not incompatible" with the GPL.

Thanks to the many outside volunteers who have worked under Guido's direction to make these releases possible.

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A. HISTORY OF THE SOFTWARE

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see <http://www.zope.com>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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| Release | Derived from | Year | Owner | GPL-compatible? (1) |
|----------------|--------------|-----------|------------|---------------------|
| 0.9.0 thru 1.2 | | 1991-1995 | CWI | yes |
| 1.3 thru 1.5.2 | 1.2 | 1995-1999 | CNRI | yes |
| 1.6 | 1.5.2 | 2000 | CNRI | no |
| 2.0 | 1.6 | 2000 | BeOpen.com | no |
| 1.6.1 | 1.6 | 2001 | CNRI | yes (2) |
| 2.1 | 2.0+1.6.1 | 2001 | PSF | no |
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| 2.1.1 | 2.1+2.0.1 | 2001 | PSF | yes |
| 2.2 | 2.1.1 | 2001 | PSF | yes |
| 2.1.2 | 2.1.1 | 2002 | PSF | yes |
| 2.1.3 | 2.1.2 | 2002 | PSF | yes |
| 2.2.1 | 2.2 | 2002 | PSF | yes |
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| 2.3.3 | 2.3.2 | 2002-2003 | PSF | yes |
| 2.3.4 | 2.3.3 | 2004 | PSF | yes |
| 2.3.5 | 2.3.4 | 2005 | PSF | yes |
| 2.4 | 2.3 | 2004 | PSF | yes |
| 2.4.1 | 2.4 | 2005 | PSF | yes |
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| 2.4.3 | 2.4.2 | 2006 | PSF | yes |
| 2.4.4 | 2.4.3 | 2006 | PSF | yes |
| 2.5 | 2.4 | 2006 | PSF | yes |
| 2.5.1 | 2.5 | 2007 | PSF | yes |
| 2.5.2 | 2.5.1 | 2008 | PSF | yes |
| 2.5.3 | 2.5.2 | 2008 | PSF | yes |
| 2.6 | 2.5 | 2008 | PSF | yes |
| 2.6.1 | 2.6 | 2008 | PSF | yes |
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| 2.6.3 | 2.6.2 | 2009 | PSF | yes |
| 2.6.4 | 2.6.3 | 2009 | PSF | yes |
| 2.6.5 | 2.6.4 | 2010 | PSF | yes |
| 3.0 | 2.6 | 2008 | PSF | yes |
| 3.0.1 | 3.0 | 2009 | PSF | yes |
| 3.1 | 3.0.1 | 2009 | PSF | yes |
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ca-bundle.crt -- Bundle of CA Root Certificates

Certificate data from Mozilla as of: Thu Nov 3 19:04:19 2011#

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It contains the certificates in PEM format and therefore can be directly used with curl / libcurl / php_curl, or with an Apache+mod_ssl webserver for SSL client authentication. Just configure this file as the SSLCACertificateFile.#

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* /opt/cola/permits/1135840457_1613613080.13/0/commons-logging-1-1-sources-9-jar/org/apache/commons/logging/impl/LogFactoryImpl.java
* /opt/cola/permits/1135840457_1613613080.13/0/commons-logging-1-1-sources-9-jar/org/apache/commons/logging/impl/Jdk13LumberjackLogger.java
* /opt/cola/permits/1135840457_1613613080.13/0/commons-logging-1-1-sources-9-jar/org/apache/commons/logging/impl/SimpleLog.java
* /opt/cola/permits/1135840457_1613613080.13/0/commons-logging-1-1-sources-9-jar/org/apache/commons/logging/impl/Jdk14Logger.java
* /opt/cola/permits/1135840457_1613613080.13/0/commons-logging-1-1-sources-9-jar/org/apache/commons/logging/LogSource.java
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Found in path(s):

*/opt/cola/permits/1135840457_1613613080.13/0/commons-logging-1-1-sources-9-jar/org/apache/commons/logging/impl/ServletContextCleaner.java

1.283 openssl 1.0.1l

1.283.1 Available under license :

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```

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```

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1.284 xmlsec 1.2.29

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xmlsec-nss library

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xmlsec-mscng library

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1.290 dash 0.5.9.1

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```
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```

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<signature of Ty Coon>, 1 April 1989

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The End

1.297 jackson-jaxrs 2.9.9

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1.298 openssl 1.0.0d

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1.299 futures 3.3.0

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1.300 icu 70

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title: Legalities
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See [svn changeset r39632](<https://github.com/unicode-org/icu/commit/0001f6c5e92f6f3a8d66c7dbc47cc24df7633a71>) for an example; there should be only two files to update.

```
## *-makefile*-
```

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```

```
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```

```
## Commands to generate dependency files
```

```
GEN_DEPS.c= $(CC) -E -MM $(DEFS) $(CPPFLAGS)
```

```
GEN_DEPS.cc= $(CXX) -E -MM $(DEFS) $(CPPFLAGS)
```

```
## Flags for position independent code
```

```
SHAREDLIBCFLAGS = -fPIC
```

```
SHAREDLIBCXXFLAGS = -fPIC
```

```
SHAREDLIBCPPFLAGS = -DPIC
```

```
## Additional flags when building libraries and with threads
```

```
THREADSCPPFLAGS = -D_REENTRANT
```

```
LIBCPPFLAGS =
```

```
## Compiler switch to embed a runtime search path
```

```
LD_RPATH=
```

```
LD_RPATH_PRE= -Wl,-rpath,
```

```
## Compiler switch to embed a library name
```

```
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))
```

```
## Shared library options
```

```
LD_SOOPTIONS= -Wl,-Bsymbolic
```

```
## Shared object suffix
```

```
SO = so
```

```
## Non-shared intermediate object suffix
```

```
STATIC_O = ao
```

```
## Compilation rules
```

```
%. $(STATIC_O): $(srcdir)/%.c
```

```
$(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<
```

```
%.o: $(srcdir)/%.c
```

```
$(COMPILE.c) $(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $<
```

```
%. $(STATIC_O): $(srcdir)/%.cpp
$(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<
%.o: $(srcdir)/%.cpp
$(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<
```

Dependency rules

```
%.d: $(srcdir)/%.c
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.c) $< \
| sed "s^\($*\)\.o[ :]*^1.o $@ : /g'" > $@; \
[ -s $@ ] || rm -f $@'
```

```
%.d: $(srcdir)/%.cpp
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.cc) $< \
| sed "s^\($*\)\.o[ :]*^1.o $@ : /g'" > $@; \
[ -s $@ ] || rm -f $@'
```

Versioned libraries rules

```
%. $(SO).$(SO_TARGET_VERSION_MAJOR): %. $(SO).$(SO_TARGET_VERSION)
$(RM) $@ && ln -s ${<F} $@
%. $(SO): %. $(SO).$(SO_TARGET_VERSION_MAJOR)
$(RM) $@ && ln -s ${*F}. $(SO).$(SO_TARGET_VERSION) $@
```

Bind internal references

```
# LDflags that pkgdata will use
BIR_LDFLAGS= -Wl,-Bsymbolic
```

```
# Dependencies [i.e. map files] for the final library
BIR_DEPS=
```

```
## Remove shared library 's'
STATIC_PREFIX_WHEN_USED =
STATIC_PREFIX =
```

End BSD-specific setup

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1.303 tcpdump 4.9.3

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1.304 logrus 1.5.0

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1.305 tomcat-tribes 8.5.32

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1.306 aop-alliance 2.5.0

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1.307 commons-lang3 3.1

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1.308 bcel 2.7.2

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 */
/*
 * $Id: Version.src 1225426 2011-12-29 04:13:08Z mrglavas $
 */
package org.apache.xalan;

/**
 * Administrative class to keep track of the version number of
 * the Xalan release.
 *
 * <P>This class implements the upcoming standard of having
 * org.apache.project-name.Version.getVersion() be a standard way
 * to get version information. This class will replace the older
 * org.apache.xalan.processor.Version class.</P>
 *
 * <P>See also: org/apache/xalan/res/XSLTInfo.properties for
 * information about the version of the XSLT spec we support.</P>
 *
 * @xsl.usage general
 */
```

```

public class Version
{

/**
 * Get the basic version string for the current Xalan release.
 * Version String formatted like
 * <CODE>"<B>Xalan</B> <B>Java</B> v.r[.dd| <B>D</B>nn]"</CODE>.
 *
 * Futurework: have this read version info from jar manifest.
 *
 * @return String denoting our current version
 */
public static String getVersion()
{
    return getProduct()+" "+getImplementationLanguage()+" "
        +getMajorVersionNum()+"."+getReleaseVersionNum()+"."
        +( getDevelopmentVersionNum() > 0) ?
        ("D"+getDevelopmentVersionNum()) : (""+getMaintenanceVersionNum());
}

/**
 * Print the processor version to the command line.
 *
 * @param argv command line arguments, unused.
 */
public static void main(String argv[])
{
    System.out.println(getVersion());
}

/**
 * Name of product: Xalan.
 */
public static String getProduct()
{
    return "Xalan";
}

/**
 * Implementation Language: Java.
 */
public static String getImplementationLanguage()
{
    return "Java";
}

/**

```

* Major version number.
* Version number. This changes only when there is a
* significant, externally apparent enhancement from
* the previous release. 'n' represents the n'th
* version.
*
* Clients should carefully consider the implications
* of new versions as external interfaces and behaviour
* may have changed.

```
*/  
public static int getMajorVersionNum()  
{  
    return @version.VERSION@;  
  
}
```

```
/**  
 * Release Number.  
 * Release number. This changes when:  
 * - a new set of functionality is to be added, eg,  
 *   implementation of a new W3C specification.  
 * - API or behaviour change.  
 * - its designated as a reference release.  
 */
```

```
public static int getReleaseVersionNum()  
{  
    return @version.RELEASE@;  
}
```

```
/**  
 * Maintenance Drop Number.  
 * Optional identifier used to designate maintenance  
 * drop applied to a specific release and contains  
 * fixes for defects reported. It maintains compatibility  
 * with the release and contains no API changes.  
 * When missing, it designates the final and complete  
 * development drop for a release.  
 */
```

```
public static int getMaintenanceVersionNum()  
{  
    return @version.MINOR@;  
}
```

```
/**  
 * Development Drop Number.  
 * Optional identifier designates development drop of  
 * a specific release. D01 is the first development drop  
 * of a new release.
```

```

*
*   Development drops are works in progress towards a
*   completed, final release. A specific development drop
*   may not completely implement all aspects of a new
*   feature, which may take several development drops to
*   complete. At the point of the final drop for the
*   release, the D suffix will be omitted.
*
*   Each 'D' drops can contain functional enhancements as
*   well as defect fixes. 'D' drops may not be as stable as
*   the final releases.
*/
public static int getDevelopmentVersionNum()
{
    try {
        if ((new String("@version.DEVELOPER@")).length() == 0)
            return 0;
        else
            return Integer.parseInt("@version.DEVELOPER@");
    } catch (NumberFormatException nfe) {
        return 0;
    }
}
}
}

```

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*/
/**
* This is a special exception that is used to stop parsing when

```

* search for an element. For instance, when searching for xml:stylesheet
* PIs, it is used to stop the parse once the document element is found.
* @see StylesheetPIHandler
* @xsl.usage internal
*/

Found in path(s):

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xml/utils/StopParseException.java

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Found in path(s):

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xalan/xsltc/compiler/XPathLexer.java

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```

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*/
/*
* $Id: Version.src 468654 2006-10-28 07:09:23Z minchau $
*/
package org.apache.xml.serializer;

/**
 * Administrative class to keep track of the version number of
 * the Serializer release.
 * <P>This class implements the upcoming standard of having
 * org.apache.project-name.Version.getVersion() be a standard way
 * to get version information.</P>
 * @xsl.usage general
 */
public final class Version
{

/**
 * Get the basic version string for the current Serializer.
 * Version String formatted like
 * <CODE>"<B>Serializer</B> <B>Java</B> v.r[.dd| <B>D</B>nn]"</CODE>.
 *
 * Futurework: have this read version info from jar manifest.
 *
 * @return String denoting our current version
 */
public static String getVersion()
{
    return getProduct()+" "+getImplementationLanguage()+" "
        +getMajorVersionNum()+"."+getReleaseVersionNum()+". "
        +( (getDevelopmentVersionNum() > 0) ?
            ("D"+getDevelopmentVersionNum()) : (""+getMaintenanceVersionNum()));
}

/**
 * Print the processor version to the command line.
 *
 * @param argv command line arguments, unused.
 */
public static void main(String argv[])
{
    System.out.println(getVersion());
}

/**
 * Name of product: Serializer.
 */

```

```

public static String getProduct()
{
    return "Serializer";
}

/**
 * Implementation Language: Java.
 */
public static String getImplementationLanguage()
{
    return "Java";
}

/**
 * Major version number.
 * Version number. This changes only when there is a
 * significant, externally apparent enhancement from
 * the previous release. 'n' represents the n'th
 * version.
 *
 * Clients should carefully consider the implications
 * of new versions as external interfaces and behaviour
 * may have changed.
 */
public static int getMajorVersionNum()
{
    return @version.VERSION@;
}

/**
 * Release Number.
 * Release number. This changes when:
 * - a new set of functionality is to be added, eg,
 *   implementation of a new W3C specification.
 * - API or behaviour change.
 * - its designated as a reference release.
 */
public static int getReleaseVersionNum()
{
    return @version.RELEASE@;
}

/**
 * Maintenance Drop Number.
 * Optional identifier used to designate maintenance
 * drop applied to a specific release and contains

```

```
*      fixes for defects reported. It maintains compatibility
*      with the release and contains no API changes.
*      When missing, it designates the final and complete
*      development drop for a release.
```

```
*/
```

```
public static int getMaintenanceVersionNum()
{
    return @version.MINOR@;
}
```

```
/**
```

```
* Development Drop Number.
* Optional identifier designates development drop of
* a specific release. D01 is the first development drop
* of a new release.
*
* Development drops are works in progress towards a
* completed, final release. A specific development drop
* may not completely implement all aspects of a new
* feature, which may take several development drops to
* complete. At the point of the final drop for the
* release, the D suffix will be omitted.
*
* Each 'D' drops can contain functional enhancements as
* well as defect fixes. 'D' drops may not be as stable as
* the final releases.
```

```
*/
```

```
public static int getDevelopmentVersionNum()
{
    try {
        if ((new String("@version.DEVELOPER@")).length() == 0)
            return 0;
        else
            return Integer.parseInt("@version.DEVELOPER@");
    } catch (NumberFormatException nfe) {
        return 0;
    }
}
}
```

Found in path(s):

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/serializer/Version.src

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```
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```

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*/

/**

* Major version number.
* Version number. This changes only when there is a
* significant, externally apparent enhancement from
* the previous release. 'n' represents the n'th
* version.
*
* Clients should carefully consider the implications
* of new versions as external interfaces and behaviour
* may have changed.

*/

Found in path(s):

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/serializer/Version.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/processor/XSLProcessorVersion.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/Version.java

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*/
// Attribution to: "Voytenko, Dimitry" <DVoytenko@SECTORBASE.COM>

Found in path(s):

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/serializer/ToHTMLStream.java

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*/

// Proprietary

/** The 'document-location()' id (Proprietary). */

Found in path(s):

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/compiler/FunctionTable.java

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Found in path(s):

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/serializer/Encodings.properties

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/serializer/XMLEntities.properties

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/serializer/output_text.properties

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/res/XSLTInfo.properties

```
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xml/serializer/output_xml.properties
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xalan/xsltc/compiler/Makefile.inc
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xml/serializer/output_unknown.properties
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xml/serializer/HTMLEntities.properties
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xml/serializer/output_html.properties
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*/
```

```
/*
* $Id: xpath.cup 1225752 2011-12-30 04:12:46Z mrglavas $
*/
```

```
/*
* @author Jacek Ambroziak
* @author Santiago Pericas-Geertsen
* @author Morten Jorgensen
* @author G. Todd Miller
*/
```

```
package org.apache.xalan.xsltc.compiler;
```

```
import java.util.Stack;
import java.util.Vector;
import java.io.StringReader;
import java_cup.runtime.*;
```

```
import org.apache.xml.dtm.DTM;
```

```

import org.apache.xalan.xsltc.DOM;
import org.apache.xml.dtm.Axis;
import org.apache.xalan.xsltc.runtime.Operators;
import org.apache.xalan.xsltc.compiler.util.ErrorMsg;

parser code {
    /**
     * Used by function calls with no args.
     */
    static public final Vector EmptyArgs = new Vector(0);

    /**
     * Reference to non-existing variable.
     */
    static public final VariableRef DummyVarRef = null;

    /**
     * Reference to the Parser class.
     */
    private Parser _parser;
    private XSLTC _xsltc;

    /**
     * String representation of the expression being parsed.
     */
    private String _expression;

    /**
     * Line number where this expression/pattern was declared.
     */
    private int _lineNumber = 0;

    /**
     * Reference to the symbol table.
     */
    public SymbolTable _symbolTable;

    public XPathParser(Parser parser) {
        _parser = parser;
        _xsltc = parser.getXSLTC();
        _symbolTable = parser.getSymbolTable();
    }

    public int getLineNumber() {
        return _lineNumber;
    }

    public QName getQNameIgnoreDefaultNs(String name) {

```

```

        return _parser.getQNameIgnoreDefaultNs(name);
    }

    public QName getQName(String namespace, String prefix, String localname) {
        return _parser.getQName(namespace, prefix, localname);
    }

    public void setMultiDocument(boolean flag) {
        _xslt.setMultiDocument(flag);
    }

    public void setCallsNodeset(boolean flag) {
        _xslt.setCallsNodeset(flag);
    }

    public void setHasIdCall(boolean flag) {
        _xslt.setHasIdCall(flag);
    }

    /**
     * This method is similar to findNodeType(int, Object) except that it
     * creates a StepPattern instead of just returning a node type. It also
     * differs in the way it handles "{uri}:*" and "{uri}:@*". The last two
     * patterns are expanded as "[namespace-uri() = 'uri']" and
     * "@*[namespace-uri() = 'uri']", respectively. This expansion considerably
     * simplifies the grouping of patterns in the Mode class. For this
     * expansion to be correct, the priority of the pattern/template must be
     * set to -0.25 (when no other predicates are present).
     */
    public StepPattern createStepPattern(int axis, Object test, Vector predicates) {
int nodeType;

        if (test == null) { // "*"
            nodeType = (axis == Axis.ATTRIBUTE) ? NodeTest.ATTRIBUTE :
                (axis == Axis.NAMESPACE) ? -1 : NodeTest.ELEMENT;

            return new StepPattern(axis, nodeType, predicates);
        }
        else if (test instanceof Integer) {
            nodeType = ((Integer) test).intValue();

            return new StepPattern(axis, nodeType, predicates);
        }
        else {
            QName name = (QName)test;
            boolean setPriority = false;

```

```

    if (axis == Axis.NAMESPACE) {
nodeType = (name.toString().equals("*")) ? -1
: _xslt.registerNamespacePrefix(name);
    }
    else {
final String uri = name.getNamespace();
final String local = name.getLocalPart();
final QName namespace_uri =
    _parser.getQNameIgnoreDefaultNs("namespace-uri");

// Expand {uri}:* to *[namespace-uri = 'uri'] - same for @*
if (uri != null && (local.equals("") || local.equals("@*"))) {
    if (predicates == null) {
predicates = new Vector(2);
    }

// Priority is set by hand if no other predicates exist
setPriority = (predicates.size() == 0);

    predicates.add(
new Predicate(
    new EqualityExpr(Operators.EQ,
new NamespaceUriCall(namespace_uri),
new LiteralExpr(uri)));
    }

if (local.equals("")) {
    nodeType = (axis == Axis.ATTRIBUTE) ? NodeTest.ATTRIBUTE
: NodeTest.ELEMENT;
    }
    else if (local.equals("@*")) {
    nodeType = NodeTest.ATTRIBUTE;
    }
    else {
    nodeType = (axis == Axis.ATTRIBUTE) ? _xslt.registerAttribute(name)
: _xslt.registerElement(name);
    }
    }

final StepPattern result = new StepPattern(axis, nodeType, predicates);

// Set priority for case prefix:* and prefix:@* (no predicates)
if (setPriority) {
result.setPriority(-0.25);
    }

return result;
}

```

```

}

public int findNodeType(int axis, Object test) {
if (test == null) { // *
    return (axis == Axis.ATTRIBUTE) ?
NodeTest.ATTRIBUTE :
(axis == Axis.NAMESPACE) ? -1 : NodeTest.ELEMENT;
    }
    else if (test instanceof Integer) {
        return ((Integer)test).intValue();
    }
    else {
QName name = (QName)test;

        if (axis == Axis.NAMESPACE) {
return (name.toString().equals("*")) ? -1
: _xslt.registerNamespacePrefix(name);
        }

        if (name.getNamespace() == null) {
final String local = name.getLocalPart();

if (local.equals("*")) {
    return (axis == Axis.ATTRIBUTE) ? NodeTest.ATTRIBUTE
: NodeTest.ELEMENT;
}
else if (local.equals("@*")) {
    return NodeTest.ATTRIBUTE;
}
}

        return (axis == Axis.ATTRIBUTE) ? _xslt.registerAttribute(name)
: _xslt.registerElement(name);
    }
}

/**
 * Parse the expression passed to the current scanner. If this
 * expression contains references to local variables and it will be
 * compiled in an external module (not in the main class) request
 * the current template to create a new variable stack frame.
 *
 * @param lineNumber Line where the current expression is defined.
 * @param external Set to <tt>true</tt> if this expression is
 * compiled in a separate module.
 *
 */
public Symbol parse(String expression, int lineNumber) throws Exception {

```

```

    try {
        _expression = expression;
        _lineNumber = lineNumber;
        return super.parse();
    }
    catch (IllegalCharException e) {
        ErrorMsg err = new ErrorMsg(ErrorMsg.ILLEGAL_CHAR_ERR,
                                    lineNumber, e.getMessage());
        _parser.reportError(Constants.FATAL, err);
    }
    return null;
}

/**
 * Lookup a variable or parameter in the symbol table given its name.
 *
 * @param name Name of the symbol being looked up.
 */
final SyntaxTreeNode lookupName(QName name) {
    // Is it a local var or param ?
    final SyntaxTreeNode result = _parser.lookupVariable(name);
    if (result != null)
        return(result);
    else
        return(_symbolTable.lookupName(name));
}

public final void addError(ErrorMsg error) {
    _parser.reportError(Constants.ERROR, error);
}

public void report_error(String message, Object info) {
    final ErrorMsg err = new ErrorMsg(ErrorMsg.SYNTAX_ERR, _lineNumber,
        _expression);
    _parser.reportError(Constants.FATAL, err);
}

public void report_fatal_error(String message, Object info) {
    // empty
}

public RelativeLocationPath insertStep(Step step, RelativeLocationPath rlp) {
    if (rlp instanceof Step) {
        return new ParentLocationPath(step, (Step) rlp);
    }
    else if (rlp instanceof ParentLocationPath) {
        final ParentLocationPath plp = (ParentLocationPath) rlp;
        final RelativeLocationPath newrlp = insertStep(step, plp.getPath());
    }
}

```

```

    return new ParentLocationPath(newrlp, plp.getStep());
}
else {
    addError(new ErrorMsg(ErrorMsg.INTERNAL_ERR, "XPathParser.insertStep"));
    return rlp;
}
}

/**
 * Returns true if the axis applies to elements only. The axes
 * child, attribute, namespace, descendant result in non-empty
 * nodesets only if the context node is of type element.
 */
public boolean isElementAxis(int axis) {
return (axis == Axis.CHILD || axis == Axis.ATTRIBUTE ||
axis == Axis.NAMESPACE || axis == Axis.DESCENDANT);
}
:}

```

```

terminal SLASH, DOT, LBRACK, RBRACK, VBAR, LPAREN, RPAREN, STAR, COMMA;
terminal DOLLAR, ATSIGN;
terminal DDOT, DCOLON, DSLASH;
terminal EQ, NE;
terminal LT, GT, LE, GE;
terminal PLUS, MINUS, DIV, MOD, MULT;
terminal String Literal;
terminal String QNAME;
terminal ID, KEY, TEXT, NODE, OR, AND, COMMENT, PI, PIPARAM, PRECEDINGSIBLING;
terminal SELF, PARENT, CHILD, ATTRIBUTE, ANCESTOR, ANCESTORORSELF, DESCENDANT;
terminal DESCENDANTORSELF, FOLLOWING, FOLLOWINGSIBLING, NAMESPACE, PRECEDING;
terminal Double REAL;
terminal Long INT;
terminal PATTERN, EXPRESSION;

```

```

non terminal SyntaxTreeNode TopLevel;

```

```

non terminal Expression Expr, Argument, LocationPath;
non terminal Expression Predicate, FilterExpr, Step;
non terminal Expression OrExpr, AndExpr, EqualityExpr;
non terminal Expression RelationalExpr, AdditiveExpr;
non terminal Expression MultiplicativeExpr, UnaryExpr;
non terminal Expression VariableReference, FunctionCall;
non terminal Expression PrimaryExpr, UnionExpr, PathExpr, AbbreviatedStep;
non terminal Expression RelativeLocationPath, AbbreviatedRelativeLocationPath;
non terminal Expression AbsoluteLocationPath, AbbreviatedAbsoluteLocationPath;

```

```

non terminal Object NodeTest, NameTest;

```

non terminal IdKeyPattern IdKeyPattern;
non terminal Pattern Pattern;
non terminal Pattern LocationPathPattern;
non terminal StepPattern ProcessingInstructionPattern;
non terminal RelativePathPattern RelativePathPattern;
non terminal StepPattern StepPattern;
non terminal Object NodeTestPattern, NameTestPattern;

non terminal Vector Predicates, NonemptyArgumentList;
non terminal QName QName, FunctionName, VariableName;
non terminal Integer AxisName, AxisSpecifier;
non terminal Integer ChildOrAttributeAxisSpecifier;

precedence left VBAR;
precedence left OR;
precedence left AND;
precedence nonassoc EQ, NE;
precedence left LT, GT, LE, GE;

precedence left PLUS, MINUS;
precedence left DIV, MOD, MULT;
precedence left DOLLAR;
precedence left ATSIGN;
precedence right DCOLON;

start with TopLevel;

TopLevel ::= PATTERN Pattern:pattern
{: RESULT = pattern; :}

| EXPRESSION Expr:expr
{: RESULT = expr; :};

/* ----- Patterns ----- */

Pattern ::= LocationPathPattern:lpp
{: RESULT = lpp; :}

| LocationPathPattern:lpp VBAR Pattern:p
{: RESULT = new AlternativePattern(lpp, p); :};

LocationPathPattern ::= SLASH
{: RESULT = new AbsolutePathPattern(null); :}

| SLASH RelativePathPattern:rpp
{: RESULT = new AbsolutePathPattern(rpp); :}

| IdKeyPattern:ikp

```

{: RESULT = ikp; :}

| IdKeyPattern:ikp SLASH RelativePathPattern:rpp
{: RESULT = new ParentPattern(ikp, rpp); :}

| IdKeyPattern:ikp DSLASH RelativePathPattern:rpp
{: RESULT = new AncestorPattern(ikp, rpp); :}

| DSLASH RelativePathPattern:rpp
{: RESULT = new AncestorPattern(rpp); :}

| RelativePathPattern:rpp
{: RESULT = rpp; :};

IdKeyPattern ::= ID LPAREN Literal:l RPAREN
{: RESULT = new IdPattern(l);
  parser.setHasIdCall(true);
:}

| KEY LPAREN Literal:l1 COMMA Literal:l2 RPAREN
{: RESULT = new KeyPattern(l1, l2); :};

ProcessingInstructionPattern ::= PIPARAM LPAREN Literal:l RPAREN
{: RESULT = new ProcessingInstructionPattern(l); :};

RelativePathPattern ::= StepPattern:sp
{: RESULT = sp; :}

| StepPattern:sp SLASH RelativePathPattern:rpp
{: RESULT = new ParentPattern(sp, rpp); :}

| StepPattern:sp DSLASH RelativePathPattern:rpp
{: RESULT = new AncestorPattern(sp, rpp); :};

StepPattern ::= NodeTestPattern:nt
{:
  RESULT = parser.createStepPattern(Axis.CHILD, nt, null);
:}

| NodeTestPattern:nt Predicates:pp
{:
  RESULT = parser.createStepPattern(Axis.CHILD, nt, pp);
:}

| ProcessingInstructionPattern:pip
{: RESULT = pip; :}

| ProcessingInstructionPattern:pip Predicates:pp

```

```

{: RESULT = (ProcessingInstructionPattern)pip.setPredicates(pp); :}

| ChildOrAttributeAxisSpecifier:axis NodeTestPattern:nt
{:
RESULT = parser.createStepPattern(axis.intValue(), nt, null);
:}

| ChildOrAttributeAxisSpecifier:axis
NodeTestPattern:nt Predicates:pp
{:
RESULT = parser.createStepPattern(axis.intValue(), nt, pp);
:}

| ChildOrAttributeAxisSpecifier:axis ProcessingInstructionPattern:pip
{:
RESULT = pip; // TODO: report error if axis is attribute
:}

| ChildOrAttributeAxisSpecifier:axis ProcessingInstructionPattern:pip
Predicates:pp
{:
// TODO: report error if axis is attribute
RESULT = (ProcessingInstructionPattern)pip.setPredicates(pp);
:};

NodeTestPattern ::= NameTestPattern:nt
{: RESULT = nt; :}

| NODE
{: RESULT = new Integer(NodeTest.ANODE); :}

| TEXT
{: RESULT = new Integer(NodeTest.TEXT); :}

| COMMENT
{: RESULT = new Integer(NodeTest.COMMENT); :}

| PI
{: RESULT = new Integer(NodeTest.PI); :};

NameTestPattern ::= STAR
{: RESULT = null; :}

| QName:qn
{: RESULT = qn; :};

ChildOrAttributeAxisSpecifier ::= ATSIGN
{: RESULT = new Integer(Axis.ATTRIBUTE); :}

```

```
| CHILD DCOLON
{: RESULT = new Integer(Axis.CHILD); :}
```

```
| ATTRIBUTE DCOLON
{: RESULT = new Integer(Axis.ATTRIBUTE); :};
```

```
Predicates ::= Predicate:p
```

```
{:
Vector temp = new Vector();
temp.addElement(p);
RESULT = temp;
:}
```

```
| Predicate:p Predicates:pp
{: pp.insertElementAt(p, 0); RESULT = pp; :};
```

```
Predicate ::= LBRACK Expr:e RBRACK
```

```
{:
RESULT = new Predicate(e);
:};
```

```
/* ----- Expressions ----- */
```

```
Expr ::= OrExpr:ex
```

```
{: RESULT = ex; :};
```

```
OrExpr ::= AndExpr:ae
```

```
{: RESULT = ae; :}
```

```
| OrExpr:oe OR AndExpr:ae
```

```
{: RESULT = new LogicalExpr(LogicalExpr.OR, oe, ae); :};
```

```
AndExpr ::= EqualityExpr:e
```

```
{: RESULT = e; :}
```

```
| AndExpr:ae AND EqualityExpr:ee
```

```
{: RESULT = new LogicalExpr(LogicalExpr.AND, ae, ee); :};
```

```
EqualityExpr ::= RelationalExpr:re
```

```
{: RESULT = re; :}
```

```
| EqualityExpr:ee EQ RelationalExpr:re
```

```
{: RESULT = new EqualityExpr(Operators.EQ, ee, re); :}
```

```
| EqualityExpr:ee NE RelationalExpr:re
```

```
{: RESULT = new EqualityExpr(Operators.NE, ee, re); :};
```

```

RelationalExpr ::= AdditiveExpr:ae
    { : RESULT = ae; : }

    | RelationalExpr:re LT AdditiveExpr:ae
    { : RESULT = new RelationalExpr(Operators.LT, re, ae); : }

    | RelationalExpr:re GT AdditiveExpr:ae
    { : RESULT = new RelationalExpr(Operators.GT, re, ae); : }

    | RelationalExpr:re LE AdditiveExpr:ae
    { : RESULT = new RelationalExpr(Operators.LE, re, ae); : }

    | RelationalExpr:re GE AdditiveExpr:ae
    { : RESULT = new RelationalExpr(Operators.GE, re, ae); : };

AdditiveExpr ::= MultiplicativeExpr:me
    { : RESULT = me; : }

    | AdditiveExpr:ae PLUS MultiplicativeExpr:me
    { : RESULT = new BinOpExpr(BinOpExpr.PLUS, ae, me); : }

    | AdditiveExpr:ae MINUS MultiplicativeExpr:me
    { : RESULT = new BinOpExpr(BinOpExpr.MINUS, ae, me); : };

MultiplicativeExpr ::= UnaryExpr:ue
    { : RESULT = ue; : }

    | MultiplicativeExpr:me MULT UnaryExpr:ue
    { : RESULT = new BinOpExpr(BinOpExpr.TIMES, me, ue); : }

    | MultiplicativeExpr:me DIV UnaryExpr:ue
    { : RESULT = new BinOpExpr(BinOpExpr.DIV, me, ue); : }

    | MultiplicativeExpr:me MOD UnaryExpr:ue
    { : RESULT = new BinOpExpr(BinOpExpr.MOD, me, ue); : };

UnaryExpr ::= UnionExpr:ue
    { : RESULT = ue; : }

    | MINUS UnaryExpr:ue
    { : RESULT = new UnaryOpExpr(ue); : };

UnionExpr ::= PathExpr:pe
    { : RESULT = pe; : }

    | PathExpr:pe VBAR UnionExpr:rest
    { : RESULT = new UnionPathExpr(pe, rest); : };

```

```

PathExpr ::= LocationPath:lp
    { : RESULT = lp; : }

    | FilterExpr:fexp
    { : RESULT = fexp; : }

    | FilterExpr:fexp SLASH RelativeLocationPath:rlp
    { : RESULT = new FilterParentPath(fexp, rlp); : }

    | FilterExpr:fexp DSLASH RelativeLocationPath:rlp
    { :
        //
        // Expand '/' into '/descendant-or-self::node()' or
        // into '/descendant-or-self:*/
        //
        int nodeType = DOM.NO_TYPE;
        if (rlp instanceof Step &&
            parser.isElementAxis(((Step) rlp).getAxis()))
        {
            nodeType = DTM.ELEMENT_NODE;
        }
        final Step step = new Step(Axis.DESCENDANTORSELF, nodeType, null);
        FilterParentPath fpp = new FilterParentPath(fexp, step);
        fpp = new FilterParentPath(fpp, rlp);
        if (fexp instanceof KeyCall == false) {
            fpp.setDescendantAxis();
        }
        RESULT = fpp;
    };

```

```

LocationPath ::= RelativeLocationPath:rlp
    { : RESULT = rlp; : }

```

```

    | AbsoluteLocationPath:alp
    { : RESULT = alp; : };

```

```

RelativeLocationPath ::= Step:step
    { : RESULT = step; : }

```

```

    | RelativeLocationPath:rlp SLASH Step:step
    { :
        if (rlp instanceof Step && ((Step) rlp).isAbbreviatedDot()) {
            RESULT = step;    // Remove './' from the middle
        }
        else if (((Step) step).isAbbreviatedDot()) {
            RESULT = rlp;    // Remove './' from the end
        }
        else {

```

```

    RESULT =
        new ParentLocationPath((RelativeLocationPath) rlp, step);
    }
    :}

```

```

| AbbreviatedRelativeLocationPath:arlp
{: RESULT = arlp; :};

```

AbsoluteLocationPath ::= SLASH

```

{: RESULT = new AbsoluteLocationPath(); :}

```

```

| SLASH RelativeLocationPath:rlp
{: RESULT = new AbsoluteLocationPath(rlp); :}

```

```

| AbbreviatedAbsoluteLocationPath:aalp
{: RESULT = aalp; :};

```

AbbreviatedRelativeLocationPath ::= RelativeLocationPath:rlp DSLASH Step:step

```

{:
final Step right = (Step)step;
    final int axis = right.getAxis();
    final int type = right.getNodeType();
    final Vector predicates = right.getPredicates();

    if ((axis == Axis.CHILD) && (type != NodeTest.ATTRIBUTE)) {
        // Compress '//child:E' into 'descendant::E' - if possible
        if (predicates == null) {
            right.setAxis(Axis.DESCENDANT);
            if (rlp instanceof Step && ((Step)rlp).isAbbreviatedDot()) {
                RESULT = right;
            }
        }
        else {
            // Expand 'rlp//child::E' into 'rlp/descendant::E'
            RelativeLocationPath left = (RelativeLocationPath)rlp;
            RESULT = new ParentLocationPath(left, right);
        }
    }
    else {
        // Expand '//step' -> 'descendant-or-self::*/*step'
        if (rlp instanceof Step && ((Step)rlp).isAbbreviatedDot()) {
            Step left = new Step(Axis.DESCENDANTORSELF,
DTM.ELEMENT_NODE, null);
            RESULT = new ParentLocationPath(left, right);
        }
        else {
            // Expand 'rlp//step' -> 'rlp/descendant-or-self::*/*step'
            RelativeLocationPath left = (RelativeLocationPath)rlp;
            Step mid = new Step(Axis.DESCENDANTORSELF,

```

```

DTM.ELEMENT_NODE, null);
    ParentLocationPath ppl = new ParentLocationPath(mid, right);
    RESULT = new ParentLocationPath(left, ppl);
}
}
}
else if ((axis == Axis.ATTRIBUTE) || (type == NodeTest.ATTRIBUTE)) {
    // Expand 'rlp//step' -> 'rlp/descendant-or-self::*/*step'
    RelativeLocationPath left = (RelativeLocationPath)rlp;
    Step middle = new Step(Axis.DESCENDANTORSELF,
DTM.ELEMENT_NODE, null);
    ParentLocationPath ppl = new ParentLocationPath(middle, right);
    RESULT = new ParentLocationPath(left, ppl);
}
else {
    // Expand 'rlp//step' -> 'rlp/descendant-or-self::node()/step'
    RelativeLocationPath left = (RelativeLocationPath)rlp;
    Step middle = new Step(Axis.DESCENDANTORSELF,
DOM.NO_TYPE, null);
    ParentLocationPath ppl = new ParentLocationPath(middle, right);
    RESULT = new ParentLocationPath(left, ppl);
}
};

```

AbbreviatedAbsolutePath ::= DSLASH RelativeLocationPath:rlp

```

{:
    //
    // Expand '/' into '/descendant-or-self::node()' or
    // into '/descendant-or-self::*/*'
    //
    int nodeType = DOM.NO_TYPE;
    if (rlp instanceof Step &&
        parser.isElementAxis(((Step) rlp).getAxis()))
    {
        nodeType = DTM.ELEMENT_NODE;
    }
    final Step step = new Step(Axis.DESCENDANTORSELF, nodeType, null);
    RESULT = new AbsoluteLocationPath(parser.insertStep(step,
(RelativeLocationPath) rlp));
};

```

Step ::= NodeTest:ntest

```

{:
    if (ntest instanceof Step) {
        RESULT = (Step)ntest;
    }
    else {

```

```

RESULT = new Step(Axis.CHILD,
    parser.findNodeType(Axis.CHILD, ntest),
    null);
}
:}

| NodeTest:ntest Predicates:pp
{:
    if (ntest instanceof Step) {
        Step step = (Step)ntest;
        step.addPredicates(pp);
        RESULT = (Step)ntest;
    }
    else {
        RESULT = new Step(Axis.CHILD,
            parser.findNodeType(Axis.CHILD, ntest), pp);
    }
:}

| AxisSpecifier:axis NodeTest:ntest Predicates:pp
{: RESULT = new Step(axis.intValue(),
    parser.findNodeType(axis.intValue(), ntest),
    pp);
:}

| AxisSpecifier:axis NodeTest:ntest
{: RESULT = new Step(axis.intValue(),
    parser.findNodeType(axis.intValue(), ntest),
    null);
:}

| AbbreviatedStep:abbrev
{: RESULT = abbrev; :};

AxisSpecifier ::= AxisName:an DCOLON
    {: RESULT = an; :}

| ATSIGN
    {: RESULT = new Integer(Axis.ATTRIBUTE); :};

AxisName ::= ANCESTOR
    {: RESULT = new Integer(Axis.ANCESTOR); :}

| ANCESTORORSELF
    {: RESULT = new Integer(Axis.ANCESTORORSELF); :}

| ATTRIBUTE
    {: RESULT = new Integer(Axis.ATTRIBUTE); :}

```

```

| CHILD
{: RESULT = new Integer(Axis.CHILD); :}

| DESCENDANT
{: RESULT = new Integer(Axis.DESCENDANT); :}

| DESCENDANTORSELF
{: RESULT = new Integer(Axis.DESCENDANTORSELF); :}

| FOLLOWING
{: RESULT = new Integer(Axis.FOLLOWING); :}

| FOLLOWINGSIBLING
{: RESULT = new Integer(Axis.FOLLOWINGSIBLING); :}

| NAMESPACE
{: RESULT = new Integer(Axis.NAMESPACE); :}

| PARENT
{: RESULT = new Integer(Axis.PARENT); :}

| PRECEDING
{: RESULT = new Integer(Axis.PRECEDING); :}

| PRECEDINGSIBLING
{: RESULT = new Integer(Axis.PRECEDINGSIBLING); :}

| SELF
{: RESULT = new Integer(Axis.SELF); :};

```

```

AbbreviatedStep ::= DOT
{: RESULT = new Step(Axis.SELF, NodeTest.ANODE, null); :}

```

```

| DDOT
{: RESULT = new Step(Axis.PARENT, NodeTest.ANODE, null); :};

```

```

FilterExpr ::= PrimaryExpr:primary
{: RESULT = primary; :}

```

```

| PrimaryExpr:primary Predicates:pp
{: RESULT = new FilterExpr(primary, pp); :};

```

```

PrimaryExpr ::= VariableReference:vr
{: RESULT = vr; :}

```

```

| LPAREN Expr:ex RPAREN
{: RESULT = ex; :}

```

```

| Literal:string
{:
/*
* If the string appears to have the syntax of a QName, store
* namespace info in the literal expression. This is used for
* element-available and function-available functions, among
* others. Also, the default namespace must be ignored.
*/
String namespace = null;
final int index = string.lastIndexOf(':');

if (index > 0) {
    final String prefix = string.substring(0, index);
    namespace = parser._symbolTable.lookupNamespace(prefix);
}
RESULT = (namespace == null) ? new LiteralExpr(string)
    : new LiteralExpr(string, namespace);
:}

| INT:num
{:
long value = num.longValue();
if (value < Integer.MIN_VALUE || value > Integer.MAX_VALUE) {
RESULT = new RealExpr(value);
}
else {
    if (num.doubleValue() == -0)
        RESULT = new RealExpr(num.doubleValue());
    else if (num.intValue() == 0)
        RESULT = new IntExpr(num.intValue());
    else if (num.doubleValue() == 0.0)
        RESULT = new RealExpr(num.doubleValue());
    else
        RESULT = new IntExpr(num.intValue());
}
:}

| REAL:num
{: RESULT = new RealExpr(num.doubleValue()); :}

| FunctionCall:fc
{: RESULT = fc; :};

VariableReference ::= DOLLAR VariableName:varName
{:
    // An empty qname prefix for a variable or parameter reference
    // should map to the null namespace and not the default URI.

```

```

SyntaxTreeNode node = parser.lookupName(varName);

if (node != null) {
    if (node instanceof Variable) {
        RESULT = new VariableRef((Variable)node);
    }
    else if (node instanceof Param) {
        RESULT = new ParameterRef((Param)node);
    }
    else {
        RESULT = new UnresolvedRef(varName);
    }
}

if (node == null) {
    RESULT = new UnresolvedRef(varName);
}
:};

```

FunctionCall ::= FunctionName:fname LPAREN RPAREN

```

{:

    if (parser.getQNameIgnoreDefaultNs("current").equals(fname)) {
        RESULT = new CurrentCall(fname);
    }
    else if (parser.getQNameIgnoreDefaultNs("number").equals(fname)) {
        RESULT = new NumberCall(fname, parser.EmptyArgs);
    }
    else if (parser.getQNameIgnoreDefaultNs("string").equals(fname)) {
        RESULT = new StringCall(fname, parser.EmptyArgs);
    }
    else if (parser.getQNameIgnoreDefaultNs("concat").equals(fname)) {
        RESULT = new ConcatCall(fname, parser.EmptyArgs);
    }
    else if (parser.getQNameIgnoreDefaultNs("true").equals(fname)) {
        RESULT = new BooleanExpr(true);
    }
    else if (parser.getQNameIgnoreDefaultNs("false").equals(fname)) {
        RESULT = new BooleanExpr(false);
    }
    else if (parser.getQNameIgnoreDefaultNs("name").equals(fname)) {
        RESULT = new NameCall(fname);
    }
    else if (parser.getQNameIgnoreDefaultNs("generate-id").equals(fname)) {
        RESULT = new GenerateIdCall(fname, parser.EmptyArgs);
    }
    else if (parser.getQNameIgnoreDefaultNs("string-length").equals(fname)) {
        RESULT = new StringLengthCall(fname, parser.EmptyArgs);
    }
}

```

```

}
else if (parser.getQNameIgnoreDefaultNs("position").equals(fname)) {
    RESULT = new PositionCall(fname);
}
else if (parser.getQNameIgnoreDefaultNs("last").equals(fname)) {
    RESULT = new LastCall(fname);
}
else if (parser.getQNameIgnoreDefaultNs("local-name").equals(fname)) {
    RESULT = new LocalNameCall(fname);
}
else if (parser.getQNameIgnoreDefaultNs("namespace-uri").equals(fname)) {
    RESULT = new NamespaceUriCall(fname);
}
else {
    RESULT = new FunctionCall(fname, parser.EmptyArgs);
}
:}

```

| FunctionName:fname LPAREN NonemptyArgumentList:argl RPAREN

```

{:
if (parser.getQNameIgnoreDefaultNs("concat").equals(fname)) {
    RESULT = new ConcatCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("number").equals(fname)) {
    RESULT = new NumberCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("document").equals(fname)) {
parser.setMultiDocument(true);
    RESULT = new DocumentCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("string").equals(fname)) {
    RESULT = new StringCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("boolean").equals(fname)) {
    RESULT = new BooleanCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("name").equals(fname)) {
    RESULT = new NameCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("generate-id").equals(fname)) {
    RESULT = new GenerateIdCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("not").equals(fname)) {
    RESULT = new NotCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("format-number").equals(fname)) {
    RESULT = new FormatNumberCall(fname, argl);
}
}

```

```

else if (parser.getQNameIgnoreDefaultNs("unparsed-entity-uri").equals(fname)) {
    RESULT = new UnparsedEntityUriCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("key").equals(fname)) {
    RESULT = new KeyCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("id").equals(fname)) {
    RESULT = new KeyCall(fname, argl);
    parser.setHasIdCall(true);
}
else if (parser.getQNameIgnoreDefaultNs("ceiling").equals(fname)) {
    RESULT = new CeilingCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("round").equals(fname)) {
    RESULT = new RoundCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("floor").equals(fname)) {
    RESULT = new FloorCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("contains").equals(fname)) {
    RESULT = new ContainsCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("string-length").equals(fname)) {
    RESULT = new StringLengthCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("starts-with").equals(fname)) {
    RESULT = new StartsWithCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("function-available").equals(fname)) {
    RESULT = new FunctionAvailableCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("element-available").equals(fname)) {
    RESULT = new ElementAvailableCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("local-name").equals(fname)) {
    RESULT = new LocalNameCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("lang").equals(fname)) {
    RESULT = new LangCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("namespace-uri").equals(fname)) {
    RESULT = new NamespaceUriCall(fname, argl);
}
else if (parser.getQName(Constants.TRANSLET_URI, "xsltc", "cast").equals(fname)) {
    RESULT = new CastCall(fname, argl);
}
}
// Special case for extension function nodeset()
else if (fname.getLocalPart().equals("nodeset") || fname.getLocalPart().equals("node-set")) {

```

```

parser.setCallsNodeset(true); // implies MultiDOM
    RESULT = new FunctionCall(fname, argl);
}
else {
    RESULT = new FunctionCall(fname, argl);
}
:};

```

NonemptyArgumentList ::= Argument:arg

```

{:
    Vector temp = new Vector();
    temp.addElement(arg);
    RESULT = temp;
:}

```

```

| Argument:arg COMMA NonemptyArgumentList:argl
{: argl.insertElementAt(arg, 0); RESULT = argl; :};

```

FunctionName ::= QName:fname

```

{:
    RESULT = fname;
:};

```

VariableName ::= QName:vname

```

{:
    RESULT = vname;
:};

```

Argument ::= Expr:ex

```

{: RESULT = ex; :};

```

NodeTest ::= NameTest:nt

```

{: RESULT = nt; :}

```

```

| NODE

```

```

{: RESULT = new Integer(NodeTest.ANODE); :}

```

```

| TEXT

```

```

{: RESULT = new Integer(NodeTest.TEXT); :}

```

```

| COMMENT

```

```

{: RESULT = new Integer(NodeTest.COMMENT); :}

```

```

| PIPARAM LPAREN Literal:l RPAREN

```

```

{:
    QName name = parser.getQNameIgnoreDefaultNs("name");
    Expression exp = new EqualityExpr(Operators.EQ,
        new NameCall(name),

```

```

        new LiteralExpr(l));
    Vector predicates = new Vector();
    predicates.addElement(new Predicate(exp));
    RESULT = new Step(Axis.CHILD, NodeTest.PI, predicates);
:}

| PI
{: RESULT = new Integer(NodeTest.PI); :};

NameTest ::= STAR
    {: RESULT = null; :}

    | QName:qn
    {: RESULT = qn; :};

QName ::= QNAME:qname
    {: RESULT = parser.getQNameIgnoreDefaultNs(qname); :}

| DIV
    {: RESULT = parser.getQNameIgnoreDefaultNs("div"); :}

| MOD
    {: RESULT = parser.getQNameIgnoreDefaultNs("mod"); :}

| KEY
    {: RESULT = parser.getQNameIgnoreDefaultNs("key"); :}

| ANCESTOR
    {: RESULT = parser.getQNameIgnoreDefaultNs("child"); :}

| ANCESTORORSELF
    {: RESULT = parser.getQNameIgnoreDefaultNs("ancestor-or-self"); :}

| ATTRIBUTE
    {: RESULT = parser.getQNameIgnoreDefaultNs("attribute"); :}

| CHILD
    {: RESULT = parser.getQNameIgnoreDefaultNs("child"); :}

| DESCENDANT
    {: RESULT = parser.getQNameIgnoreDefaultNs("decendant"); :}

| DESCENDANTORSELF
    {: RESULT = parser.getQNameIgnoreDefaultNs("decendant-or-self"); :}

| FOLLOWING
    {: RESULT = parser.getQNameIgnoreDefaultNs("following"); :}

```

| FOLLOWINGSIBLING
{: RESULT = parser.getQNameIgnoreDefaultNs("following-sibling"); :}

| NAMESPACE
{: RESULT = parser.getQNameIgnoreDefaultNs("namespace"); :}

| PARENT
{: RESULT = parser.getQNameIgnoreDefaultNs("parent"); :}

| PRECEDING
{: RESULT = parser.getQNameIgnoreDefaultNs("preceding"); :}

| PRECEDINGSIBLING
{: RESULT = parser.getQNameIgnoreDefaultNs("preceding-sibling"); :}

| SELF
{: RESULT = parser.getQNameIgnoreDefaultNs("self"); :}

| ID
{: RESULT = parser.getQNameIgnoreDefaultNs("id"); :};

Found in path(s):

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/xpath.cup

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*/

// is found, then throw a special exception in order to terminate

Found in path(s):

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/processor/TransformerFactoryImpl.java

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*/

Found in path(s):

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xpath/domapi/XPathStylesheetDOM3Exception.java
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*/

/**

* Execute the proprietary document-location() function, which returns
* a node set of documents.
* @xsl.usage advanced
*/

Found in path(s):

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xpath/functions/FuncDoclocation.java
No license file was found, but licenses were detected in source scan.

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```

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*/
/*
* $Id: xpath.lex 1225752 2011-12-30 04:12:46Z mrglavas $
*/

```

```

/*
* @author Jacek Ambroziak
* @author Santiago Pericas-Geertsen
* @author Morten Jorgensen
*
*/

```

```
package org.apache.xalan.xsltc.compiler;
```

```

import java_cup.runtime.Symbol;
%%
%cup
%unicode
%class XPathLexer
%yyeof

%{
    int last;

    void initialize() {
        last = -1;
    }

    static boolean isWhitespace(int c) {
        return (c == ' ' || c == '\t' || c == '\r' || c == '\n' || c == '\f');
    }

/**
 * If symbol is not followed by ':' or '(', then treat it as a

```

```

* name instead of an axis or function (Jira-1912).
*/
Symbol disambiguateAxisOrFunction(int ss) throws Exception {
    // Peek in the input buffer without changing the internal state
    int index = yy_buffer_index;

    // Skip whitespace
    while (index < yy_buffer_read && isWhitespace(yy_buffer[index])) {
        index++;
    }

    // If end of buffer, can't disambiguate :(
    if (index >= yy_buffer_read) {
        // Can't disambiguate, so return as symbol
        return new Symbol(ss);
    }

    // Return symbol if next token is ':' or '('
    return (yy_buffer[index] == ':' && yy_buffer[index+1] == ':' ||
            yy_buffer[index] == '(') ?
        newSymbol(ss) : newSymbol(sym.QNAME, yytext());
}

/**
 * If symbol is first token or if it follows any of the operators
 * listed in http://www.w3.org/TR/xpath#exprlex then treat as a
 * name instead of a keyword (Jira-1912).
 */
Symbol disambiguateOperator(int ss) throws Exception {
    switch (last) {
    case -1: // first token
    case sym.ATSIGN:
    case sym.DCOLON:
    case sym.LPAREN:
    case sym.LBRACK:
    case sym.COMMA:
    case sym.AND:
    case sym.OR:
    case sym.MOD:
    case sym.DIV:
    case sym.MULT:
    case sym.SLASH:
    case sym.DSLASH:
    case sym.VBAR:
    case sym.PLUS:
    case sym.MINUS:
    case sym.EQ:
    case sym.NE:

```

```

    case sym.LT:
    case sym.LE:
    case sym.GT:
    case sym.GE:
        return newSymbol(sym.QNAME, yytext());
    }
    return newSymbol(ss);
}

/**
 * If symbol is first token or if it follows any of the operators
 * listed in http://www.w3.org/TR/xpath#exprlex then treat as a
 * wildcard instead of a multiplication operator
 */
Symbol disambiguateStar() throws Exception {
    switch (last) {
    case -1: // first token
    case sym.ATSIGN:
    case sym.DCOLON:
    case sym.LPAREN:
    case sym.LBRACK:
    case sym.COMMA:
    case sym.AND:
    case sym.OR:
    case sym.MOD:
    case sym.DIV:
    case sym.MULT:
    case sym.SLASH:
    case sym.DSLASH:
    case sym.VBAR:
    case sym.PLUS:
    case sym.MINUS:
    case sym.EQ:
    case sym.NE:
    case sym.LT:
    case sym.LE:
    case sym.GT:
    case sym.GE:
        return newSymbol(sym.STAR);
    }
    return newSymbol(sym.MULT);
}

Symbol newSymbol(int ss) {
    last = ss;
    return new Symbol(ss);
}

```

```

Symbol newSymbol(int ss, String value) {
    last = ss;
    return new Symbol(ss, value);
}

Symbol newSymbol(int ss, Long value) {
    last = ss;
    return new Symbol(ss, value);
}

Symbol newSymbol(int ss, Double value) {
    last = ss;
    return new Symbol(ss, value);
}

% }

%eofval{
return newSymbol(sym.EOF);
%eofval}

%yylexthrow{
Exception
%yylexthrow}

Letter={BaseChar}|{Ideographic}

BaseChar=[\u0041-\u005A\u0061-\u007A\u00C0-\u00D6\u00D8-\u00F6\u00F8-\u00FF\u0100-\u0131\u0134-
\u013E\u0141-\u0148\u014A-\u017E\u0180-\u01C3\u01CD-\u01F0\u01F4-\u01F5\u01FA-\u0217\u0250-
\u02A8\u02BB-\u02C1\u0386\u0388-\u038A\u038C\u038E-\u03A1\u03A3-\u03CE\u03D0-
\u03D6\u03DA\u03DC\u03DE\u03E0\u03E2-\u03F3\u0401-\u040C\u040E-\u044F\u0451-\u045C\u045E-
\u0481\u0490-\u04C4\u04C7-\u04C8\u04CB-\u04CC\u04D0-\u04EB\u04EE-\u04F5\u04F8-\u04F9\u0531-
\u0556\u0559\u0561-\u0586\u05D0-\u05EA\u05F0-\u05F2\u0621-\u063A\u0641-\u064A\u0671-\u06B7\u06BA-
\u06BE\u06C0-\u06CE\u06D0-\u06D3\u06D5\u06E5-\u06E6\u0905-\u0939\u093D\u0958-\u0961\u0985-
\u098C\u098F-\u0990\u0993-\u09A8\u09AA-\u09B0\u09B2\u09B6-\u09B9\u09DC-\u09DD\u09DF-
\u09E1\u09F0-\u09F1\u0A05-\u0A0A\u0A0F-\u0A10\u0A13-\u0A28\u0A2A-\u0A30\u0A32-\u0A33\u0A35-
\u0A36\u0A38-\u0A39\u0A59-\u0A5C\u0A5E\u0A72-\u0A74\u0A85-\u0A8B\u0A8D\u0A8F-\u0A91\u0A93-
\u0AA8\u0AAA-\u0AB0\u0AB2-\u0AB3\u0AB5-\u0AB9\u0ABD\u0AE0\u0B05-\u0B0C\u0B0F-\u0B10\u0B13-
\u0B28\u0B2A-\u0B30\u0B32-\u0B33\u0B36-\u0B39\u0B3D\u0B5C-\u0B5D\u0B5F-\u0B61\u0B85-
\u0B8A\u0B8E-\u0B90\u0B92-\u0B95\u0B99-\u0B9A\u0B9C\u0B9E-\u0B9F\u0BA3-\u0BA4\u0BA8-
\u0BAA\u0BAE-\u0BB5\u0BB7-\u0BB9\u0C05-\u0C0C\u0C0E-\u0C10\u0C12-\u0C28\u0C2A-\u0C33\u0C35-
\u0C39\u0C60-\u0C61\u0C85-\u0C8C\u0C8E-\u0C90\u0C92-\u0CA8\u0CAA-\u0CB3\u0CB5-
\u0CB9\u0CDE\u0CE0-\u0CE1\u0D05-\u0D0C\u0D0E-\u0D10\u0D12-\u0D28\u0D2A-\u0D39\u0D60-
\u0D61\u0E01-\u0E2E\u0E30\u0E32-\u0E33\u0E40-\u0E45\u0E81-\u0E82\u0E84\u0E87-
\u0E88\u0E8A\u0E8D\u0E94-\u0E97\u0E99-\u0E9F\u0EA1-\u0EA3\u0EA5\u0EA7\u0EAA-\u0EAB\u0EAD-
\u0EAE\u0EB0\u0EB2-\u0EB3\u0EBD\u0EC0-\u0EC4\u0F40-\u0F47\u0F49-\u0F69\u10A0-\u10C5\u10D0-
\u10F6\u1100\u1102-\u1103\u1105-\u1107\u1109\u110B-\u110C\u110E-
\u1112\u113C\u113E\u1140\u114C\u114E\u1150\u1154-\u1155\u1159\u115F-
\u1161\u1163\u1165\u1167\u1169\u116D-\u116E\u1172-\u1173\u1175\u119E\u11A8\u11AB\u11AE-

```

\u11AF\u11B7-\u11B8\u11BA\u11BC-\u11C2\u11EB\u11F0\u11F9\u1E00-\u1E9B\u1EA0-\u1EF9\u1F00-\u1F15\u1F18-\u1F1D\u1F20-\u1F45\u1F48-\u1F4D\u1F50-\u1F57\u1F59\u1F5B\u1F5D\u1F5F-\u1F7D\u1F80-\u1FB4\u1FB6-\u1FBC\u1FBE\u1FC2-\u1FC4\u1FC6-\u1FCC\u1FD0-\u1FD3\u1FD6-\u1FDB\u1FE0-\u1FEC\u1FF2-\u1FF4\u1FF6-\u1FFC\u2126\u212A-\u212B\u212E\u2180-\u2182\u3041-\u3094\u30A1-\u30FA\u3105-\u312C\uAC00-\uD7A3]

Ideographic=[\u4E00-\u9FA5\u3007\u3021-\u3029]

CombiningChar=[\u0300-\u0345\u0360-\u0361\u0483-\u0486\u0591-\u05A1\u05A3-\u05B9\u05BB-\u05BD\u05BF\u05C1-\u05C2\u05C4\u064B-\u0652\u0670\u06D6-\u06DC\u06DD-\u06DF\u06E0-\u06E4\u06E7-\u06E8\u06EA-\u06ED\u0901-\u0903\u093C\u093E-\u094C\u094D\u0951-\u0954\u0962-\u0963\u0981-\u0983\u09BC\u09BE\u09BF\u09C0-\u09C4\u09C7-\u09C8\u09CB-\u09CD\u09D7\u09E2-\u09E3\u0A02\u0A3C\u0A3E\u0A3F\u0A40-\u0A42\u0A47-\u0A48\u0A4B-\u0A4D\u0A70-\u0A71\u0A81-\u0A83\u0ABC\u0ABE-\u0AC5\u0AC7-\u0AC9\u0ACB-\u0ACD\u0B01-\u0B03\u0B3C\u0B3E-\u0B43\u0B47-\u0B48\u0B4B-\u0B4D\u0B56-\u0B57\u0B82-\u0B83\u0BBE-\u0BC2\u0BC6-\u0BC8\u0BCA-\u0BCD\u0BD7\u0C01-\u0C03\u0C3E-\u0C44\u0C46-\u0C48\u0C4A-\u0C4D\u0C55-\u0C56\u0C82-\u0C83\u0CBE-\u0CC4\u0CC6-\u0CC8\u0CCA-\u0CCD\u0CD5-\u0CD6\u0D02-\u0D03\u0D3E-\u0D43\u0D46-\u0D48\u0D4A-\u0D4D\u0D57\u0E31\u0E34-\u0E3A\u0E47-\u0E4E\u0EB1\u0EB4-\u0EB9\u0EBB-\u0EBC\u0EC8-\u0ECD\u0F18-\u0F19\u0F35\u0F37\u0F39\u0F3E\u0F3F\u0F71-\u0F84\u0F86-\u0F8B\u0F90-\u0F95\u0F97\u0F99-\u0FAD\u0FB1-\u0FB7\u0FB9\u20D0-\u20DC\u20E1\u302A-\u302F\u3099\u309A]

Digit=[\u0030-\u0039\u0660-\u0669\u06F0-\u06F9\u0966-\u096F\u09E6-\u09EF\u0A66-\u0A6F\u0AE6-\u0AEF\u0B66-\u0B6F\u0BE7-\u0BEF\u0C66-\u0C6F\u0CE6-\u0CEF\u0D66-\u0D6F\u0E50-\u0E59\u0ED0-\u0ED9\u0F20-\u0F29]

Extender=[\u00B7\u02D0\u02D1\u0387\u0640\u0E46\u0EC6\u3005\u3031-\u3035\u309D-\u309E\u30FC-\u30FE]

NCName={({Letter}}|"_|{|NCNameStartChar}|{|NCNameChar})*

NCNameChar={Letter}|{|Digit}}|".|-|_|{|CombiningChar}|{|Extender}|{|NCNameStartChar}| \u00B7 | [\u0300-\u036F] | [\u203F-\u2040] | [\u0130-\u0136]

NCNameStartChar=[\u0041-\u005A\u0061-\u007A\u00C0-\u00D6\u00D8-\u00F6\u00F8-\u02FF\u0370-\u037D\u037F-\u1FFF\u200C-\u200D\u2070-\u218F\u2C00-\u2FEF\u3001-\uD7FF\uF900-\uFDFF\uFDF0-\uFFFD] | \u005F (|{|HighSurrogate}|{|LowSurrogate})

HighSurrogate=[\uD800-\uDBFF]

LowSurrogate=[\uDC00-\uDFFF]

%%

"*" { return disambiguateStar(); }
"/" { return newSymbol(sym.SLASH); }
"+" { return newSymbol(sym.PLUS); }
"- " { return newSymbol(sym.MINUS); }
"div" { return disambiguateOperator(sym.DIV); }
"mod" { return disambiguateOperator(sym.MOD); }

```

"::"      { return newSymbol(sym.DCOLON); }
","      { return newSymbol(sym.COMMA); }
"@       { return newSymbol(sym.ATSIGN); }
"..     { return newSymbol(sym.DDOT); }
"|"     { return newSymbol(sym.VBAR); }
"$      { return newSymbol(sym.DOLLAR); }
"//"    { return newSymbol(sym.DSLASH); }
"="     { return newSymbol(sym.EQ); }
"!="    { return newSymbol(sym.NE); }
"<"     { return newSymbol(sym.LT); }
">"     { return newSymbol(sym.GT); }
"<="    { return newSymbol(sym.LE); }
">="    { return newSymbol(sym.GE); }
"id     { return disambiguateAxisOrFunction(sym.ID); }
"key    { return disambiguateAxisOrFunction(sym.KEY); }
"text() { return newSymbol(sym.TEXT); }
"text"+[\t\r\n\f]+"() { return newSymbol(sym.TEXT); }
"node()" { return newSymbol(sym.NODE); }
"node"+[\t\r\n\f]+"() { return newSymbol(sym.NODE); }
"comment()" { return newSymbol(sym.COMMENT); }
"comment"+[\t\r\n\f]+"() { return newSymbol(sym.COMMENT); }
"processing-instruction" { return disambiguateAxisOrFunction(sym.PIPARAM); }
"processing-instruction()" { return newSymbol(sym.PI); }
"processing-instruction"+[\t\r\n\f]+"() { return newSymbol(sym.PI); }
"or"     { return disambiguateOperator(sym.OR); }
"and"    { return disambiguateOperator(sym.AND); }
"child"  { return disambiguateAxisOrFunction(sym.CHILD); }
"attribute" { return disambiguateAxisOrFunction(sym.ATTRIBUTE); }
"ancestor" { return disambiguateAxisOrFunction(sym.ANCESTOR); }
"ancestor-or-self" { return disambiguateAxisOrFunction(sym.ANCESTORORSELF); }
"descendant" { return disambiguateAxisOrFunction(sym.DESCENDANT); }
"descendant-or-self" { return disambiguateAxisOrFunction(sym.DESCENDANTORSELF); }
"following" { return disambiguateAxisOrFunction(sym.FOLLOWING); }
"following-sibling" { return disambiguateAxisOrFunction(sym.FOLLOWINGSIBLING); }
"namespace" { return disambiguateAxisOrFunction(sym.NAMESPACE); }
"parent" { return disambiguateAxisOrFunction(sym.PARENT); }
"preceding" { return disambiguateAxisOrFunction(sym.PRECEDING); }
"preceding-sibling" { return disambiguateAxisOrFunction(sym.PRECEDINGSIBLING); }
"self"     { return disambiguateAxisOrFunction(sym.SELF); }
"["       { return newSymbol(sym.LBRACK); }
"]"      { return newSymbol(sym.RBRACK); }
"("      { return newSymbol(sym.LPAREN); }
")"      { return newSymbol(sym.RPAREN); }
"<PATTERN>" { initialize(); return new Symbol(sym.PATTERN); }
"<EXPRESSION>" { initialize(); return new Symbol(sym.EXPRESSION); }
\[^\]*\   { return newSymbol(sym.Literal,
    ytext().substring(1, ytext().length() - 1)); }
\[^\]*\   { return newSymbol(sym.Literal,

```

```

        yytext().substring(1, yytext().length() - 1)); }
{Digit}+      { return newSymbol(sym.INT, new Long(yytext())); }
{Digit}+("."{Digit}*)? { return newSymbol(sym.REAL, new Double(yytext())); }
"."{Digit}+   { return newSymbol(sym.REAL, new Double(yytext())); }
"."          { return newSymbol(sym.DOT); }
({NCName}":")?{NCName} { return newSymbol(sym.QNAME, yytext()); }
({NCName}":")?"*"      { return newSymbol(sym.QNAME, yytext()); }
({NCName}":")?"@*"     { return newSymbol(sym.QNAME, yytext()); }
[\\t\\r\\n\\f]      { /* ignore white space. */ }
.                  { throw new Exception(yytext()); }

```

Found in path(s):

```
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xalan/xsltc/compiler/xpath.lex
```

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```

/*
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 * WITHOUT WARRANTIES OR CONDITIONS OF ANY KIND, either express or implied.
 * See the License for the specific language governing permissions and
 * limitations under the License.
 */
/*
 * $Id: XSLProcessorVersion.src 468640 2006-10-28 06:53:53Z minchau $
 */
package org.apache.xalan.processor;

/**
 * Administrative class to keep track of the version number of
 * the Xalan release.
 * <P>See also: org/apache/xalan/res/XSLTInfo.properties</P>
 * @deprecated To be replaced by org.apache.xalan.Version.getVersion()
 * @xsl.usage general
 */
public class XSLProcessorVersion
{

```

```

/**
 * Print the processor version to the command line.
 *
 * @param argv command line arguments, unused.
 */
public static void main(String argv[])
{
    System.out.println(S_VERSION);
}

/**
 * Constant name of product.
 */
public static final String PRODUCT = "Xalan";

/**
 * Implementation Language.
 */
public static final String LANGUAGE = "Java";

/**
 * Major version number.
 * Version number. This changes only when there is a
 *     significant, externally apparent enhancement from
 *     the previous release. 'n' represents the n'th
 *     version.
 *
 * Clients should carefully consider the implications
 * of new versions as external interfaces and behaviour
 * may have changed.
 */
public static final int VERSION = @version.VERSION@;

/**
 * Release Number.
 * Release number. This changes when:
 *     - a new set of functionality is to be added, eg,
 *       implementation of a new W3C specification.
 *     - API or behaviour change.
 *     - its designated as a reference release.
 */
public static final int RELEASE = @version.RELEASE@;

/**
 * Maintenance Drop Number.
 * Optional identifier used to designate maintenance
 *     drop applied to a specific release and contains
 *     fixes for defects reported. It maintains compatibility

```

```

* with the release and contains no API changes.
* When missing, it designates the final and complete
* development drop for a release.
*/
public static final int MAINTENANCE = @version.MINOR@;

/**
 * Development Drop Number.
 * Optional identifier designates development drop of
 * a specific release. D01 is the first development drop
 * of a new release.
 *
 * Development drops are works in progress towards a
 * completed, final release. A specific development drop
 * may not completely implement all aspects of a new
 * feature, which may take several development drops to
 * complete. At the point of the final drop for the
 * release, the D suffix will be omitted.
 *
 * Each 'D' drops can contain functional enhancements as
 * well as defect fixes. 'D' drops may not be as stable as
 * the final releases.
 */
public static final int DEVELOPMENT = 0;

/**
 * Version String like <CODE>"<B>Xalan</B> <B>Language</B>
 * v.r[.dd] <B>D</B>"</CODE>.
 * <P>Semantics of the version string are identical to the Xerces project.</P>
 */
public static final String S_VERSION = PRODUCT+" "+LANGUAGE+" "
    +VERSION+"."+RELEASE+"."
    +(DEVELOPMENT > 0 ? ("D"+DEVELOPMENT)
    : (""+MAINTENANCE));
}

```

Found in path(s):

```

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xalan/processor/XSLProcessorVersion.src

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Found in path(s):

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/trace/package.html
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/functions/package.html
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/transformer/package.html
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/client/package.html
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/processor/package.html
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/lib/package.html
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/domapi/package.html
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/templates/package.html
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/serializer/package.html
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/extensions/package.html
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/operations/package.html
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/compiler/package.html
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/patterns/package.html
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/package.html
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/lib/sql/package.html
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/res/package.html
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/utils/package.html
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/objects/package.html
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/axes/package.html
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/res/package.html

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*/
// Proprietary, built in functions
/** current function string (Proprietary). */
```

Found in path(s):

```
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xpath/compiler/Keywords.java
```

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```

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```
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xalan/transformer/QueuedEvents.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xalan/xsltc/compiler/XslAttribute.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xml/dtm/ref/NodeLocator.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/serializer/ToStream.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xalan/xsltc/runtime/MessageHandler.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xalan/xsltc/dom/KeyIndex.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/utils/ElemDesc.java
```

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/templates/ElemForEach.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/patterns/ContextMatchStepPattern.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/res/XPATHErrorResources_zh.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/functions/FuncBoolean.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/util/NodeSetType.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/CurrentCall.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/trax/TransformerHandlerImpl.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/When.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/util/SecuritySupport.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/axes/WalkingIteratorSorted.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/templates/ElemWhen.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/serializer/utils/SerializerMessages_ja.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/serializer/StylesheetPIHandler.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/functions/FuncSubstring.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/trax/TrAXFilter.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/functions/FuncConcat.java

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* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/functions/Function.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/trax/XSLTSource.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/axes/AttributeIterator.java

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* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/dom/LoadDocument.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/runtime/ObjectFactory.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/transformer/NumeratorFormatter.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/serializer/Utils/SerializerMessages_hu.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/runtime/SecuritySupport.java
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* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/utils/res/CharArrayWrapper.java

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* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/extensions/ExtensionNamespaceSupport.java
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* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/operations/And.java
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* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/serializer/ExtendedLexicalHandler.java
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* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/transformer/TransformerIdentityImpl.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/utils/IntVector.java
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- * /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/templates/ElemTemplateElement.java
- * /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/LiteralElement.java
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- * /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/objects/XNodeSet.java
- * /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/templates/ElemText.java
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- * /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/FilterParentPath.java
- * /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/Sort.java
- * /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/functions/FuncLast.java
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- * /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/functions/Function3Args.java
- * /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/utils/URI.java
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* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xalan/templates/XSLTVisitor.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xalan/xsltc/compiler/LiteralExpr.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/ApplyImports.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/lib/sql/ConnectionPoolManager.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/processor/ProcessorAttributeSet.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/operations/Quo.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/utils/NSInfo.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/extensions/ExtensionHandlerJavaClass.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/serializer/dom3/DOMConstants.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/dtm/ref/ExpandedNameTable.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/QName.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/dtm/DTMManager.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/GenerateIdCall.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/utils/QName.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/transformer/TransformSnapshot.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/serializer/utils/SerializerMessages_ca.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/lib/sql/QueryParameter.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/util/ErrorMessageMessages_es.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/processor/ProcessorOutputElem.java
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* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/util/SlotAllocator.java
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* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/utils/res/XResources_el.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/transformer/TransformerHandlerImpl.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/trax/DOM2TO.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/templates/Stylesheet.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/VariableStack.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/dtm/DTMWSFilter.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/axes/FilterExprIteratorSimple.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/res/XSLTErrorResources_zh_CN.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/dom/MultiValuedNodeHeapIterator.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/res/XSLTErrorResources_zh_TW.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/templates/ElemElement.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/ConcatCall.java

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* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/transformer/ResultNameSpace.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/processor/ProcessorLRE.java

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* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/utils/res/XResources_ja_JP_A.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/operations/Gt.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/res/XMLErrorResources_fr.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/res/XMLErrorResources_cs.java

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* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/dtm/ref/DTMNodeList.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/dtm/Axis.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/templates/XUnresolvedVariable.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/functions/FuncFalse.java

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* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/dom/AbsoluteIterator.java

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/dom/CurrentNodeListIterator.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/IllegalCharException.java

1.309 jakarta-inject 2.5.0

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- * <https://github.com/eclipse-ee4j/jersey>

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1.312 sbsigntool 1.3

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1.313 python-requests 2.27.1

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1.314 chardet 4.0.0

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1.315 log4j-api 2.17.1

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1.316 libxml2 2.9.8

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1.317 commons-codec 1.3

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* /opt/cola/permits/1135953634_1613641193.46/0/commons-codec-1-3-sources-1-

jar/org/apache/commons/codec/digest/package.html

* /opt/cola/permits/1135953634_1613641193.46/0/commons-codec-1-3-sources-1-

jar/org/apache/commons/codec/language/package.html

* /opt/cola/permits/1135953634_1613641193.46/0/commons-codec-1-3-sources-1-

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jar/org/apache/commons/codec/digest/DigestUtils.java

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jar/org/apache/commons/codec/net/URLConnection.java

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jar/org/apache/commons/codec/Encoder.java

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jar/org/apache/commons/codec/net/StringEncodings.java

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jar/org/apache/commons/codec/StringEncoder.java

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jar/org/apache/commons/codec/binary/Base64.java

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jar/org/apache/commons/codec/net/BCodec.java

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jar/org/apache/commons/codec/binary/BinaryCodec.java

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jar/org/apache/commons/codec/language/RefinedSoundex.java

* /opt/cola/permits/1135953634_1613641193.46/0/commons-codec-1-3-sources-1-
jar/org/apache/commons/codec/DecoderException.java

* /opt/cola/permits/1135953634_1613641193.46/0/commons-codec-1-3-sources-1-jar/org/apache/commons/codec/StringEncoderComparator.java
* /opt/cola/permits/1135953634_1613641193.46/0/commons-codec-1-3-sources-1-jar/org/apache/commons/codec/language/Soundex.java
* /opt/cola/permits/1135953634_1613641193.46/0/commons-codec-1-3-sources-1-jar/org/apache/commons/codec/BinaryEncoder.java
* /opt/cola/permits/1135953634_1613641193.46/0/commons-codec-1-3-sources-1-jar/org/apache/commons/codec/binary/Hex.java
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* /opt/cola/permits/1135953634_1613641193.46/0/commons-codec-1-3-sources-1-jar/org/apache/commons/codec/Decoder.java
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* /opt/cola/permits/1135953634_1613641193.46/0/commons-codec-1-3-sources-1-jar/org/apache/commons/codec/StringDecoder.java
* /opt/cola/permits/1135953634_1613641193.46/0/commons-codec-1-3-sources-1-jar/org/apache/commons/codec/BinaryDecoder.java
* /opt/cola/permits/1135953634_1613641193.46/0/commons-codec-1-3-sources-1-jar/org/apache/commons/codec/EncoderException.java
* /opt/cola/permits/1135953634_1613641193.46/0/commons-codec-1-3-sources-1-jar/org/apache/commons/codec/language/DoubleMetaphone.java

1.318 jaxb2-reflection 2.3.0

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1.319 libjpeg 6b

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README for release 6b of 27-Mar-1998

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DOCUMENTATION ROADMAP

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This file contains the following sections:

OVERVIEW General description of JPEG and the IJG software.
LEGAL ISSUES Copyright, lack of warranty, terms of distribution.
REFERENCES Where to learn more about JPEG.
ARCHIVE LOCATIONS Where to find newer versions of this software.
RELATED SOFTWARE Other stuff you should get.
FILE FORMAT WARS Software *not* to get.
TO DO Plans for future IJG releases.

Other documentation files in the distribution are:

User documentation:

install.doc How to configure and install the IJG software.
usage.doc Usage instructions for cjpeg, djpeg, jpegtran,
 rdjpgcom, and wrjpgcom.
*.1 Unix-style man pages for programs (same info as usage.doc).
wizard.doc Advanced usage instructions for JPEG wizards only.
change.log Version-to-version change highlights.

Programmer and internal documentation:

libjpeg.doc How to use the JPEG library in your own programs.
example.c Sample code for calling the JPEG library.
structure.doc Overview of the JPEG library's internal structure.
filelist.doc Road map of IJG files.
coderrules.doc Coding style rules --- please read if you contribute code.

Please read at least the files install.doc and usage.doc. Useful information can also be found in the JPEG FAQ (Frequently Asked Questions) article. See ARCHIVE LOCATIONS below to find out where to obtain the FAQ article.

If you want to understand how the JPEG code works, we suggest reading one or more of the REFERENCES, then looking at the documentation files (in roughly the order listed) before diving into the code.

OVERVIEW

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This package contains C software to implement JPEG image compression and

decompression. JPEG (pronounced "jay-peg") is a standardized compression method for full-color and gray-scale images. JPEG is intended for compressing "real-world" scenes; line drawings, cartoons and other non-realistic images are not its strong suit. JPEG is lossy, meaning that the output image is not exactly identical to the input image. Hence you must not use JPEG if you have to have identical output bits. However, on typical photographic images, very good compression levels can be obtained with no visible change, and remarkably high compression levels are possible if you can tolerate a low-quality image. For more details, see the references, or just experiment with various compression settings.

This software implements JPEG baseline, extended-sequential, and progressive compression processes. Provision is made for supporting all variants of these processes, although some uncommon parameter settings aren't implemented yet. For legal reasons, we are not distributing code for the arithmetic-coding variants of JPEG; see LEGAL ISSUES. We have made no provision for supporting the hierarchical or lossless processes defined in the standard.

We provide a set of library routines for reading and writing JPEG image files, plus two sample applications "cjpeg" and "djpeg", which use the library to perform conversion between JPEG and some other popular image file formats. The library is intended to be reused in other applications.

In order to support file conversion and viewing software, we have included considerable functionality beyond the bare JPEG coding/decoding capability; for example, the color quantization modules are not strictly part of JPEG decoding, but they are essential for output to colormapped file formats or colormapped displays. These extra functions can be compiled out of the library if not required for a particular application. We have also included "jpegtran", a utility for lossless transcoding between different JPEG processes, and "rdjpgcom" and "wrjpgcom", two simple applications for inserting and extracting textual comments in JFIF files.

The emphasis in designing this software has been on achieving portability and flexibility, while also making it fast enough to be useful. In particular, the software is not intended to be read as a tutorial on JPEG. (See the REFERENCES section for introductory material.) Rather, it is intended to be reliable, portable, industrial-strength code. We do not claim to have achieved that goal in every aspect of the software, but we strive for it.

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It appears that the arithmetic coding option of the JPEG spec is covered by patents owned by IBM, AT&T, and Mitsubishi. Hence arithmetic coding cannot legally be used without obtaining one or more licenses. For this reason, support for arithmetic coding has been removed from the free JPEG software. (Since arithmetic coding provides only a marginal gain over the unpatented Huffman mode, it is unlikely that very many implementations will support it.) So far as we are aware, there are no patent restrictions on the remaining code.

The IJG distribution formerly included code to read and write GIF files. To avoid entanglement with the Unisys LZW patent, GIF reading support has been removed altogether, and the GIF writer has been simplified to produce "uncompressed GIFs". This technique does not use the LZW algorithm; the resulting GIF files are larger than usual, but are readable by all standard GIF decoders.

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REFERENCES

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We highly recommend reading one or more of these references before trying to understand the innards of the JPEG software.

The best short technical introduction to the JPEG compression algorithm is Wallace, Gregory K. "The JPEG Still Picture Compression Standard", Communications of the ACM, April 1991 (vol. 34 no. 4), pp. 30-44. (Adjacent articles in that issue discuss MPEG motion picture compression, applications of JPEG, and related topics.) If you don't have the CACM issue handy, a PostScript file containing a revised version of Wallace's article is

available at <ftp://ftp.uu.net/graphics/jpeg/wallace.ps.gz>. The file (actually a preprint for an article that appeared in IEEE Trans. Consumer Electronics) omits the sample images that appeared in CACM, but it includes corrections and some added material. Note: the Wallace article is copyright ACM and IEEE, and it may not be used for commercial purposes.

A somewhat less technical, more leisurely introduction to JPEG can be found in "The Data Compression Book" by Mark Nelson and Jean-loup Gailly, published by M&T Books (New York), 2nd ed. 1996, ISBN 1-55851-434-1. This book provides good explanations and example C code for a multitude of compression methods including JPEG. It is an excellent source if you are comfortable reading C code but don't know much about data compression in general. The book's JPEG sample code is far from industrial-strength, but when you are ready to look at a full implementation, you've got one here...

The best full description of JPEG is the textbook "JPEG Still Image Data Compression Standard" by William B. Pennebaker and Joan L. Mitchell, published by Van Nostrand Reinhold, 1993, ISBN 0-442-01272-1. Price US\$59.95, 638 pp. The book includes the complete text of the ISO JPEG standards (DIS 10918-1 and draft DIS 10918-2). This is by far the most complete exposition of JPEG in existence, and we highly recommend it.

The JPEG standard itself is not available electronically; you must order a paper copy through ISO or ITU. (Unless you feel a need to own a certified official copy, we recommend buying the Pennebaker and Mitchell book instead; it's much cheaper and includes a great deal of useful explanatory material.) In the USA, copies of the standard may be ordered from ANSI Sales at (212) 642-4900, or from Global Engineering Documents at (800) 854-7179. (ANSI doesn't take credit card orders, but Global does.) It's not cheap: as of 1992, ANSI was charging \$95 for Part 1 and \$47 for Part 2, plus 7% shipping/handling. The standard is divided into two parts, Part 1 being the actual specification, while Part 2 covers compliance testing methods. Part 1 is titled "Digital Compression and Coding of Continuous-tone Still Images, Part 1: Requirements and guidelines" and has document numbers ISO/IEC IS 10918-1, ITU-T T.81. Part 2 is titled "Digital Compression and Coding of Continuous-tone Still Images, Part 2: Compliance testing" and has document numbers ISO/IEC IS 10918-2, ITU-T T.83.

Some extensions to the original JPEG standard are defined in JPEG Part 3, a newer ISO standard numbered ISO/IEC IS 10918-3 and ITU-T T.84. IJG currently does not support any Part 3 extensions.

The JPEG standard does not specify all details of an interchangeable file format. For the omitted details we follow the "JFIF" conventions, revision 1.02. A copy of the JFIF spec is available from:

Literature Department
C-Cube Microsystems, Inc.
1778 McCarthy Blvd.

Milpitas, CA 95035

phone (408) 944-6300, fax (408) 944-6314

A PostScript version of this document is available by FTP at <ftp://ftp.uu.net/graphics/jpeg/jfif.ps.gz>. There is also a plain text version at <ftp://ftp.uu.net/graphics/jpeg/jfif.txt.gz>, but it is missing the figures.

The TIFF 6.0 file format specification can be obtained by FTP from <ftp://ftp.sgi.com/graphics/tiff/TIFF6.ps.gz>. The JPEG incorporation scheme found in the TIFF 6.0 spec of 3-June-92 has a number of serious problems. IJG does not recommend use of the TIFF 6.0 design (TIFF Compression tag 6). Instead, we recommend the JPEG design proposed by TIFF Technical Note #2 (Compression tag 7). Copies of this Note can be obtained from <ftp.sgi.com> or from <ftp://ftp.uu.net/graphics/jpeg/>. It is expected that the next revision of the TIFF spec will replace the 6.0 JPEG design with the Note's design. Although IJG's own code does not support TIFF/JPEG, the free libtiff library uses our library to implement TIFF/JPEG per the Note. libtiff is available from <ftp://ftp.sgi.com/graphics/tiff/>.

ARCHIVE LOCATIONS

=====

The "official" archive site for this software is <ftp.uu.net> (Internet address 192.48.96.9). The most recent released version can always be found there in directory <graphics/jpeg>. This particular version will be archived as <ftp://ftp.uu.net/graphics/jpeg/jpegsrc.v6b.tar.gz>. If you don't have direct Internet access, UUNET's archives are also available via UUCP; contact help@uunet.uu.net for information on retrieving files that way.

Numerous Internet sites maintain copies of the UUNET files. However, only <ftp.uu.net> is guaranteed to have the latest official version.

You can also obtain this software in DOS-compatible "zip" archive format from the SimTel archives (<ftp://ftp.simtel.net/pub/simtelnet/msdos/graphics/>), or on CompuServe in the Graphics Support forum (GO CIS:GRAPHSUP), library 12 "JPEG Tools". Again, these versions may sometimes lag behind the <ftp.uu.net> release.

The JPEG FAQ (Frequently Asked Questions) article is a useful source of general information about JPEG. It is updated constantly and therefore is not included in this distribution. The FAQ is posted every two weeks to Usenet newsgroups <comp.graphics.misc>, <news.answers>, and other groups. It is available on the World Wide Web at <http://www.faqs.org/faqs/jpeg-faq/> and other <news.answers> archive sites, including the official <news.answers> archive at <rtfm.mit.edu>: <ftp://rtfm.mit.edu/pub/usenet/news.answers/jpeg-faq/>. If you don't have Web or FTP access, send e-mail to mail-server@rtfm.mit.edu with body

send usenet/news.answers/jpeg-faq/part1

send usenet/news.answers/jpeg-faq/part2

RELATED SOFTWARE

=====

Numerous viewing and image manipulation programs now support JPEG. (Quite a few of them use this library to do so.) The JPEG FAQ described above lists some of the more popular free and shareware viewers, and tells where to obtain them on Internet.

If you are on a Unix machine, we highly recommend Jef Poskanzer's free PBPLUS software, which provides many useful operations on PPM-format image files. In particular, it can convert PPM images to and from a wide range of other formats, thus making cjpeg/djpeg considerably more useful. The latest version is distributed by the NetPBM group, and is available from numerous sites, notably <ftp://wuarchive.wustl.edu/graphics/graphics/packages/NetPBM/>. Unfortunately PBPLUS/NETPBM is not nearly as portable as the IJG software is; you are likely to have difficulty making it work on any non-Unix machine.

A different free JPEG implementation, written by the PVRG group at Stanford, is available from <ftp://havefun.stanford.edu/pub/jpeg/>. This program is designed for research and experimentation rather than production use; it is slower, harder to use, and less portable than the IJG code, but it is easier to read and modify. Also, the PVRG code supports lossless JPEG, which we do not. (On the other hand, it doesn't do progressive JPEG.)

FILE FORMAT WARS

=====

Some JPEG programs produce files that are not compatible with our library. The root of the problem is that the ISO JPEG committee failed to specify a concrete file format. Some vendors "filled in the blanks" on their own, creating proprietary formats that no one else could read. (For example, none of the early commercial JPEG implementations for the Macintosh were able to exchange compressed files.)

The file format we have adopted is called JFIF (see REFERENCES). This format has been agreed to by a number of major commercial JPEG vendors, and it has become the de facto standard. JFIF is a minimal or "low end" representation. We recommend the use of TIFF/JPEG (TIFF revision 6.0 as modified by TIFF Technical Note #2) for "high end" applications that need to record a lot of additional data about an image. TIFF/JPEG is fairly new and not yet widely supported, unfortunately.

The upcoming JPEG Part 3 standard defines a file format called SPIFF.

SPIFF is interoperable with JFIF, in the sense that most JFIF decoders should be able to read the most common variant of SPIFF. SPIFF has some technical advantages over JFIF, but its major claim to fame is simply that it is an official standard rather than an informal one. At this point it is unclear whether SPIFF will supersede JFIF or whether JFIF will remain the de-facto standard. IJG intends to support SPIFF once the standard is frozen, but we have not decided whether it should become our default output format or not. (In any case, our decoder will remain capable of reading JFIF indefinitely.)

Various proprietary file formats incorporating JPEG compression also exist. We have little or no sympathy for the existence of these formats. Indeed, one of the original reasons for developing this free software was to help force convergence on common, open format standards for JPEG files. Don't use a proprietary file format!

TO DO

=====

The major thrust for v7 will probably be improvement of visual quality. The current method for scaling the quantization tables is known not to be very good at low Q values. We also intend to investigate block boundary smoothing, "poor man's variable quantization", and other means of improving quality-vs-file-size performance without sacrificing compatibility.

In future versions, we are considering supporting some of the upcoming JPEG Part 3 extensions --- principally, variable quantization and the SPIFF file format.

As always, speeding things up is of great interest.

Please send bug reports, offers of help, etc. to jpeg-info@uunet.uu.net.

1.320 log4j-to-slf4j 2.17.1

1.320.1 Available under license :

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1.321 cast 1.3.1

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1.322 idna 2.10-1.ph4

1.322.1 Available under license :

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Metadata-Version: 1.2

Name: idna

Version: 2.10

Summary: Internationalized Domain Names in Applications (IDNA)

Home-page: <https://github.com/kjd/idna>

Author: Kim Davies

Author-email: kim@cynosure.com.au

License: BSD-like

Description: Internationalized Domain Names in Applications (IDNA)

=====

Support for the Internationalised Domain Names in Applications (IDNA) protocol as specified in `RFC 5891 <<http://tools.ietf.org/html/rfc5891>>`_. This is the latest version of the protocol and is sometimes referred to as “IDNA 2008”.

This library also provides support for Unicode Technical Standard 46, `Unicode IDNA Compatibility Processing <<http://unicode.org/reports/tr46/>>`_.

This acts as a suitable replacement for the “encodings.idna” module that comes with the Python standard library, but only supports the old, deprecated IDNA specification (`RFC 3490 <<http://tools.ietf.org/html/rfc3490>>`_).

Basic functions are simply executed:

```
.. code-block:: pycon

# Python 3
>>> import idna
>>> idna.encode('.')
b'xn--eckwd4c7c.xn--zckzah'
>>> print(idna.decode('xn--eckwd4c7c.xn--zckzah'))
.

# Python 2
>>> import idna
>>> idna.encode(u'.')
'xn--eckwd4c7c.xn--zckzah'
>>> print idna.decode('xn--eckwd4c7c.xn--zckzah')
.
```

Packages

The latest tagged release version is published in the PyPI repository:

```
.. image:: https://badge.fury.io/py/idna.svg
:target: http://badge.fury.io/py/idna
```

Installation

To install this library, you can use pip:

```
.. code-block:: bash
```

```
$ pip install idna
```

Alternatively, you can install the package using the bundled setup script:

```
.. code-block:: bash

    $ python setup.py install
```

This library works with Python 2.7 and Python 3.4 or later.

Usage

For typical usage, the `encode` and `decode` functions will take a domain name argument and perform a conversion to A-labels or U-labels respectively.

```
.. code-block:: pycon

    # Python 3
    >>> import idna
    >>> idna.encode('.')
    b'xn--eckwd4c7c.xn--zckzah'
    >>> print(idna.decode('xn--eckwd4c7c.xn--zckzah'))
    .
```

You may use the codec encoding and decoding methods using the `idna.codec` module:

```
.. code-block:: pycon

    # Python 2
    >>> import idna.codec
    >>> print u'.'.encode('idna')
    xn--80ahd1agd.xn--80akhbyknj4f
    >>> print 'xn--80ahd1agd.xn--80akhbyknj4f'.decode('idna')
    .
```

Conversions can be applied at a per-label basis using the `ulabel` or `alabel` functions if necessary:

```
.. code-block:: pycon

    # Python 2
    >>> idna.alabel(u'')
    'xn--0zwm56d'
```

Compatibility Mapping (UTS #46)

+++++

As described in `RFC 5895 <<http://tools.ietf.org/html/rfc5895>>`, the IDNA specification no longer normalizes input from different potential ways a user may input a domain name. This functionality, known as a “mapping”, is now considered by the specification to be a local user-interface issue distinct from IDNA conversion functionality.

This library provides one such mapping, that was developed by the Unicode Consortium. Known as `Unicode IDNA Compatibility Processing <<http://unicode.org/reports/tr46/>>`, it provides for both a regular mapping for typical applications, as well as a transitional mapping to help migrate from older IDNA 2003 applications.

For example, “Königsgäßchen” is not a permissible label as **LATIN CAPITAL LETTER K** is not allowed (nor are capital letters in general). UTS 46 will convert this into lower case prior to applying the IDNA conversion.

.. code-block:: pycon

```
# Python 3
>>> import idna
>>> idna.encode(u'Königsgäßchen')
...
idna.core.InvalidCodepoint: Codepoint U+004B at position 1 of 'Königsgäßchen' not allowed
>>> idna.encode('Königsgäßchen', uts46=True)
b'xn--knigsgchen-b4a3dun'
>>> print(idna.decode('xn--knigsgchen-b4a3dun'))
königsgäßchen
```

Transitional processing provides conversions to help transition from the older 2003 standard to the current standard. For example, in the original IDNA specification, the **LATIN SMALL LETTER SHARP S** (ß) was converted into two **LATIN SMALL LETTER S** (ss), whereas in the current IDNA specification this conversion is not performed.

.. code-block:: pycon

```
# Python 2
>>> idna.encode(u'Königsgäßchen', uts46=True, transitional=True)
'xn--knigsgsschen-lcb0w'
```

Implementors should use transitional processing with caution, only in rare cases where conversion from legacy labels to current labels must be performed (i.e. IDNA implementations that pre-date 2008). For typical applications that just need to convert labels, transitional processing is unlikely to be beneficial and could produce unexpected incompatible results.

```
``encodings.idna`` Compatibility
+++++
```

Function calls from the Python built-in `encodings.idna` module are mapped to their IDNA 2008 equivalents using the `idna.compat` module. Simply substitute the `import` clause in your code to refer to the new module name.

Exceptions

All errors raised during the conversion following the specification should raise an exception derived from the `idna.IDNAError` base class.

More specific exceptions that may be generated as `idna.IDNABidiError` when the error reflects an illegal combination of left-to-right and right-to-left characters in a label; `idna.InvalidCodepoint` when a specific codepoint is an illegal character in an IDN label (i.e. INVALID); and `idna.InvalidCodepointContext` when the codepoint is illegal based on its positional context (i.e. it is CONTEXTO or CONTEXTJ but the contextual requirements are not satisfied.)

Building and Diagnostics

The IDNA and UTS 46 functionality relies upon pre-calculated lookup tables for performance. These tables are derived from computing against eligibility criteria in the respective standards. These tables are computed using the command-line script `tools/idna-data`.

This tool will fetch relevant tables from the Unicode Consortium and perform the required calculations to identify eligibility. It has three main modes:

- * `idna-data make-libdata`. Generates `idnadata.py` and `uts46data.py`, the pre-calculated lookup tables used for IDNA and UTS 46 conversions. Implementors who wish to track this library against a different Unicode version may use this tool to manually generate a different version of the `idnadata.py` and `uts46data.py` files.
- * `idna-data make-table`. Generate a table of the IDNA disposition (e.g. PVALID, CONTEXTJ, CONTEXTO) in the format found in Appendix B.1 of RFC 5892 and the pre-computed tables published by IANA <<http://iana.org/>>.
- * `idna-data U+0061`. Prints debugging output on the various properties associated with an individual Unicode codepoint (in this case, U+0061), that are used to assess the IDNA and UTS 46 status of a codepoint. This is helpful in debugging or analysis.

The tool accepts a number of arguments, described using `idna-data -h`. Most notably, the `--version` argument allows the specification of the version of Unicode to use in computing the table data. For example, `idna-data --version 9.0.0 make-libdata`

will generate library data against Unicode 9.0.0.

Note that this script requires Python 3, but all generated library data will work in Python 2.7.

Testing

The library has a test suite based on each rule of the IDNA specification, as well as tests that are provided as part of the Unicode Technical Standard 46, `Unicode IDNA Compatibility Processing <<http://unicode.org/reports/tr46/>>`.

The tests are run automatically on each commit at Travis CI:

```
.. image:: https://travis-ci.org/kjd/idna.svg?branch=master
   :target: https://travis-ci.org/kjd/idna
```

Platform: UNKNOWN

Classifier: Development Status :: 5 - Production/Stable

Classifier: Intended Audience :: Developers

Classifier: Intended Audience :: System Administrators

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Classifier: Programming Language :: Python :: 3.7

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Classifier: Topic :: Internet :: Name Service (DNS)

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Requires-Python: >=2.7, !=3.0.*, !=3.1.*, !=3.2.*, !=3.3.*

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""""

A library to support the Internationalised Domain Names in Applications (IDNA) protocol as specified in RFC 5890 et.al. This new methodology, known as IDNA 2008, can generate materially different results to the previous standard. The library can act as a drop-in replacement for the "encodings.idna" module.

""""

```
import io, sys
from setuptools import setup
```

```

def main():

    python_version = sys.version_info[:2]
    if python_version < (2,7):
        raise SystemExit("Sorry, Python 2.7 or newer required")

    package_data = {}
    exec(open('idna/package_data.py').read(), package_data)

    arguments = {
        'name': 'idna',
        'packages': ['idna'],
        'version': package_data['__version__'],
        'description': 'Internationalized Domain Names in Applications (IDNA)',
        'long_description': io.open("README.rst", encoding="UTF-8").read(),
        'author': 'Kim Davies',
        'author_email': 'kim@cynosure.com.au',
        'license': 'BSD-like',
        'url': 'https://github.com/kjd/idna',
        'classifiers': [
            'Development Status :: 5 - Production/Stable',
            'Intended Audience :: Developers',
            'Intended Audience :: System Administrators',
            'License :: OSI Approved :: BSD License',
            'Operating System :: OS Independent',
            'Programming Language :: Python',
            'Programming Language :: Python :: 2',
            'Programming Language :: Python :: 2.7',
            'Programming Language :: Python :: 3',
            'Programming Language :: Python :: 3.4',
            'Programming Language :: Python :: 3.5',
            'Programming Language :: Python :: 3.6',
            'Programming Language :: Python :: 3.7',
            'Programming Language :: Python :: 3.8',
            'Programming Language :: Python :: Implementation :: CPython',
            'Programming Language :: Python :: Implementation :: PyPy',
            'Topic :: Internet :: Name Service (DNS)',
            'Topic :: Software Development :: Libraries :: Python Modules',
            'Topic :: Utilities',
        ],
        'python_requires': '>=2.7, !=3.0.*, !=3.1.*, !=3.2.*, !=3.3.*',
    }

    setup(**arguments)

if __name__ == '__main__':
    main()

```

Found in path(s):

* /opt/cola/permits/1175352047_1627367064.54/0/idna-2-10-1-tar-gz/idna-2.10/setup.py

1.323 also 1.1.2

1.323.1 Available under license :

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Version 2.1, February 1999

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Jackson JSON processor

Jackson is a high-performance, Free/Open Source JSON processing library.

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Last revised by Reagle \$Date: 2005-06-03 18:49:13 -0400 (Fri, 03 Jun 2005) \$\n## Cryptix AES v3.2.0

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Jackson JSON processor

Jackson is a high-performance, Free/Open Source JSON processing library.

It was originally written by Tatu Saloranta (tatu.saloranta@iki.fi), and has been in development since 2007.

It is currently developed by a community of developers, as well as supported commercially by FasterXML.com.

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Jackson JSON processor

Jackson is a high-performance, Free/Open Source JSON processing library. It was originally written by Tatu Saloranta (tatu.saloranta@iki.fi), and has been in development since 2007.

It is currently developed by a community of developers, as well as supported commercially by FasterXML.com.

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* lib/domain_name/punycode.rb

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* `lib/domain_name/etld_data.rb`

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The Arabic,Persian,Romanian,Bulgarian, and Hindi analyzers (common) come with a default
stopword list that is BSD-licensed created by Jacques Savoy. These files reside in:
analysis/common/src/resources/org/apache/lucene/analysis/ar/stopwords.txt,
analysis/common/src/resources/org/apache/lucene/analysis/fa/stopwords.txt,
analysis/common/src/resources/org/apache/lucene/analysis/ro/stopwords.txt,
analysis/common/src/resources/org/apache/lucene/analysis/bg/stopwords.txt,
analysis/common/src/resources/org/apache/lucene/analysis/hi/stopwords.txt
See <http://members.unine.ch/jacques.savoy/clef/index.html>.

The German,Spanish,Finnish,French,Hungarian,Italian,Portuguese,Russian and Swedish light stemmers
(common) are based on BSD-licensed reference implementations created by Jacques Savoy and
Ljiljana Dolamic. These files reside in:
analysis/common/src/java/org/apache/lucene/analysis/de/GermanLightStemmer.java
analysis/common/src/java/org/apache/lucene/analysis/de/GermanMinimalStemmer.java
analysis/common/src/java/org/apache/lucene/analysis/es/SpanishLightStemmer.java
analysis/common/src/java/org/apache/lucene/analysis/fi/FinnishLightStemmer.java
analysis/common/src/java/org/apache/lucene/analysis/fr/FrenchLightStemmer.java
analysis/common/src/java/org/apache/lucene/analysis/fr/FrenchMinimalStemmer.java
analysis/common/src/java/org/apache/lucene/analysis/hu/HungarianLightStemmer.java
analysis/common/src/java/org/apache/lucene/analysis/it/ItalianLightStemmer.java
analysis/common/src/java/org/apache/lucene/analysis/pt/PortugueseLightStemmer.java
analysis/common/src/java/org/apache/lucene/analysis/ru/RussianLightStemmer.java
analysis/common/src/java/org/apache/lucene/analysis/sv/SwedishLightStemmer.java

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Kuromoji Japanese Morphological Analyzer - Apache Lucene Integration
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ASM Bytecode Manipulation Framework v8.0.1

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Independent JPEG Group: JPEG release 6b

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## CUP Parser Generator for Java v 0.11b
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Richard Grainger (github: liger1978)
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```
<!-- regenerate TOC with `markdown-toc -i` -->
```

```
<!-- toc -->
```

```
- [Platform Releases](#platform-releases)
```

```
  * [Default platform release ("ruby")](#default-platform-release-ruby)
```

```
  * [Native Linux platform releases ("x86_64-linux" and "arm64-linux")](#native-linux%E2%93%A1-platform-releases-x86_64-linux-and-arm64-linux)
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  * [Native Darwin (macOS) platform releases ("x86_64-darwin" and "arm64-darwin")](#native-darwin-macos%E2%93%A1-platform-releases-x86_64-darwin-and-arm64-darwin)
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  * [Java (JRuby) platform release ("java")](#java%E2%93%A1-jruby-platform-release-java)
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- [Appendix: Dependencies' License Texts](#appendix-dependencies-license-texts)
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  * [libxslt](#libxslt)
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  * [zlib](#zlib)
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  * [xml-apis](#xml-apis)
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<!-- tocstop -->
```

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You can double-check the dependencies in your gem file by examining the output of `nokogiri -v` after installation, which will emit the complete set of libraries in use (for versions `>= 1.11.0.rc4`).

In particular, I'm sure somebody's lawyer, somewhere, is going to freak out that the LGPL appears in this file; and so I'd like to take special note that the dependency covered by LGPL, `libiconv`, is only being redistributed in the native Windows and native Darwin platform releases. It's not present in default, Java, or native Linux releases.

Platform Releases

Default platform release ("ruby")

The default platform release distributes the following dependencies in source form:

- [libxml2](#libxml2)
- [libxslt](#libxslt)
- [libgumbo and nokogumbo](#libgumbo-and-nokogumbo)

This distribution can be identified by inspecting the included Gem::Specification, which will have the value "ruby" for its "platform" attribute.

Native Linux platform releases ("x86_64-linux" and "arm64-linux")

The native Linux platform release distributes the following dependencies in source form:

- [libxml2](#libxml2)
- [libxslt](#libxslt)
- [libgumbo and nokogumbo](#libgumbo-and-nokogumbo)
- [zlib](#zlib)

This distribution can be identified by inspecting the included Gem::Specification, which will have a value similar to "x86_64-linux" or "x86-linux" for its "platform.cpu" attribute.

Native Darwin (macOS) platform releases ("x86_64-darwin" and "arm64-darwin")

The native Darwin platform release distributes the following dependencies in source form:

- [libxml2](#libxml2)
- [libxslt](#libxslt)
- [libgumbo and nokogumbo](#libgumbo-and-nokogumbo)
- [zlib](#zlib)
- [libiconv](#libiconv)

This distribution can be identified by inspecting the included Gem::Specification, which will have a value similar to "x86_64-darwin" or "arm64-darwin" for its "platform.cpu" attribute. Darwin is also known more familiarly as "OSX" or "macOS" and is the operating system for many Apple computers.

Native Windows platform releases ("x86-mingw32" and "x64-mingw32")

The native Windows platform release distributes the following dependencies in source form:

- [libxml2](#libxml2)

- [libxslt](#libxslt)
- [libgumbo and nokogumbo](#libgumbo-and-nokogumbo)
- [zlib](#zlib)
- [libiconv](#libiconv)

This distribution can be identified by inspecting the included Gem::Specification, which will have a value similar to "x64-mingw32" or "x86-mingw32" for its "platform.cpu" attribute.

Java (JRuby) platform release ("java")

The Java platform release distributes the following dependencies as compiled jar files:

- [isorelax](#isorelax)
- [jing](#jing)
- [nekodtd](#nekodtd)
- [nekohtml](#nekohtml)
- [xalan](#xalan)
- [xerces](#xerces)
- [xml-apis](#xml-apis)

This distribution can be identified by inspecting the included Gem::Specification, which will have the value "java" for its "platform.os" attribute.

Appendix: Dependencies' License Texts

This section contains a subsection for each potentially-distributed dependency, which includes the name of the license and the license text.

Please see previous sections to understand which of these potential dependencies is actually distributed in the gem file you're downloading and using.

libgumbo and nokogumbo

Apache 2.0

<https://github.com/rubys/nokogumbo/blob/f6a7412/LICENSE.txt>

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Version 2.0, January 2004
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libiconv

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Mesa 3-D Graphics Library v21.0.3

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| | GL/ glxext.h | Khronos |
| | GL/ wglxext.h | Khronos |
| | KHR/ khrplatform.h | Khronos |

include/GL/gl.h :

Mesa 3-D graphics library

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```
include/GL/glex.h
include/GL/glxext.h
include/GL/wglxext.h :
```

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```
include/KHR/khrplatform.h :
```

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Little Color Management System (LCMS) v2.12

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<pre>

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xwd v1.0.7

xwd utility

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Jackson JSON processor

Jackson is a high-performance, Free/Open Source JSON processing library. It was originally written by Tatu Saloranta (tatu.saloranta@iki.fi), and has been in development since 2007. It is currently developed by a community of developers.

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Harfbuzz v4.4.1

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1.338 perl 5.32.0

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1.339 talloc 2.1.10

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Unix SMB/CIFS implementation.
Samba temporary memory allocation functions

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1.340 simplejson 2.0.9

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1.341 visual-studio-runtime 14.00.24210.0

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WinJS

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1.343 python-setuptools 44.0.0

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1.344 ini 1.62.0

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1.345 bzip2 1.0.8

1.346 tomcat-jsp-api 8.5.32

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jar/javax/servlet/jsp/tagext/SimpleTag.java
* /opt/cola/permits/1151085963_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-
jar/javax/servlet/jsp/tagext/TagData.java
* /opt/cola/permits/1151085963_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-
jar/javax/servlet/jsp/el/ELParseException.java
* /opt/cola/permits/1151085963_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-
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* /opt/cola/permits/1151085963_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-
jar/javax/servlet/jsp/JspContext.java
* /opt/cola/permits/1151085963_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-
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* /opt/cola/permits/1151085963_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-
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* /opt/cola/permits/1151085963_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-jar/javax/servlet/jsp/tagext/TagExtraInfo.java
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depcomp - compile a program generating dependencies as side-effects

scriptversion=2005-07-09.11

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1.355 quartz 2.3.2

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Upstream-Contact: Yann Collet <Cyan4973@github.com>

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1.361 json-java 20131018

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* /opt/cola/permits/1601182112_1679281943.5494998/0/json-20131018-sources-jar/org/json/zip/BitInputStream.java
* /opt/cola/permits/1601182112_1679281943.5494998/0/json-20131018-sources-jar/org/json/zip/PostMortem.java
* /opt/cola/permits/1601182112_1679281943.5494998/0/json-20131018-sources-jar/org/json/zip/MapKeep.java
* /opt/cola/permits/1601182112_1679281943.5494998/0/json-20131018-sources-jar/org/json/zip/JSONzip.java
* /opt/cola/permits/1601182112_1679281943.5494998/0/json-20131018-sources-jar/org/json/zip/Compressor.java
* /opt/cola/permits/1601182112_1679281943.5494998/0/json-20131018-sources-jar/org/json/zip/BitOutputStream.java
* /opt/cola/permits/1601182112_1679281943.5494998/0/json-20131018-sources-jar/org/json/Kim.java
* /opt/cola/permits/1601182112_1679281943.5494998/0/json-20131018-sources-jar/org/json/zip/TrieKeep.java
* /opt/cola/permits/1601182112_1679281943.5494998/0/json-20131018-sources-jar/org/json/zip/Huff.java
* /opt/cola/permits/1601182112_1679281943.5494998/0/json-20131018-sources-jar/org/json/zip/Keep.java

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* /opt/cola/permits/1601182112_1679281943.5494998/0/json-20131018-sources-jar/org/json/JSONObject.java
* /opt/cola/permits/1601182112_1679281943.5494998/0/json-20131018-sources-jar/org/json/JSONArray.java

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- * /opt/cola/permits/1601182112_1679281943.5494998/0/json-20131018-sources-jar/org/json/HTTP.java
- * /opt/cola/permits/1601182112_1679281943.5494998/0/json-20131018-sources-jar/org/json/HTTPTokener.java
- * /opt/cola/permits/1601182112_1679281943.5494998/0/json-20131018-sources-jar/org/json/CookieList.java
- * /opt/cola/permits/1601182112_1679281943.5494998/0/json-20131018-sources-jar/org/json/XML.java
- * /opt/cola/permits/1601182112_1679281943.5494998/0/json-20131018-sources-jar/org/json/CDL.java
- * /opt/cola/permits/1601182112_1679281943.5494998/0/json-20131018-sources-jar/org/json/XMLTokener.java
- * /opt/cola/permits/1601182112_1679281943.5494998/0/json-20131018-sources-jar/org/json/JSONTokener.java
- * /opt/cola/permits/1601182112_1679281943.5494998/0/json-20131018-sources-jar/org/json/Cookie.java

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* /opt/cola/permits/1601182112_1679281943.5494998/0/json-20131018-sources-jar/org/json/JSONWriter.java

* /opt/cola/permits/1601182112_1679281943.5494998/0/json-20131018-sources-jar/org/json/JSONStringer.java

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1.362 hk2-locator 2.5.0

1.363 jaxb-api 2.2.11

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- * /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-jar/javax/xml/bind/JAXBElement.java
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- * /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-jar/javax/xml/bind/annotation/XmlNsForm.java
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- * /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-jar/javax/xml/bind/annotation/XmlIDREF.java

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* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-jar/javax/xml/bind/PrintConversionEvent.java
* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-jar/javax/xml/bind/ParseConversionEvent.java
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* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-jar/javax/xml/bind/annotation/adapters/NormalizedStringAdapter.java
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* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-jar/javax/xml/bind/helpers/package.html

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* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-
jar/javax/xml/bind/GetPropertyAction.java
* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-
jar/javax/xml/bind/annotation/XmlSeeAlso.java
* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-
jar/javax/xml/bind/DataBindingException.java
* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-jar/javax/xml/bind/JAXB.java
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 * `/opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-jar/javax/xml/bind/UnmarshallerHandler.java`
 * `/opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-jar/javax/xml/bind/helpers/AbstractMarshallerImpl.java`
 * `/opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-jar/javax/xml/bind/DatatypeConverterInterface.java`
 * `/opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-jar/javax/xml/bind/ValidationEvent.java`
 * `/opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-`

jar/javax/xml/bind/util/JAXBSource.java
* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-
jar/javax/xml/bind/Unmarshaller.java
* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-
jar/javax/xml/bind/TypeConstraintException.java
* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-
jar/javax/xml/bind/helpers/ParseConversionEventImpl.java
* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-
jar/javax/xml/bind/ValidationException.java
* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-jar/javax/xml/bind/Element.java
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jar/javax/xml/bind/ContextFinder.java
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jar/javax/xml/bind/util/Messages.java
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jar/javax/xml/bind/helpers/AbstractUnmarshallerImpl.java
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jar/javax/xml/bind/ValidationEventHandler.java
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jar/javax/xml/bind/JAXBException.java
* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-

jar/javax/xml/bind/helpers/PrintConversionEventImpl.java
* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-
jar/javax/xml/bind/helpers/DefaultValidationEventHandler.java
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* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-
jar/javax/xml/bind/Messages.properties
* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-
jar/javax/xml/bind/util/Messages.properties
* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-
jar/javax/xml/bind/helpers/Messages.properties
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jar/javax/xml/bind/annotation/XmlAccessorOrder.java
* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-
jar/javax/xml/bind/annotation/XmlAccessType.java
* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-
jar/javax/xml/bind/annotation/XmlAnyAttribute.java
* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-
jar/javax/xml/bind/annotation/XmlMimeType.java
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jar/javax/xml/bind/annotation/XmlSchemaTypes.java
* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-
jar/javax/xml/bind/annotation/XmlMixed.java
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* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-
jar/javax/xml/bind/SchemaOutputResolver.java
* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-
jar/javax/xml/bind/annotation/XmlAnyElement.java
* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-jar/javax/xml/bind/Binder.java
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* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-
jar/javax/xml/bind/attachment/AttachmentMarshaller.java
* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-
jar/javax/xml/bind/annotation/XmlSchemaType.java

* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-jar/javax/xml/bind/annotation/XmlAccessorType.java
* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-jar/javax/xml/bind/annotation/XmlElementWrapper.java
* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-jar/javax/xml/bind/annotation/XmlList.java

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* /opt/cola/permits/1278352604_1654022869.4097037/0/jaxb-api-2-2-11-sources-jar/javax/xml/bind/DatatypeConverterImpl.java

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```

```
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1.365 future 0.18.2-1.ph3

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1.366 python-requests 2.23.0

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1.367 snowball-stemmer 1.2.1

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Snowball stemming library collection for Python

=====

This document pertains to the Python version of the stemmer library distribution, available for download from:

* <https://github.com/shibukawa/snowball/>

This repository includes Python source code generator

* https://github.com/shibukawa/snowball_python/

This repository includes generated Python source codes

Original program is maintained at following place:

* <http://snowball.tartarus.org/>

Original Snowball product created by Dr Martin Porter and Richard Boulton (Java porting). Original Snowball and my products are released under BSD license.

How to use library

The `snowballstemmer` module has two functions.

The `snowballstemmer.algorithms` function returns a list of available algorithm name' string.

The `snowballstemmer.stemmer` function accepts algorithm name and returns `Stemmer` objects.

`Stemmer` objects have `Stemmer.stemWord(word)` method and `Stemmer.stemWords(word[])` method.

.. code-block:: python

```
import snowballstemmer

stemmer = snowballstemmer.stemmer('english');
print(stemmer.stemWords("We are the world".split()));
```

`Stemmer` objects have `Stemmer.maxCacheSize` property. They cache result within the value. Default is `10000`.

Accerates Stemming

if `PyStemmer` is installed, `snowballstemmer.stemmer` returns `PyStemmer`'s `Stemmer` objects. This `Stemmer` object has same methods (`Stemmer.stemWord()`, `Stemmer.stemWords()`).

`PyStemmer` is a Snowball's `libstemmer_c` wrapper module and it returns 100% compatible result with `snowballstemmer`.

`PyStemmer` has faster speed because it uses C-lang module, and `snowballstemmer` has higher usability because it is pure Python module.

* `PyStemmer` <<http://pypi.python.org/pypi/PyStemmer/>>_

Benchmark

~~~~~

Test Case: Snowball stemmer check data (16 algorithms, total 582560 words, cache hit 0%)

Computer: MacBook Pro 3rd Gen Corei7 2.3GHz

\* Python 2.7 + `snowballstemmer` : 2m 30s

\* PyPy 1.9 + `snowballstemmer` : 45s

\* Python 2.7 + `PyStemmer` : 5s

This test case is much harder than usual usecases!

## The TestApp example

-----

The `testapp.py` example program allows you to run any of the stemmers on a sample vocabulary.

Usage::

```
testapp.py <algorithm> "sentences ... "
```

.. code-block:: bash

```
$ python testapp.py English "sentences... "
```

## Thanks

-----

\* Original Snowball authors

\* Emil Stenstrm

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-----

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\* /opt/cola/permits/1111463316\_1606927819.08/0/snowballstemmer-1-2-1-1-tar-gz/snowballstemmer-1.2.1/README.rst

No license file was found, but licenses were detected in source scan.

Metadata-Version: 1.1

Name: snowballstemmer

Version: 1.2.1

Summary: This package provides 16 stemmer algorithms (15 + Porter English stemmer) generated from Snowball algorithms.

Home-page: [https://github.com/shibukawa/snowball\\_py](https://github.com/shibukawa/snowball_py)

Author: Yoshiki Shibukawa

Author-email: yoshiki at shibu.jp

License: BSD

Description:

It includes following language algorithms:

- \* Danish

- \* Dutch
- \* English (Standard, Porter)
- \* Finnish
- \* French
- \* German
- \* Hungarian
- \* Italian
- \* Norwegian
- \* Portuguese
- \* Romanian
- \* Russian
- \* Spanish
- \* Swedish
- \* Turkish

This is a pure Python stemming library. If `PyStemmer` <<http://pypi.python.org/pypi/PyStemmer>>`\_ is available, this module uses it to accelerate.

Keywords: stemmer

Platform: UNKNOWN

Classifier: Development Status :: 5 - Production/Stable

Classifier: Intended Audience :: Developers

Classifier: License :: OSI Approved :: BSD License

Classifier: Programming Language :: Python

Classifier: Natural Language :: Danish

Classifier: Natural Language :: Dutch

Classifier: Natural Language :: English

Classifier: Natural Language :: Finnish

Classifier: Natural Language :: French

Classifier: Natural Language :: German

Classifier: Natural Language :: Hungarian

Classifier: Natural Language :: Italian

Classifier: Natural Language :: Norwegian

Classifier: Natural Language :: Portuguese

Classifier: Natural Language :: Romanian

Classifier: Natural Language :: Russian

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Classifier: Operating System :: OS Independent

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Classifier: Programming Language :: Python :: 2.6

Classifier: Programming Language :: Python :: 2.7

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Classifier: Programming Language :: Python :: Implementation :: PyPy

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Classifier: Topic :: Internet :: WWW/HTTP :: Indexing/Search

Classifier: Topic :: Text Processing :: Indexing

Classifier: Topic :: Text Processing :: Linguistic

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Found in path(s):

## 1.368 snowball-stemmer 1.2.1-2.ph3

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## 1.369 bzip2 1.0.7

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## 1.370 txw2 2.3.0

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# 1.371 python-certifi 2021.10.08

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This package contains a modified version of ca-bundle.crt:

ca-bundle.crt -- Bundle of CA Root Certificates

Certificate data from Mozilla as of: Thu Nov 3 19:04:19 2011#

This is a bundle of X.509 certificates of public Certificate Authorities (CA). These were automatically extracted from Mozilla's root certificates file (certdata.txt). This file can be found in the mozilla source tree:

<http://mxr.mozilla.org/mozilla/source/security/nss/lib/ckfw/builtins/certdata.txt?raw=1#>

It contains the certificates in PEM format and therefore can be directly used with curl / libcurl / php\_curl, or with an Apache+mod\_ssl webserver for SSL client authentication. Just configure this file as the SSLCACertificateFile.#

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@(#) \$RCSfile: certdata.txt,v \$ \$Revision: 1.80 \$ \$Date: 2011/11/03 15:11:58 \$

# 1.372 asciidoctor 2.0.10

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# 1.373 urllib3 1.25.11

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# Contributions to the urllib3 project

## Creator & Maintainer

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## Contributors

In chronological order:

\* [victor.vde](http://code.google.com/u/victor.vde/) <<http://code.google.com/u/victor.vde/>>

\* HTTPS patch (which inspired HTTPSConnectionPool)

\* [erikcederstrand](http://code.google.com/u/erikcederstrand/) <<http://code.google.com/u/erikcederstrand/>>

\* NTLM-authenticated HTTPSConnectionPool

\* Basic-authenticated HTTPSConnectionPool (merged into make\_headers)

- \* niphlod <niphlod@gmail.com>
- \* Client-verified SSL certificates for HTTPSConnectionPool
- \* Response gzip and deflate encoding support
- \* Better unicode support for filepost using StringIO buffers
  
- \* btoconnor <brian@btoconnor.net>
- \* Non-multipart encoding for POST requests
  
- \* p.dobrogost <http://code.google.com/u/@WBRSRIBZDhBFXQB6/>
- \* Code review, PEP8 compliance, benchmark fix
  
- \* kennethreitz <me@kennethreitz.com>
- \* Bugfixes, suggestions, Requests integration
  
- \* georgemarshall <https://github.com/georgemarshall>
- \* Bugfixes, Improvements and Test coverage
  
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- \* Python 3 support
  
- \* brandon-rhodes <http://rhodesmill.org/brandon>
- \* Design review, bugfixes, test coverage.
  
- \* studer <theo.studer@gmail.com>
- \* IPv6 url support and test coverage
  
- \* Shivaram Lingamneni <slingamn@cs.stanford.edu>
- \* Support for explicitly closing pooled connections
  
- \* hartator <hartator@gmail.com>
- \* Corrected multipart behavior for params
  
- \* Thomas Weischuh <thomas@t-8ch.de>
- \* Support for TLS SNI
- \* API unification of ssl\_version/cert\_reqs
- \* SSL fingerprint and alternative hostname verification
- \* Bugfixes in testsuite
  
- \* Sune Kirkeby <mig@ibofobi.dk>
- \* Optional SNI-support for Python 2 via PyOpenSSL.
  
- \* Marc Schlaich <marc.schlaich@gmail.com>
- \* Various bugfixes and test improvements.
  
- \* Bryce Boe <bbzbryce@gmail.com>
- \* Correct six.moves conflict
- \* Fixed pickle support of some exceptions

- \* Boris Figovsky <boris.figovsky@ravello systems.com>
- \* Allowed to skip SSL hostname verification
  
- \* Cory Benfield <https://lukasa.co.uk/about/>
- \* Stream method for Response objects.
- \* Return native strings in header values.
- \* Generate 'Host' header when using proxies.
  
- \* Jason Robinson <jaywink@basshero.org>
- \* Add missing WrappedSocket.fileno method in PyOpenSSL
  
- \* Audrius Butkevicius <audrius.butkevicius@elastic hosts.com>
- \* Fixed a race condition
  
- \* Stanislav Vitkovskiy <stas.vitkovsky@gmail.com>
- \* Added HTTPS (CONNECT) proxy support
  
- \* Stephen Holsapple <sholsapp@gmail.com>
- \* Added abstraction for granular control of request fields
  
- \* Martin von Gagern <Martin.vGagern@gmx.net>
- \* Support for non-ASCII header parameters
  
- \* Kevin Burke <kev@inburke.com> and Pavel Kirichenko <juanych@yandex-team.ru>
- \* Support for separate connect and request timeouts
  
- \* Peter Waller <p@pwaller.net>
- \* HTTPResponse.tell() for determining amount received over the wire
  
- \* Nipunn Koorapati <nipunn1313@gmail.com>
- \* Ignore default ports when comparing hosts for equality
  
- \* Danilo @dbrgn <https://dbrgn.ch/>
- \* Disabled TLS compression by default on Python 3.2+
- \* Disabled TLS compression in pyopenssl contrib module
- \* Configurable cipher suites in pyopenssl contrib module
  
- \* Roman Bogorodskiy <roman.bogorodskiy@ericsson.com>
- \* Account retries on proxy errors
  
- \* Nicolas Delaby <nicolas.delaby@ezeep.com>
- \* Use the platform-specific CA certificate locations
  
- \* Josh Schneier <https://github.com/jschneier>
- \* HTTPHeaderDict and associated tests and docs
- \* Bugfixes, docs, test coverage
  
- \* Tahia Khan <http://tahia.tk/>

- \* Added Timeout examples in docs
  
- \* Arthur Grunseid <<https://grunseid.com>>
  - \* source\_address support and tests (with <https://github.com/bui>)
  
- \* Ian Cordasco <[graffatcolmingov@gmail.com](mailto:graffatcolmingov@gmail.com)>
  - \* PEP8 Compliance and Linting
  - \* Add ability to pass socket options to an HTTP Connection
  
- \* Erik Tollerud <[erik.tollerud@gmail.com](mailto:erik.tollerud@gmail.com)>
  - \* Support for standard library io module.
  
- \* Krishna Prasad <[kprasad.iitd@gmail.com](mailto:kprasad.iitd@gmail.com)>
  - \* Google App Engine documentation
  
- \* Aaron Meurer <[asmeurer@gmail.com](mailto:asmeurer@gmail.com)>
  - \* Added Url.url, which unparses a Url
  
- \* Evgeny Kapun <[abacabadabacaba@gmail.com](mailto:abacabadabacaba@gmail.com)>
  - \* Bugfixes
  
- \* Benjamin Meyer <[bm\\_witness@yahoo.com](mailto:bm_witness@yahoo.com)>
  - \* Security Warning Documentation update for proper capture
  
- \* Shivan Sornarajah <[github@sornars.com](mailto:github@sornars.com)>
  - \* Support for using ConnectionPool and PoolManager as context managers.
  
- \* Alex Gaynor <[alex.gaynor@gmail.com](mailto:alex.gaynor@gmail.com)>
  - \* Updates to the default SSL configuration
  
- \* Tomas Tomecek <[ttomecek@redhat.com](mailto:ttomecek@redhat.com)>
  - \* Implemented generator for getting chunks from chunked responses.
  
- \* tlynn <<https://github.com/tlynn>>
  - \* Respect the warning preferences at import.
  
- \* David D. Riddle <[ddriddle@illinois.edu](mailto:ddriddle@illinois.edu)>
  - \* IPv6 bugfixes in testsuite
  
- \* Thea Flowers <[magicalgirl@google.com](mailto:magicalgirl@google.com)>
  - \* App Engine environment tests.
  - \* Documentation re-write.
  
- \* John Krauss <<https://github.com/talos>>
  - \* Clues to debugging problems with `cryptography` dependency in docs
  
- \* Disassem <<https://github.com/Disassem>>
  - \* Fix pool-default headers not applying for url-encoded requests like GET.

- \* James Atherfold <jlatherfold@hotmail.com>
- \* Bugfixes relating to cleanup of connections during errors.
  
- \* Christian Pedersen <https://github.com/chripede>
- \* IPv6 HTTPS proxy bugfix
  
- \* Jordan Moldow <https://github.com/jmoldow>
- \* Fix low-level exceptions leaking from ``HTTPResponse.stream()``.
- \* Bugfix for ``ConnectionPool.urlopen(release\_conn=False)``.
- \* Creation of ``HTTPConnectionPool.ResponseCls``.
  
- \* Predrag Gruevski <https://github.com/obi1kenobi>
- \* Made cert digest comparison use a constant-time algorithm.
  
- \* Adam Talsma <https://github.com/a-tal>
- \* Bugfix to ca\_cert file paths.
  
- \* Evan Meagher <https://evanmeagher.net>
- \* Bugfix related to `memoryview` usage in PyOpenSSL adapter
  
- \* John Vandenberg <jayvdb@gmail.com>
- \* Python 2.6 fixes; pyflakes and pep8 compliance
  
- \* Andy Caldwell <andy.m.caldwell@googlemail.com>
- \* Bugfix related to reusing connections in indeterminate states.
  
- \* Ville Skytt <ville.skytta@iki.fi>
- \* Logging efficiency improvements, spelling fixes, Travis config.
  
- \* Shige Takeda <smtakeda@gmail.com>
- \* Started Recipes documentation and added a recipe about handling concatenated gzip data in HTTP response
  
- \* Jess Shapiro <jesse@jesseshapiro.net>
- \* Various character-encoding fixes/tweaks
- \* Disabling IPv6 DNS when IPv6 connections not supported
  
- \* David Foster <http://dafoster.net/>
- \* Ensure order of request and response headers are preserved.
  
- \* Jeremy Cline <jeremy@jcline.org>
- \* Added connection pool keys by scheme
  
- \* Aviv Palivoda <palaviv@gmail.com>
- \* History list to Retry object.
- \* HTTPResponse contains the last Retry object.
  
- \* Nate Prewitt <nate.prewitt@gmail.com>

- \* Ensure timeouts are not booleans and greater than zero.
- \* Fixed infinite loop in ``stream`` when amt=None.
- \* Added length\_remaining to determine remaining data to be read.
- \* Added enforce\_content\_length to raise exception when incorrect content-length received.
  
- \* Seth Michael Larson <sethmichaellarson@protonmail.com>
- \* Created selectors backport that supports PEP 475.
  
- \* Alexandre Dias <alex.dias@smarkets.com>
- \* Don't retry on timeout if method not in whitelist
  
- \* Moinuddin Quadri <moin18@gmail.com>
- \* Lazily load idna package
  
- \* Tom White <s6ygl3z3@mail2tor.com>
- \* Made SOCKS handler differentiate socks5h from socks5 and socks4a from socks4.
  
- \* Tim Burke <tim.burke@gmail.com>
- \* Stop buffering entire deflate-encoded responses.
  
- \* Tuukka Mustonen <tuukka.mustonen@gmail.com>
- \* Add counter for status\_forcelist retries.
  
- \* Erik Rose <erik@mozilla.com>
- \* Bugfix to pyopenssl vendoring
  
- \* Wolfgang Richter <wolfgang.richter@gmail.com>
- \* Bugfix related to loading full certificate chains with PyOpenSSL backend.
  
- \* Mike Miller <github@mikeage.net>
- \* Logging improvements to include the HTTP(S) port when opening a new connection
  
- \* Ioannis Tziakos <mail@itziakos.gr>
- \* Fix ``util.selectors.\_fileobj\_to\_fd`` to accept ``long``.
- \* Update appveyor tox setup to use the 64bit python.
  
- \* Akamai (through Jess Shapiro) <jshapiro@akamai.com>
- \* Ongoing maintenance; 2017-2018
  
- \* Dominique Leuenberger <dimstar@opensuse.org>
- \* Minor fixes in the test suite
  
- \* Will Bond <will@wbond.net>
- \* Add Python 2.6 support to ``contrib.securetransport``
  
- \* Aleksei Alekseev <alekseev.yeskela@gmail.com>
- \* using auth info for socks proxy

- \* Chris Wilcox <git@crwilcox.com>
- \* Improve contribution guide
- \* Add ``HTTPResponse.geturl`` method to provide ``urllib2.urlopen().geturl()`` behavior
  
- \* Bruce Merry <https://www.brucemerry.org.za>
- \* Fix leaking exceptions when system calls are interrupted with zero timeout
  
- \* Hugo van Kemenade <https://github.com/hugovk>
- \* Drop support for EOL Python 2.6
  
- \* Tim Bell <https://github.com/timb07>
- \* Bugfix for responses with Content-Type: message/\* logging warnings
  
- \* Justin Bramley <https://github.com/jbramleycl>
- \* Add ability to handle multiple Content-Encodings
  
- \* Katsuhiko YOSHIDA <https://github.com/kyoshidajp>
- \* Remove Authorization header regardless of case when redirecting to cross-site
  
- \* James Meickle <https://permadeath.com/>
- \* Improve handling of Retry-After header
  
- \* Chris Jerdonek <chris.jerdonek@gmail.com>
- \* Remove a spurious TypeError from the exception chain inside HTTPConnectionPool.\_make\_request(), also for BaseExceptions.
  
- \* Jorge Lopez Silva <https://github.com/jalopezsilva>
- \* Added support for forwarding requests through HTTPS proxies.
  
- \* Benno Rice <benno@jeamland.net>
- \* Allow cadata parameter to be passed to underlying ``SSLContext.load\_verify\_locations()``.
  
- \* Keiichi Kobayashi <abok.1k@gmail.com>
- \* Rename VerifiedHTTPSConnection to HTTPSConnection
  
- \* Himanshu Garg <garg\_himanshu@outlook.com>
- \* DOC & LICENSE Update
  
- \* Hod Bin Noon <bin.noon.hod@gmail.com>
- \* Test improvements
  
- \* Chris Olufson <tycarac@gmail.com>
- \* Fix for connection not being released on HTTP redirect and response not preloaded
  
- \* [Bastiaan Bakker] <https://github.com/bastiaanb>
- \* Support for logging session keys via environment variable ``SSLKEYLOGFILE`` (Python 3.8+)
  
- \* [Your name or handle] <[email or website]>

\* [Brief summary of your changes]

# 1.374 jakarta-ws-rs-api 2.1.5

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\* @version 3.0 (December 2000)

\*

\* Optimised ANSI C code for the Rijndael cipher (now AES)

\*

\* @author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>

\* @author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>

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# 1.384 urllib3 1.25.8-2ubuntu0.1

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# Contributions to the urllib3 project

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In chronological order:

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\* HTTPS patch (which inspired HTTPSConnectionPool)

\* erikcederstrand <<http://code.google.com/u/erikcederstrand/>>

\* NTLM-authenticated HTTPSConnectionPool

\* Basic-authenticated HTTPSConnectionPool (merged into make\_headers)

\* niphlod <niphlod@gmail.com>

\* Client-verified SSL certificates for HTTPSConnectionPool

\* Response gzip and deflate encoding support

\* Better unicode support for filepost using StringIO buffers

\* btoconnor <brian@btoconnor.net>

\* Non-multipart encoding for POST requests

\* p.dobrogost <<http://code.google.com/u/@WBRSRIBZDhBFXQB6/>>

\* Code review, PEP8 compliance, benchmark fix

\* kennethreitz <me@kennethreitz.com>

\* Bugfixes, suggestions, Requests integration

\* georgemarshall <<https://github.com/georgemarshall>>

\* Bugfixes, Improvements and Test coverage

\* Thomas Kluyver <thomas@kluyver.me.uk>

\* Python 3 support

\* brandon-rhodes <<http://rhodesmill.org/brandon>>

\* Design review, bugfixes, test coverage.

\* studer <theo.studer@gmail.com>

\* IPv6 url support and test coverage

- \* Shivaram Lingamneni <slingamn@cs.stanford.edu>
  - \* Support for explicitly closing pooled connections
  
- \* hartator <hartator@gmail.com>
  - \* Corrected multipart behavior for params
  
- \* Thomas Weischuh <thomas@t-8ch.de>
  - \* Support for TLS SNI
  - \* API unification of ssl\_version/cert\_reqs
  - \* SSL fingerprint and alternative hostname verification
  - \* Bugfixes in testsuite
  
- \* Sune Kirkeby <mig@ibofobi.dk>
  - \* Optional SNI-support for Python 2 via PyOpenSSL.
  
- \* Marc Schlaich <marc.schlaich@gmail.com>
  - \* Various bugfixes and test improvements.
  
- \* Bryce Boe <bbzbryce@gmail.com>
  - \* Correct six.moves conflict
  - \* Fixed pickle support of some exceptions
  
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  - \* Allowed to skip SSL hostname verification
  
- \* Cory Benfield <<https://lukasa.co.uk/about/>>
  - \* Stream method for Response objects.
  - \* Return native strings in header values.
  - \* Generate 'Host' header when using proxies.
  
- \* Jason Robinson <jaywink@basshero.org>
  - \* Add missing WrappedSocket.fileno method in PyOpenSSL
  
- \* Audrius Butkevicius <audrius.butkevicius@elastichosts.com>
  - \* Fixed a race condition
  
- \* Stanislav Vitkovskiy <stas.vitkovsky@gmail.com>
  - \* Added HTTPS (CONNECT) proxy support
  
- \* Stephen Holsapple <sholsapp@gmail.com>
  - \* Added abstraction for granular control of request fields
  
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  - \* Support for non-ASCII header parameters
  
- \* Kevin Burke <kev@inburke.com> and Pavel Kirichenko <juanych@yandex-team.ru>
  - \* Support for separate connect and request timeouts

- \* Peter Waller <p@pwaller.net>
- \* HTTPResponse.tell() for determining amount received over the wire
  
- \* Nipunn Koorapati <nipunn1313@gmail.com>
- \* Ignore default ports when comparing hosts for equality
  
- \* Danilo @dbrgn <https://dbrgn.ch/>
- \* Disabled TLS compression by default on Python 3.2+
- \* Disabled TLS compression in pyopenssl contrib module
- \* Configurable cipher suites in pyopenssl contrib module
  
- \* Roman Bogorodskiy <roman.bogorodskiy@ericsson.com>
- \* Account retries on proxy errors
  
- \* Nicolas Delaby <nicolas.delaby@ezeep.com>
- \* Use the platform-specific CA certificate locations
  
- \* Josh Schneier <https://github.com/jschneier>
- \* HTTPHeaderDict and associated tests and docs
- \* Bugfixes, docs, test coverage
  
- \* Tahia Khan <http://tahia.tk/>
- \* Added Timeout examples in docs
  
- \* Arthur Grunseid <https://grunseid.com>
- \* source\_address support and tests (with https://github.com/bui)
  
- \* Ian Cordasco <graffatcolmingov@gmail.com>
- \* PEP8 Compliance and Linting
- \* Add ability to pass socket options to an HTTP Connection
  
- \* Erik Tollerud <erik.tollerud@gmail.com>
- \* Support for standard library io module.
  
- \* Krishna Prasad <kprasad.iitd@gmail.com>
- \* Google App Engine documentation
  
- \* Aaron Meurer <asmeurer@gmail.com>
- \* Added Url.url, which unparses a Url
  
- \* Evgeny Kapun <abacabadabacaba@gmail.com>
- \* Bugfixes
  
- \* Benjamin Meyer <bm\_witness@yahoo.com>
- \* Security Warning Documentation update for proper capture
  
- \* Shivan Sornarajah <github@sornars.com>
- \* Support for using ConnectionPool and PoolManager as context managers.

- \* Alex Gaynor <alex.gaynor@gmail.com>
- \* Updates to the default SSL configuration
  
- \* Tomas Tomecek <ttomecek@redhat.com>
- \* Implemented generator for getting chunks from chunked responses.
  
- \* tlynn <https://github.com/tlynn>
- \* Respect the warning preferences at import.
  
- \* David D. Riddle <ddriddle@illinois.edu>
- \* IPv6 bugfixes in testsuite
  
- \* Thea Flowers <magicalgirl@google.com>
- \* App Engine environment tests.
- \* Documentation re-write.
  
- \* John Krauss <https://github.com/talos>
- \* Clues to debugging problems with `cryptography` dependency in docs
  
- \* Disassem <https://github.com/Disassem>
- \* Fix pool-default headers not applying for url-encoded requests like GET.
  
- \* James Atherfold <jlatherfold@hotmail.com>
- \* Bugfixes relating to cleanup of connections during errors.
  
- \* Christian Pedersen <https://github.com/chripede>
- \* IPv6 HTTPS proxy bugfix
  
- \* Jordan Moldow <https://github.com/jmoldow>
- \* Fix low-level exceptions leaking from ``HTTPResponse.stream()``.
- \* Bugfix for ``ConnectionPool.urlopen(release\_conn=False)``.
- \* Creation of ``HTTPConnectionPool.ResponseCls``.
  
- \* Predrag Gruevski <https://github.com/obi1kenobi>
- \* Made cert digest comparison use a constant-time algorithm.
  
- \* Adam Talsma <https://github.com/a-tal>
- \* Bugfix to ca\_cert file paths.
  
- \* Evan Meagher <https://evanmeagher.net>
- \* Bugfix related to `memoryview` usage in PyOpenSSL adapter
  
- \* John Vandenberg <jayvdb@gmail.com>
- \* Python 2.6 fixes; pyflakes and pep8 compliance
  
- \* Andy Caldwell <andy.m.caldwell@googlemail.com>
- \* Bugfix related to reusing connections in indeterminate states.

- \* Ville Skytt <ville.skytta@iki.fi>
- \* Logging efficiency improvements, spelling fixes, Travis config.
  
- \* Shige Takeda <smtakeda@gmail.com>
- \* Started Recipes documentation and added a recipe about handling concatenated gzip data in HTTP response
  
- \* Jess Shapiro <jesse@jesseshapiro.net>
- \* Various character-encoding fixes/tweaks
- \* Disabling IPv6 DNS when IPv6 connections not supported
  
- \* David Foster <http://dafoster.net/>
- \* Ensure order of request and response headers are preserved.
  
- \* Jeremy Cline <jeremy@jcline.org>
- \* Added connection pool keys by scheme
  
- \* Aviv Palivoda <palaviv@gmail.com>
- \* History list to Retry object.
- \* HTTPResponse contains the last Retry object.
  
- \* Nate Prewitt <nate.prewitt@gmail.com>
- \* Ensure timeouts are not booleans and greater than zero.
- \* Fixed infinite loop in ``stream`` when amt=None.
- \* Added length\_remaining to determine remaining data to be read.
- \* Added enforce\_content\_length to raise exception when incorrect content-length received.
  
- \* Seth Michael Larson <sethmichaellarson@protonmail.com>
- \* Created selectors backport that supports PEP 475.
  
- \* Alexandre Dias <alex.dias@smarkets.com>
- \* Don't retry on timeout if method not in whitelist
  
- \* Moinuddin Quadri <moin18@gmail.com>
- \* Lazily load idna package
  
- \* Tom White <s6yg1ez3@mail2tor.com>
- \* Made SOCKS handler differentiate socks5h from socks5 and socks4a from socks4.
  
- \* Tim Burke <tim.burke@gmail.com>
- \* Stop buffering entire deflate-encoded responses.
  
- \* Tuukka Mustonen <tuukka.mustonen@gmail.com>
- \* Add counter for status\_forcelist retries.
  
- \* Erik Rose <erik@mozilla.com>
- \* Bugfix to pyopenssl vendoring

- \* Wolfgang Richter <wolfgang.richter@gmail.com>
- \* Bugfix related to loading full certificate chains with PyOpenSSL backend.
  
- \* Mike Miller <github@mikeage.net>
- \* Logging improvements to include the HTTP(S) port when opening a new connection
  
- \* Ioannis Tziakos <mail@itziakos.gr>
- \* Fix ``util.selectors.\_fileobj\_to\_fd`` to accept ``long``.
- \* Update appveyor tox setup to use the 64bit python.
  
- \* Akamai (through Jess Shapiro) <jshapiro@akamai.com>
- \* Ongoing maintenance; 2017-2018
  
- \* Dominique Leuenberger <dimstar@opensuse.org>
- \* Minor fixes in the test suite
  
- \* Will Bond <will@wbond.net>
- \* Add Python 2.6 support to ``contrib.securetransport``
  
- \* Aleksei Alekseev <alekseev.yeskela@gmail.com>
- \* using auth info for socks proxy
  
- \* Chris Wilcox <git@crwilcox.com>
- \* Improve contribution guide
- \* Add ``HTTPResponse.geturl`` method to provide ``urllib2.urlopen().geturl()`` behavior
  
- \* Bruce Merry <https://www.brucemerry.org.za>
- \* Fix leaking exceptions when system calls are interrupted with zero timeout
  
- \* Hugo van Kemenade <https://github.com/hugovk>
- \* Drop support for EOL Python 2.6
  
- \* Tim Bell <https://github.com/timb07>
- \* Bugfix for responses with Content-Type: message/\* logging warnings
  
- \* Justin Bramley <https://github.com/jbramleycl>
- \* Add ability to handle multiple Content-Encodings
  
- \* Katsuhiko YOSHIDA <https://github.com/kyoshidajp>
- \* Remove Authorization header regardless of case when redirecting to cross-site
  
- \* James Meickle <https://permadeath.com/>
- \* Improve handling of Retry-After header
  
- \* Chris Jerdonek <chris.jerdonek@gmail.com>
- \* Remove a spurious TypeError from the exception chain inside  
 HTTPConnectionPool.\_make\_request(), also for BaseExceptions.

\* [Your name or handle] <[email or website]>

\* [Brief summary of your changes]

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Upstream-Contact: Andrey Petrov <andrey.petrov@shazow.net>

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Files: \*

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## 1.391 websocket-client 0.59.0

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# 1.392 zlib 1.2.3

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/\* zlib.h -- interface of the 'zlib' general purpose compression library  
version 1.2.11, January 15th, 2017

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\*/

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## 1.393 flex 2.6.4

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## 1.395 glib 2.46.2

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-----  
\*/

```
#ifndef _PCRE_H
#define _PCRE_H
```

```
/* The current PCRE version information. */
```

```
#define PCRE_MAJOR      8
#define PCRE_MINOR      31
#define PCRE_PRERELEASE
#define PCRE_DATE       2012-07-06
```

```
/* When an application links to a PCRE DLL in Windows, the symbols that are imported have to be identified as such. When building PCRE, the appropriate export setting is defined in pcre_internal.h, which includes this file. So we don't change existing definitions of PCRE_EXP_DECL and PCRECPP_EXP_DECL. */
```

```
#if defined(_WIN32) && !defined(PCRE_STATIC)
#  ifndef PCRE_EXP_DECL
#    define PCRE_EXP_DECL extern __declspec(dllexport)
#  endif
#  ifdef __cplusplus
#    ifndef PCRECPP_EXP_DECL
#      define PCRECPP_EXP_DECL extern __declspec(dllexport)
#    endif
#  endif
#endif
```

```

# ifndef PCRECPP_EXP_DEFN
#   define PCRECPP_EXP_DEFN __declspec(dllimport)
# endif
# endif
#endif

/* By default, we use the standard "extern" declarations. */

#ifndef PCRE_EXP_DECL
# ifdef __cplusplus
#   define PCRE_EXP_DECL extern "C"
# else
#   define PCRE_EXP_DECL extern
# endif
#endif

#ifdef __cplusplus
# ifndef PCRECPP_EXP_DECL
#   define PCRECPP_EXP_DECL extern
# endif
# ifndef PCRECPP_EXP_DEFN
#   define PCRECPP_EXP_DEFN
# endif
#endif

/* Have to include stdlib.h in order to ensure that size_t is defined;
it is needed here for malloc. */

#include <stdlib.h>

/* Allow for C++ users */

#ifdef __cplusplus
extern "C" {
#endif

/* Options. Some are compile-time only, some are run-time only, and some are
both, so we keep them all distinct. However, almost all the bits in the options
word are now used. In the long run, we may have to re-use some of the
compile-time only bits for runtime options, or vice versa. In the comments
below, "compile", "exec", and "DFA exec" mean that the option is permitted to
be set for those functions; "used in" means that an option may be set only for
compile, but is subsequently referenced in exec and/or DFA exec. Any of the
compile-time options may be inspected during studying (and therefore JIT
compiling). */

#define PCRE_CASELESS      0x00000001 /* Compile */
#define PCRE_MULTILINE     0x00000002 /* Compile */

```

```

#define PCRE_DOTALL      0x00000004 /* Compile */
#define PCRE_EXTENDED    0x00000008 /* Compile */
#define PCRE_ANCHORED    0x00000010 /* Compile, exec, DFA exec */
#define PCRE_DOLLAR_ENDONLY 0x00000020 /* Compile, used in exec, DFA exec */
#define PCRE_EXTRA       0x00000040 /* Compile */
#define PCRE_NOTBOL      0x00000080 /* Exec, DFA exec */
#define PCRE_NOTEOL      0x00000100 /* Exec, DFA exec */
#define PCRE_UNGREEDY    0x00000200 /* Compile */
#define PCRE_NOTEMPTY    0x00000400 /* Exec, DFA exec */
/* The next two are also used in exec and DFA exec */
#define PCRE_UTF8        0x00000800 /* Compile (same as PCRE_UTF16) */
#define PCRE_UTF16       0x00000800 /* Compile (same as PCRE_UTF8) */
#define PCRE_NO_AUTO_CAPTURE 0x00001000 /* Compile */
/* The next two are also used in exec and DFA exec */
#define PCRE_NO_UTF8_CHECK 0x00002000 /* Compile (same as PCRE_NO_UTF16_CHECK) */
#define PCRE_NO_UTF16_CHECK 0x00002000 /* Compile (same as PCRE_NO_UTF8_CHECK) */
#define PCRE_AUTO_CALLOUT 0x00004000 /* Compile */
#define PCRE_PARTIAL_SOFT 0x00008000 /* Exec, DFA exec */
#define PCRE_PARTIAL      0x00008000 /* Backwards compatible synonym */
#define PCRE_DFA_SHORTEST 0x00010000 /* DFA exec */
#define PCRE_DFA_RESTART  0x00020000 /* DFA exec */
#define PCRE_FIRSTLINE    0x00040000 /* Compile, used in exec, DFA exec */
#define PCRE_DUPNAMES     0x00080000 /* Compile */
#define PCRE_NEWLINE_CR   0x00100000 /* Compile, exec, DFA exec */
#define PCRE_NEWLINE_LF   0x00200000 /* Compile, exec, DFA exec */
#define PCRE_NEWLINE_CRLF 0x00300000 /* Compile, exec, DFA exec */
#define PCRE_NEWLINE_ANY  0x00400000 /* Compile, exec, DFA exec */
#define PCRE_NEWLINE_ANYCRLF 0x00500000 /* Compile, exec, DFA exec */
#define PCRE_BSR_ANYCRLF  0x00800000 /* Compile, exec, DFA exec */
#define PCRE_BSR_UNICODE  0x01000000 /* Compile, exec, DFA exec */
#define PCRE_JAVASCRIPT_COMPAT 0x02000000 /* Compile, used in exec */
#define PCRE_NO_START_OPTIMIZE 0x04000000 /* Compile, exec, DFA exec */
#define PCRE_NO_START_OPTIMISE 0x04000000 /* Synonym */
#define PCRE_PARTIAL_HARD  0x08000000 /* Exec, DFA exec */
#define PCRE_NOTEMPTY_ATSTART 0x10000000 /* Exec, DFA exec */
#define PCRE_UCP           0x20000000 /* Compile, used in exec, DFA exec */

```

```

/* Exec-time and get/set-time error codes */

```

```

#define PCRE_ERROR_NOMATCH      (-1)
#define PCRE_ERROR_NULL        (-2)
#define PCRE_ERROR_BADOPTION    (-3)
#define PCRE_ERROR_BADMAGIC     (-4)
#define PCRE_ERROR_UNKNOWN_OPCODE (-5)
#define PCRE_ERROR_UNKNOWN_NODE  (-5) /* For backward compatibility */
#define PCRE_ERROR_NOMEMORY     (-6)
#define PCRE_ERROR_NOSUBSTRING  (-7)
#define PCRE_ERROR_MATCHLIMIT   (-8)

```

```

#define PCRE_ERROR_CALLOUT      (-9) /* Never used by PCRE itself */
#define PCRE_ERROR_BADUTF8      (-10) /* Same for 8/16 */
#define PCRE_ERROR_BADUTF16    (-10) /* Same for 8/16 */
#define PCRE_ERROR_BADUTF8_OFFSET (-11) /* Same for 8/16 */
#define PCRE_ERROR_BADUTF16_OFFSET (-11) /* Same for 8/16 */
#define PCRE_ERROR_PARTIAL      (-12)
#define PCRE_ERROR_BADPARTIAL   (-13)
#define PCRE_ERROR_INTERNAL     (-14)
#define PCRE_ERROR_BADCOUNT   (-15)
#define PCRE_ERROR_DFA_UITEM    (-16)
#define PCRE_ERROR_DFA_UCOND    (-17)
#define PCRE_ERROR_DFA_UMLIMIT (-18)
#define PCRE_ERROR_DFA_WSSIZE  (-19)
#define PCRE_ERROR_DFA_RECURSE (-20)
#define PCRE_ERROR_RECURSIONLIMIT (-21)
#define PCRE_ERROR_NULLWSLIMIT  (-22) /* No longer actually used */
#define PCRE_ERROR_BADNEWLINE   (-23)
#define PCRE_ERROR_BADOFFSET    (-24)
#define PCRE_ERROR_SHORTUTF8    (-25)
#define PCRE_ERROR_SHORTUTF16  (-25) /* Same for 8/16 */
#define PCRE_ERROR_RECURSELOOP (-26)
#define PCRE_ERROR_JIT_STACKLIMIT (-27)
#define PCRE_ERROR_BADMODE     (-28)
#define PCRE_ERROR_BADENDIANNESS (-29)
#define PCRE_ERROR_DFA_BADRESTART (-30)

```

/\* Specific error codes for UTF-8 validity checks \*/

```

#define PCRE_UTF8_ERR0      0
#define PCRE_UTF8_ERR1      1
#define PCRE_UTF8_ERR2      2
#define PCRE_UTF8_ERR3      3
#define PCRE_UTF8_ERR4      4
#define PCRE_UTF8_ERR5      5
#define PCRE_UTF8_ERR6      6
#define PCRE_UTF8_ERR7      7
#define PCRE_UTF8_ERR8      8
#define PCRE_UTF8_ERR9      9
#define PCRE_UTF8_ERR10     10
#define PCRE_UTF8_ERR11     11
#define PCRE_UTF8_ERR12     12
#define PCRE_UTF8_ERR13     13
#define PCRE_UTF8_ERR14     14
#define PCRE_UTF8_ERR15     15
#define PCRE_UTF8_ERR16     16
#define PCRE_UTF8_ERR17     17
#define PCRE_UTF8_ERR18     18
#define PCRE_UTF8_ERR19     19

```

```

#define PCRE_UTF8_ERR20      20
#define PCRE_UTF8_ERR21      21

/* Specific error codes for UTF-16 validity checks */

#define PCRE_UTF16_ERR0      0
#define PCRE_UTF16_ERR1      1
#define PCRE_UTF16_ERR2      2
#define PCRE_UTF16_ERR3      3
#define PCRE_UTF16_ERR4      4

/* Request types for pcre_fullinfo() */

#define PCRE_INFO_OPTIONS      0
#define PCRE_INFO_SIZE        1
#define PCRE_INFO_CAPTURECOUNT  2
#define PCRE_INFO_BACKREFMAX    3
#define PCRE_INFO_FIRSTBYTE     4
#define PCRE_INFO_FIRSTCHAR     4 /* For backwards compatibility */
#define PCRE_INFO_FIRSTTABLE    5
#define PCRE_INFO_LASTLITERAL   6
#define PCRE_INFO_NAMEENTRYSIZE 7
#define PCRE_INFO_NAMECOUNT    8
#define PCRE_INFO_NAMETABLE    9
#define PCRE_INFO_STUDYSIZE    10
#define PCRE_INFO_DEFAULT_TABLES 11
#define PCRE_INFO_OKPARTIAL    12
#define PCRE_INFO_JCHANGED     13
#define PCRE_INFO_HASCORLRF    14
#define PCRE_INFO_MINLENGTH    15
#define PCRE_INFO_JIT          16
#define PCRE_INFO_JITSIZE      17
#define PCRE_INFO_MAXLOOKBEHIND 18

/* Request types for pcre_config(). Do not re-arrange, in order to remain
compatible. */

#define PCRE_CONFIG_UTF8        0
#define PCRE_CONFIG_NEWLINE     1
#define PCRE_CONFIG_LINK_SIZE   2
#define PCRE_CONFIG_POSIX_MALLOC_THRESHOLD 3
#define PCRE_CONFIG_MATCH_LIMIT 4
#define PCRE_CONFIG_STACKRECURSE 5
#define PCRE_CONFIG_UNICODE_PROPERTIES 6
#define PCRE_CONFIG_MATCH_LIMIT_RECURSION 7
#define PCRE_CONFIG_BSR         8
#define PCRE_CONFIG_JIT         9
#define PCRE_CONFIG_UTF16      10

```

```

#define PCRE_CONFIG_JITTARGET      11

/* Request types for pcre_study(). Do not re-arrange, in order to remain
compatible. */

#define PCRE_STUDY_JIT_COMPILE      0x0001
#define PCRE_STUDY_JIT_PARTIAL_SOFT_COMPILE 0x0002
#define PCRE_STUDY_JIT_PARTIAL_HARD_COMPILE 0x0004

/* Bit flags for the pcre[16]_extra structure. Do not re-arrange or redefine
these bits, just add new ones on the end, in order to remain compatible. */

#define PCRE_EXTRA_STUDY_DATA      0x0001
#define PCRE_EXTRA_MATCH_LIMIT     0x0002
#define PCRE_EXTRA_CALLOUT_DATA    0x0004
#define PCRE_EXTRA_TABLES          0x0008
#define PCRE_EXTRA_MATCH_LIMIT_RECURSION 0x0010
#define PCRE_EXTRA_MARK            0x0020
#define PCRE_EXTRA_EXECUTABLE_JIT  0x0040

/* Types */

struct real_pcre;          /* declaration; the definition is private */
typedef struct real_pcre pcre;

struct real_pcre16;       /* declaration; the definition is private */
typedef struct real_pcre16 pcre16;

struct real_pcre_jit_stack; /* declaration; the definition is private */
typedef struct real_pcre_jit_stack pcre_jit_stack;

struct real_pcre16_jit_stack; /* declaration; the definition is private */
typedef struct real_pcre16_jit_stack pcre16_jit_stack;

/* If PCRE is compiled with 16 bit character support, PCRE_UCHAR16 must contain
a 16 bit wide signed data type. Otherwise it can be a dummy data type since
pcre16 functions are not implemented. There is a check for this in pcre_internal.h. */
#ifndef PCRE_UCHAR16
#define PCRE_UCHAR16 unsigned short
#endif

#ifndef PCRE_SPTR16
#define PCRE_SPTR16 const PCRE_UCHAR16 *
#endif

/* When PCRE is compiled as a C++ library, the subject pointer type can be
replaced with a custom type. For conventional use, the public interface is a
const char *. */

```

```

#ifndef PCRE_SPTR
#define PCRE_SPTR const char *
#endif

/* The structure for passing additional data to pcre_exec(). This is defined in
such a way as to be extensible. Always add new fields at the end, in order to
remain compatible. */

typedef struct pcre_extra {
    unsigned long int flags;      /* Bits for which fields are set */
    void *study_data;           /* Opaque data from pcre_study() */
    unsigned long int match_limit; /* Maximum number of calls to match() */
    void *callout_data;         /* Data passed back in callouts */
    const unsigned char *tables; /* Pointer to character tables */
    unsigned long int match_limit_recursion; /* Max recursive calls to match() */
    unsigned char **mark;        /* For passing back a mark pointer */
    void *executable_jit;        /* Contains a pointer to a compiled jit code */
} pcre_extra;

/* Same structure as above, but with 16 bit char pointers. */

typedef struct pcre16_extra {
    unsigned long int flags;      /* Bits for which fields are set */
    void *study_data;           /* Opaque data from pcre_study() */
    unsigned long int match_limit; /* Maximum number of calls to match() */
    void *callout_data;         /* Data passed back in callouts */
    const unsigned char *tables; /* Pointer to character tables */
    unsigned long int match_limit_recursion; /* Max recursive calls to match() */
    PCRE_UCHAR16 **mark;        /* For passing back a mark pointer */
    void *executable_jit;        /* Contains a pointer to a compiled jit code */
} pcre16_extra;

/* The structure for passing out data via the pcre_callout_function. We use a
structure so that new fields can be added on the end in future versions,
without changing the API of the function, thereby allowing old clients to work
without modification. */

typedef struct pcre_callout_block {
    int    version;      /* Identifies version of block */
    /* ----- Version 0 ----- */
    int    callout_number; /* Number compiled into pattern */
    int    *offset_vector; /* The offset vector */
    PCRE_SPTR subject;    /* The subject being matched */
    int    subject_length; /* The length of the subject */
    int    start_match;    /* Offset to start of this match attempt */
    int    current_position; /* Where we currently are in the subject */
    int    capture_top;    /* Max current capture */

```

```

int    capture_last; /* Most recently closed capture */
void   *callout_data; /* Data passed in with the call */
/* ----- Added for Version 1 ----- */
int    pattern_position; /* Offset to next item in the pattern */
int    next_item_length; /* Length of next item in the pattern */
/* ----- Added for Version 2 ----- */
const unsigned char *mark; /* Pointer to current mark or NULL */
/* ----- */
} pcre_callout_block;

/* Same structure as above, but with 16 bit char pointers. */

typedef struct pcre16_callout_block {
int    version; /* Identifies version of block */
/* ----- Version 0 ----- */
int    callout_number; /* Number compiled into pattern */
int    *offset_vector; /* The offset vector */
PCRE_SPTR16 subject; /* The subject being matched */
int    subject_length; /* The length of the subject */
int    start_match; /* Offset to start of this match attempt */
int    current_position; /* Where we currently are in the subject */
int    capture_top; /* Max current capture */
int    capture_last; /* Most recently closed capture */
void   *callout_data; /* Data passed in with the call */
/* ----- Added for Version 1 ----- */
int    pattern_position; /* Offset to next item in the pattern */
int    next_item_length; /* Length of next item in the pattern */
/* ----- Added for Version 2 ----- */
const PCRE_UCHAR16 *mark; /* Pointer to current mark or NULL */
/* ----- */
} pcre16_callout_block;

/* Indirection for store get and free functions. These can be set to
alternative malloc/free functions if required. Special ones are used in the
non-recursive case for "frames". There is also an optional callout function
that is triggered by the (?) regex item. For Virtual Pascal, these definitions
have to take another form. */

#ifdef VPCOMPAT
PCRE_EXP_DECL void (*pcre_malloc)(size_t);
PCRE_EXP_DECL void (*pcre_free)(void *);
PCRE_EXP_DECL void (*pcre_stack_malloc)(size_t);
PCRE_EXP_DECL void (*pcre_stack_free)(void *);
PCRE_EXP_DECL int (*pcre_callout)(pcre_callout_block *);

PCRE_EXP_DECL void (*pcre16_malloc)(size_t);
PCRE_EXP_DECL void (*pcre16_free)(void *);
PCRE_EXP_DECL void (*pcre16_stack_malloc)(size_t);

```

```

PCRE_EXP_DECL void (*pcre16_stack_free)(void *);
PCRE_EXP_DECL int (*pcre16_callout)(pcre16_callout_block *);
#else /* VPCOMPAT */
PCRE_EXP_DECL void *pcre_malloc(size_t);
PCRE_EXP_DECL void pcre_free(void *);
PCRE_EXP_DECL void *pcre_stack_malloc(size_t);
PCRE_EXP_DECL void pcre_stack_free(void *);
PCRE_EXP_DECL int pcre_callout(pcre_callout_block *);

PCRE_EXP_DECL void *pcre16_malloc(size_t);
PCRE_EXP_DECL void pcre16_free(void *);
PCRE_EXP_DECL void *pcre16_stack_malloc(size_t);
PCRE_EXP_DECL void pcre16_stack_free(void *);
PCRE_EXP_DECL int pcre16_callout(pcre16_callout_block *);
#endif /* VPCOMPAT */

/* User defined callback which provides a stack just before the match starts. */

typedef pcre_jit_stack *(*pcre_jit_callback)(void *);
typedef pcre16_jit_stack *(*pcre16_jit_callback)(void *);

/* Exported PCRE functions */

PCRE_EXP_DECL pcre *pcre_compile(const char *, int, const char **, int *,
    const unsigned char *);
PCRE_EXP_DECL pcre16 *pcre16_compile(PCRE_SPTR16, int, const char **, int *,
    const unsigned char *);
PCRE_EXP_DECL pcre *pcre_compile2(const char *, int, int *, const char **,
    int *, const unsigned char *);
PCRE_EXP_DECL pcre16 *pcre16_compile2(PCRE_SPTR16, int, int *, const char **,
    int *, const unsigned char *);
PCRE_EXP_DECL int pcre_config(int, void *);
PCRE_EXP_DECL int pcre16_config(int, void *);
PCRE_EXP_DECL int pcre_copy_named_substring(const pcre *, const char *,
    int *, int, const char *, char *, int);
PCRE_EXP_DECL int pcre16_copy_named_substring(const pcre16 *, PCRE_SPTR16,
    int *, int, PCRE_SPTR16, PCRE_UCHAR16 *, int);
PCRE_EXP_DECL int pcre_copy_substring(const char *, int *, int, int,
    char *, int);
PCRE_EXP_DECL int pcre16_copy_substring(PCRE_SPTR16, int *, int, int,
    PCRE_UCHAR16 *, int);
PCRE_EXP_DECL int pcre_dfa_exec(const pcre *, const pcre_extra *,
    const char *, int, int, int, int *, int *, int *, int);
PCRE_EXP_DECL int pcre16_dfa_exec(const pcre16 *, const pcre16_extra *,
    PCRE_SPTR16, int, int, int, int *, int *, int *, int);
PCRE_EXP_DECL int pcre_exec(const pcre *, const pcre_extra *, PCRE_SPTR,
    int, int, int, int *, int);
PCRE_EXP_DECL int pcre16_exec(const pcre16 *, const pcre16_extra *,

```

```

    PCRE_SPTR16, int, int, int, int *, int);
PCRE_EXP_DECL void pcre_free_substring(const char *);
PCRE_EXP_DECL void pcre16_free_substring(PCRE_SPTR16);
PCRE_EXP_DECL void pcre_free_substring_list(const char **);
PCRE_EXP_DECL void pcre16_free_substring_list(PCRE_SPTR16 *);
PCRE_EXP_DECL int pcre_fullinfo(const pcre *, const pcre_extra *, int,
    void *);
PCRE_EXP_DECL int pcre16_fullinfo(const pcre16 *, const pcre16_extra *, int,
    void *);
PCRE_EXP_DECL int pcre_get_named_substring(const pcre *, const char *,
    int *, int, const char *, const char **);
PCRE_EXP_DECL int pcre16_get_named_substring(const pcre16 *, PCRE_SPTR16,
    int *, int, PCRE_SPTR16, PCRE_SPTR16 *);
PCRE_EXP_DECL int pcre_get_stringnumber(const pcre *, const char *);
PCRE_EXP_DECL int pcre16_get_stringnumber(const pcre16 *, PCRE_SPTR16);
PCRE_EXP_DECL int pcre_get_stringtable_entries(const pcre *, const char *,
    char **, char **);
PCRE_EXP_DECL int pcre16_get_stringtable_entries(const pcre16 *, PCRE_SPTR16,
    PCRE_UCHAR16 **, PCRE_UCHAR16 **);
PCRE_EXP_DECL int pcre_get_substring(const char *, int *, int, int,
    const char **);
PCRE_EXP_DECL int pcre16_get_substring(PCRE_SPTR16, int *, int, int,
    PCRE_SPTR16 *);
PCRE_EXP_DECL int pcre_get_substring_list(const char *, int *, int,
    const char ***);
PCRE_EXP_DECL int pcre16_get_substring_list(PCRE_SPTR16, int *, int,
    PCRE_SPTR16 **);
PCRE_EXP_DECL const unsigned char *pcre_maketables(void);
PCRE_EXP_DECL const unsigned char *pcre16_maketables(void);
PCRE_EXP_DECL int pcre_refcount(pcre *, int);
PCRE_EXP_DECL int pcre16_refcount(pcre16 *, int);
PCRE_EXP_DECL pcre_extra *pcre_study(const pcre *, int, const char **);
PCRE_EXP_DECL pcre16_extra *pcre16_study(const pcre16 *, int, const char **);
PCRE_EXP_DECL void pcre_free_study(pcre_extra *);
PCRE_EXP_DECL void pcre16_free_study(pcre16_extra *);
PCRE_EXP_DECL const char *pcre_version(void);
PCRE_EXP_DECL const char *pcre16_version(void);

/* Utility functions for byte order swaps. */
PCRE_EXP_DECL int pcre_pattern_to_host_byte_order(pcre *, pcre_extra *,
    const unsigned char *);
PCRE_EXP_DECL int pcre16_pattern_to_host_byte_order(pcre16 *, pcre16_extra *,
    const unsigned char *);
PCRE_EXP_DECL int pcre16_utf16_to_host_byte_order(PCRE_UCHAR16 *,
    PCRE_SPTR16, int, int *, int);

/* JIT compiler related functions. */

```

```

PCRE_EXP_DECL pcre_jit_stack *pcre_jit_stack_alloc(int, int);
PCRE_EXP_DECL pcre16_jit_stack *pcre16_jit_stack_alloc(int, int);
PCRE_EXP_DECL void pcre_jit_stack_free(pcre_jit_stack *);
PCRE_EXP_DECL void pcre16_jit_stack_free(pcre16_jit_stack *);
PCRE_EXP_DECL void pcre_assign_jit_stack(pcre_extra *,
    pcre_jit_callback, void *);
PCRE_EXP_DECL void pcre16_assign_jit_stack(pcre16_extra *,
    pcre16_jit_callback, void *);

#ifdef __cplusplus
} /* extern "C" */
#endif

#endif /* End of pcre.h */
/* GLIB - Library of useful routines for C programming
* Copyright (C) 1995-1997 Peter Mattis, Spencer Kimball and Josh MacDonald
*
* This library is free software; you can redistribute it and/or
* modify it under the terms of the GNU Lesser General Public
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*
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* Lesser General Public License for more details.
*
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* License along with this library; if not, see <http://www.gnu.org/licenses/>.
*/

/*
* Modified by the GLib Team and others 1997-2000. See the AUTHORS
* file for a list of people on the GLib Team. See the ChangeLog
* files for a list of changes. These files are distributed with
* GLib at ftp://ftp.gtk.org/pub/gtk/.
*/

#ifndef __G_LIB_H__
#define __G_LIB_H__

#define __GLIB_H_INSIDE__

#include <glib/galloca.h>
#include <glib/garray.h>
#include <glib/gasyncqueue.h>
#include <glib/gatomic.h>
#include <glib/gbacktrace.h>

```

```
#include <glib/gbase64.h>
#include <glib/gbitlock.h>
#include <glib/gbookmarkfile.h>
#include <glib/gbytes.h>
#include <glib/gcharset.h>
#include <glib/gchecksum.h>
#include <glib/gconvert.h>
#include <glib/gdataset.h>
#include <glib/gdate.h>
#include <glib/gdatetime.h>
#include <glib/gdir.h>
#include <glib/genviron.h>
#include <glib/gerror.h>
#include <glib/gfileutils.h>
#include <glib/ggettext.h>
#include <glib/ghash.h>
#include <glib/ghmac.h>
#include <glib/ghook.h>
#include <glib/ghostutils.h>
#include <glib/giochannel.h>
#include <glib/gkeyfile.h>
#include <glib/glist.h>
#include <glib/gmacros.h>
#include <glib/gmain.h>
#include <glib/gmappedfile.h>
#include <glib/gmarkup.h>
#include <glib/gmem.h>
#include <glib/gmessages.h>
#include <glib/gnode.h>
#include <glib/goption.h>
#include <glib/gpattern.h>
#include <glib/gpoll.h>
#include <glib/gprimes.h>
#include <glib/gqsort.h>
#include <glib/gquark.h>
#include <glib/gqueue.h>
#include <glib/grand.h>
#include <glib/gregex.h>
#include <glib/gscanner.h>
#include <glib/gsequence.h>
#include <glib/gshell.h>
#include <glib/gslice.h>
#include <glib/gslist.h>
#include <glib/gspawn.h>
#include <glib/gstrfuncs.h>
#include <glib/gstring.h>
#include <glib/gstringchunk.h>
#include <glib/gtestutils.h>
```

```

#include <glib/gthread.h>
#include <glib/gthreadpool.h>
#include <glib/gtimer.h>
#include <glib/gtimezone.h>
#include <glib/gtrashstack.h>
#include <glib/gtree.h>
#include <glib/gtypes.h>
#include <glib/gunicode.h>
#include <glib/gurifuncs.h>
#include <glib/gutils.h>
#include <glib/gvarianttype.h>
#include <glib/gvariant.h>
#include <glib/gversion.h>
#include <glib/gversionmacros.h>
#ifdef G_PLATFORM_WIN32
#include <glib/gwin32.h>
#endif

#ifndef G_DISABLE_DEPRECATED
#include <glib/deprecated/gallocator.h>
#include <glib/deprecated/gcache.h>
#include <glib/deprecated/gcompletion.h>
#include <glib/deprecated/gmain.h>
#include <glib/deprecated/grel.h>
#include <glib/deprecated/gthread.h>
#endif /* G_DISABLE_DEPRECATED */

#include <glib/glib-autocleanups.h>

#undef __GLIB_H_INSIDE__

#endif /* __G_LIB_H__ */

```

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## 1.396 javassist 3.22.0-CR2

### 1.396.1 Available under license :

No license file was found, but licenses were detected in source scan.

/\*

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\*/

Found in path(s):

\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/compiler/NoFieldException.java

\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/InnerClassesAttribute.java

\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/analysis/Subroutine.java

\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/runtime/Cflow.java

\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/annotation/AnnotationImpl.java

\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/util/proxy/FactoryHelper.java

\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/convert/Transformer.java

\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-

jar/javassist/bytecode/SyntheticAttribute.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/tools/reflect/Reflection.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/expr/Instanceof.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/util/proxy/SecurityActions.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/bytecode/ByteStream.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/compiler/SyntaxError.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/tools/reflect/CannotReflectException.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/convert/TransformCall.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/CtNewWrappedMethod.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/bytecode/ClassFilePrinter.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/compiler/ast/AssignExpr.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/CtArray.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/CtPrimitiveType.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/compiler/ast/NewExpr.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/bytecode/DeprecatedAttribute.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/compiler/MemberResolver.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/compiler/ast/MethodDecl.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/bytecode/stackmap/Tracer.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/bytecode/stackmap/BasicBlock.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/bytecode/CodeAnalyzer.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/compiler/JvstCodeGen.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/bytecode/StackMap.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/bytecode/LocalVariableAttribute.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/compiler/CodeGen.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/bytecode/analysis/MultiType.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/compiler/ast/Declarator.java

\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/util/proxy/ProxyObject.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/analysis/Util.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/EnclosingMethodAttribute.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/compiler/ast/DoubleConst.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/tools/Dump.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/analysis/ControlFlow.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/CtMethod.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/LineNumberAttribute.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/CodeIterator.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/scopedpool/ScopedClassPoolRepository.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/compiler/ast/Stmnt.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/annotation/NoSuchClassError.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/stackmap/TypedBlock.java  
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\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/convert/TransformNewClass.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/compiler/ast/Visitor.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/CtMember.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/ByteArray.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/tools/framedump.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/expr/Cast.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/util/proxy/ProxyFactory.java  
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\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/ClassClassPath.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/expr/MethodCall.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/ClassFileWriter.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/CannotCompileException.java  
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\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/stackmap/TypeData.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/CtNewClass.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/tools/Callback.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/Modifier.java  
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\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/runtime/Inner.java  
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\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/tools/rmi/ObjectNotFoundException.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/compiler/TypeChecker.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/CtClass.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/scopedpool/SoftValueHashMap.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/util/proxy/ProxyObjectInputStream.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/NotFoundException.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/CtField.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/Opcode.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/Translator.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/compiler/ast/Pair.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/tools/reflect/Sample.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/util/proxy/MethodHandler.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/CtConstructor.java  
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\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/tools/rmi/Sample.java  
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\*

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include/GL/glex.h

include/GL/glxext.h

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```
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```

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(This file is under construction.) -\*- text -\*-

If you've contributed to gas and your name isn't listed here, it is not meant as a slight. I just don't know about it. Email me, nickc@redhat.com and I'll correct the situation.

This file will eventually be deleted: The general info will go into the documentation, and info on specific files will go into an AUTHORS file, as requested by the FSF.

+++++

Dean Elsner wrote the original gas for vax. [more details?]

Jay Fenlason maintained gas for a while, adding support for gdb-specific debug information and the 68k series machines, most of the preprocessing pass, and extensive changes in messages.c, input-file.c, write.c.

K. Richard Pixley maintained gas for a while, adding various enhancements and many bug fixes, including merging support for several processors, breaking gas up to handle multiple object file format backends (including heavy rewrite, testing, an integration of the coff and b.out backends), adding configuration including heavy testing and verification of cross assemblers and file splits and renaming, converted gas to strictly ansi C including full prototypes, added support for m680[34]0 & cpu32, considerable work on i960 including a coff port (including considerable amounts of reverse engineering), a sparc opcode file rewrite, decstation, rs6000, and hp300hpux host ports, updated "know" assertions and made them work, much other reorganization, cleanup, and lint.

Ken Raeburn wrote the high-level BFD interface code to replace most of the code in format-specific I/O modules.

The original Vax-VMS support was contributed by David L. Kashtan. Eric Youngdale and Pat Rankin have done much work with it since.

The Intel 80386 machine description was written by Eliot Dresselhaus.

Minh Tran-Le at IntelliCorp contributed some AIX 386 support.

The Motorola 88k machine description was contributed by Devon Bowen of Buffalo University and Torbjorn Granlund of the Swedish Institute of Computer Science.

Keith Knowles at the Open Software Foundation wrote the original MIPS back end (tc-mips.c, tc-mips.h), and contributed Rose format support

that hasn't been merged in yet. Ralph Campbell worked with the MIPS code to support a.out format.

Support for the Zilog Z8k and Hitachi H8/300, H8/500 and SH processors (tc-z8k, tc-h8300, tc-h8500, tc-sh), and IEEE 695 object file format (obj-ieee), was written by Steve Chamberlain of Cygnus Solutions. Steve also modified the COFF back end (obj-coffbfd) to use BFD for some low-level operations, for use with the Hitachi, 29k and Zilog targets.

John Gilmore built the AMD 29000 support, added .include support, and simplified the configuration of which versions accept which pseudo-ops. He updated the 68k machine description so that Motorola's opcodes always produced fixed-size instructions (e.g. jsr), while synthetic instructions remained shrinkable (jbsr). John fixed many bugs, including true tested cross-compilation support, and one bug in relaxation that took a week and required the proverbial one-bit fix.

Ian Lance Taylor of Cygnus Solutions merged the Motorola and MIT syntaxes for the 68k, completed support for some COFF targets (68k, i386 SVR3, and SCO Unix), wrote the ECOFF support based on Michael Meissner's mips-tfile program, wrote the PowerPC and RS/6000 support, and made a few other minor patches. He handled the binutils releases for versions 2.7 through 2.9.

David Edelsohn contributed fixes for the PowerPC and AIX support.

Steve Chamberlain made gas able to generate listings.

Support for the HP9000/300 was contributed by Glenn Engel of HP.

Support for ELF format files has been worked on by Mark Eichin of Cygnus Solutions (original, incomplete implementation), Pete Hoogenboom at the University of Utah (HPPA mainly), Michael Meissner of the Open Software Foundation (i386 mainly), and Ken Raeburn of Cygnus Solutions (sparc, initial 64-bit support).

Several engineers at Cygnus Solutions have also provided many small bug fixes and configuration enhancements.

The initial Alpha support was contributed by Carnegie-Mellon University. Additional work was done by Ken Raeburn of Cygnus Solutions. Richard Henderson then rewrote much of the Alpha support.

Ian Dall updated the support code for the National Semiconductor 32000 series, and added support for Mach 3 and NetBSD running on the PC532.

Klaus Kaempf ported the assembler and the binutils to openVMS/Alpha.

Steve Haworth contributed the support for the Texas Instruction c30 (tms320c30).

H.J. Lu has contributed many patches and much testing.

Alan Modra reworked much of the i386 backend, improving the error checking, updating the code, and improving the 16 bit support, using patches from the work of Martynas Kunigelis and H.J. Lu.

Many others have contributed large or small bugfixes and enhancements. If you've contributed significant work and are not mentioned on this list, and want to be, let us know. Some of the history has been lost; we aren't intentionally leaving anyone out.

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## 1.405 cJSON 1.7.14

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- \* [Jiri Zouhar](https://github.com/loigu)
- \* [Jonathan Fether](https://github.com/jfether)
- \* [Julian Ste](https://github.com/julian-st)
- \* [Julin Vsquez](https://github.com/juvasquezg)
- \* [Kevin Branigan](https://github.com/kbranigan)
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- \* [Kyle Chisholm](https://github.com/ChisholmKyle)
- \* [Linus Wallgren](https://github.com/ecksun)
- \* [Mateusz Szafoni](https://github.com/raiden00pl)
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- \* [Mike Jerris](https://github.com/mjerris)
- \* [Mike Robinson](https://github.com/mhrobinson)
- \* [Moorthy](https://github.com/moorthy-bs)
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# 1.406 goleveldb 1.0.0

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## 1.407 webencodings 0.5.1

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Found in path(s):

```
* /opt/cola/permits/1401468445_1661617535.3616152/0/webencodings-0-5-1-1-tar-gz/webencodings-0.5.1/webencodings/labels.py
* /opt/cola/permits/1401468445_1661617535.3616152/0/webencodings-0-5-1-1-tar-gz/webencodings-0.5.1/webencodings/__init__.py
* /opt/cola/permits/1401468445_1661617535.3616152/0/webencodings-0-5-1-1-tar-gz/webencodings-0.5.1/webencodings/tests.py
* /opt/cola/permits/1401468445_1661617535.3616152/0/webencodings-0-5-1-1-tar-gz/webencodings-0.5.1/webencodings/x_user_defined.py
```

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```
from setuptools import setup, find_packages
import io
from os import path
import re
```

```
VERSION = re.search("VERSION = '([^\s]+)'", io.open(
    path.join(path.dirname(__file__), 'webencodings', '__init__.py'),
    encoding='utf-8'
).read().strip()).group(1)
```

```
LONG_DESCRIPTION = io.open(
    path.join(path.dirname(__file__), 'README.rst'),
    encoding='utf-8'
).read()
```

```
setup(
    name='webencodings',
    version=VERSION,
    url='https://github.com/SimonSapin/python-webencodings',
```

```

license='BSD',
author='Simon Sapin',
author_email='simon.sapin@exyr.org',
maintainer='Geoffrey Sneddon',
maintainer_email='me@gsnedders.com',
description='Character encoding aliases for legacy web content',
long_description=LONG_DESCRIPTION,
classifiers=[
    'Development Status :: 4 - Beta',
    'Intended Audience :: Developers',
    'License :: OSI Approved :: BSD License',
    'Programming Language :: Python',
    'Programming Language :: Python :: 2',
    'Programming Language :: Python :: 2.6',
    'Programming Language :: Python :: 2.7',
    'Programming Language :: Python :: 3',
    'Programming Language :: Python :: 3.3',
    'Programming Language :: Python :: 3.4',
    'Programming Language :: Python :: 3.5',
    'Programming Language :: Python :: 3.6',
    'Programming Language :: Python :: Implementation :: CPython',
    'Programming Language :: Python :: Implementation :: PyPy',
    'Topic :: Internet :: WWW/HTTP',
],
packages=find_packages(),
)

```

Found in path(s):

```
* /opt/cola/permits/1401468445_1661617535.3616152/0/webencodings-0-5-1-1-tar-gz/webencodings-0.5.1/setup.py
```

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Metadata-Version: 1.1

Name: webencodings

Version: 0.5.1

Summary: Character encoding aliases for legacy web content

Home-page: <https://github.com/SimonSapin/python-webencodings>

Author: Geoffrey Sneddon

Author-email: [me@gsnedders.com](mailto:me@gsnedders.com)

License: BSD

Description: python-webencodings

=====

This is a Python implementation of the `WHATWG Encoding standard`  
 <<http://encoding.spec.whatwg.org/>>` \_.

\* Latest documentation: <http://packages.python.org/webencodings/>

\* Source code and issue tracker:

<https://github.com/gsnedders/python-webencodings>  
\* PyPI releases: <http://pypi.python.org/pypi/webencodings>  
\* License: BSD  
\* Python 2.6+ and 3.3+

In order to be compatible with legacy web content when interpreting something like `Content-Type: text/html; charset=latin1``, tools need to use a particular set of aliases for encoding labels as well as some overriding rules. For example, `US-ASCII`` and `iso-8859-1`` on the web are actually aliases for `windows-1252``, and an UTF-8 or UTF-16 BOM takes precedence over any other encoding declaration. The Encoding standard defines all such details so that implementations do not have to reverse-engineer each other.

This module has encoding labels and BOM detection, but the actual implementation for encoders and decoders is Python's.

Platform: UNKNOWN  
Classifier: Development Status :: 4 - Beta  
Classifier: Intended Audience :: Developers  
Classifier: License :: OSI Approved :: BSD License  
Classifier: Programming Language :: Python  
Classifier: Programming Language :: Python :: 2  
Classifier: Programming Language :: Python :: 2.6  
Classifier: Programming Language :: Python :: 2.7  
Classifier: Programming Language :: Python :: 3  
Classifier: Programming Language :: Python :: 3.3  
Classifier: Programming Language :: Python :: 3.4  
Classifier: Programming Language :: Python :: 3.5  
Classifier: Programming Language :: Python :: 3.6  
Classifier: Programming Language :: Python :: Implementation :: CPython  
Classifier: Programming Language :: Python :: Implementation :: PyPy  
Classifier: Topic :: Internet :: WWW/HTTP

Found in path(s):

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\* /opt/cola/permits/1401468445\_1661617535.3616152/0/webencodings-0-5-1-1-tar-gz/webencodings-0.5.1/PKG-INFO

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\*\*\*\*

webencodings.mklables

~~~~~

Regenerate the webencodings.labels module.

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"""

```
import json
```

```
try:
```

```
    from urllib import urlopen
```

```
except ImportError:
```

```
    from urllib.request import urlopen
```

```
def assert_lower(string):
```

```
    assert string == string.lower()
```

```
    return string
```

```
def generate(url):
```

```
    parts = []\
```

```
    """
```

```
    webencodings.labels
```

```
    ~~~~~
```

```
    Map encoding labels to their name.
```

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"""

```
# XXX Do not edit!
```

```
# This file is automatically generated by mklabels.py
```

```
LABELS = {
```

```
    ""
```

```
    labels = [
```

```
        (repr(assert_lower(label)).rstrip('u'),
```

```
         repr(encoding['name']).rstrip('u'))
```

```
        for category in json.loads(urlopen(url).read().decode('ascii'))
```

```
        for encoding in category['encodings']
```

```
        for label in encoding['labels']]
```

```
    max_len = max(len(label) for label, name in labels)
```

```
    parts.extend(
```

```
        ' %s:%s %s,\n' % (label, ' ' * (max_len - len(label)), name)
```

```
        for label, name in labels)
```

```
    parts.append('}')
```

```
return ".join(parts)
```

```
if __name__ == '__main__':  
    print(generate('http://encoding.spec.whatwg.org/encodings.json'))
```

Found in path(s):

* /opt/cola/permits/1401468445_1661617535.3616152/0/webencodings-0-5-1-1-tar-gz/webencodings-0.5.1/webencodings/mklabels.py

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python-webencodings

=====

This is a Python implementation of the `WHATWG Encoding standard

<<http://encoding.spec.whatwg.org/>>`_.

* Latest documentation: <http://packages.python.org/webencodings/>

* Source code and issue tracker:

<https://github.com/gsnedders/python-webencodings>

* PyPI releases: <http://pypi.python.org/pypi/webencodings>

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* Python 2.6+ and 3.3+

In order to be compatible with legacy web content

when interpreting something like ``Content-Type: text/html; charset=latin1``,

tools need to use a particular set of aliases for encoding labels

as well as some overriding rules.

For example, ``US-ASCII`` and ``iso-8859-1`` on the web are actually

aliases for ``windows-1252``, and an UTF-8 or UTF-16 BOM takes precedence

over any other encoding declaration.

The Encoding standard defines all such details so that implementations do

not have to reverse-engineer each other.

This module has encoding labels and BOM detection,

but the actual implementation for encoders and decoders is Python's.

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* /opt/cola/permits/1401468445_1661617535.3616152/0/webencodings-0-5-1-1-tar-gz/webencodings-0.5.1/README.rst

1.408 jakarta-annotation-api 1.3.4

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1.411 zlib 1.2.12

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* zlib.h -- interface of the 'zlib' general purpose compression library
version 1.2.12, March 11th, 2022

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Jean-loup Gailly Mark Adler
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The data format used by the zlib library is described by RFCs (Request for Comments) 1950 to 1952 in the files <http://tools.ietf.org/html/rfc1950> (zlib format), rfc1951 (deflate format) and rfc1952 (gzip format).

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1.412 guava 18.0

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1.413 cryptography 3.3.2

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1.416 pkix-ssh 12.5.1

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* @version 3.0 (December 2000)

*

* Optimised ANSI C code for the Rijndael cipher (now AES)

*

* @author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>

* @author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>

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1.417 gnutls 3.6.15

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Constant-time SSSE3 AES core implementation.

version 0.1

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By Mike Hamburg (Stanford University), 2009

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1.420 six 1.16.0

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1.421 python 2.4.3R

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations, which became Zope Corporation. In 2001, the Python Software Foundation (PSF, see <https://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation was a sponsoring member of the PSF.

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| Release | Derived from | Year | Owner | GPL-compatible? (1) |
|----------------|--------------|-----------|-------|---------------------|
| 0.9.0 thru 1.2 | | 1991-1995 | CWI | yes |
| 1.3 thru 1.5.2 | 1.2 | 1995-1999 | CNRI | yes |
| 1.6 | 1.5.2 | 2000 | CNRI | no |

| | | | | |
|---------------|-----------|----------|------------|---------|
| 2.0 | 1.6 | 2000 | BeOpen.com | no |
| 1.6.1 | 1.6 | 2001 | CNRI | yes (2) |
| 2.1 | 2.0+1.6.1 | 2001 | PSF | no |
| 2.0.1 | 2.0+1.6.1 | 2001 | PSF | yes |
| 2.1.1 | 2.1+2.0.1 | 2001 | PSF | yes |
| 2.1.2 | 2.1.1 | 2002 | PSF | yes |
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| 2.2 and above | 2.1.1 | 2001-now | PSF | yes |

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1.422 tcp-wrappers 7.6.24

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1.423 tomcat-coyote 8.5.32

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```
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/bcel/Const.java
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 */
/**
 * Return the initialization parameters for the TrustManager.
```

```

* Currently, only the default <code>PKIX</code> is supported.
*
* @param crlf The path to the CRL file.
* @param trustStore The configured TrustStore.
* @param revocationEnabled Should the JSSE provider perform revocation
*       checks? Ignored if { @code crlf } is non-null.
*       Configuration of revocation checks are expected
*       to be via proprietary JSSE provider methods.
* @return The parameters including the CRLs and TrustStore.
* @throws Exception An error occurred
*/

```

Found in path(s):

```

* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/net/jsse/JSSEUtil.java

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jar/org/apache/tomcat/util/http/parser/LocalStrings.properties
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/http11/filters/LocalStrings.properties
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/net/openssl/ciphers/LocalStrings.properties
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/http/res/LocalStrings.properties
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/http11/upgrade/LocalStrings.properties
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/http2/LocalStrings.properties
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/http/LocalStrings.properties
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/ajp/LocalStrings.properties
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/net/openssl/LocalStrings.properties
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/LocalStrings.properties
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/net/LocalStrings.properties
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/net/jsse/LocalStrings.properties

```

* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http11/LocalStrings.properties

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* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/bcel/classfile/EnumElementValue.java

* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/bcel/classfile/JavaClass.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/bcel/classfile/ClassElementValue.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/bcel/classfile/ClassFormatException.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/bcel/classfile/Annotations.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/bcel/classfile/AnnotationElementValue.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/bcel/classfile/ConstantInteger.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/bcel/classfile/ElementValuePair.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/bcel/classfile/Constant.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/bcel/classfile/ConstantDouble.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/bcel/classfile/AnnotationEntry.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/bcel/classfile/SimpleElementValue.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/bcel/classfile/ConstantClass.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/bcel/classfile/ClassParser.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/bcel/classfile/ConstantFloat.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/bcel/classfile/ArrayElementValue.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/bcel/classfile/ConstantPool.java

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* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/AsyncStateMachine.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/SendfileKeepAliveState.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/AprEndpoint.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/SSLUtil.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/ajp/Constants.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http2/HPackHuffman.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/jsse/JSSEKeyManager.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/ajp/AjpNio2Protocol.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/SendfileDataBase.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http11/Http11NioProtocol.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http2/Http2Exception.java
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* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/openssl/OpenSSLConfCmd.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/openssl/ciphers/Cipher.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http11/AbstractHttp11JsseProtocol.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http11/OutputFilter.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http11/filters/VoidInputFilter.java
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* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/bcel/classfile/ElementValue.java

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* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http2/AbstractStream.java

* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http11/AbstractHttp11Protocol.java

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* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/openssl/OpenSSLX509Certificate.java

* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/util/package-info.java

* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/util/Closeable.java

* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/parser/Cookie.java

* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/openssl/OpenSSLSessionContext.java

* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/openssl/OpenSSLKeyManager.java

* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/parser/Authorization.java

* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/FileUploadBase.java

* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/UploadContext.java

* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/util/LimitedInputStream.java

* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/FileItemHeaders.java

* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/modeler/BaseAttributeFilter.java

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* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/openssl/OpenSSLContext.java

* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/disk/DiskFileItemFactory.java

* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/log/UserDataHelper.java

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* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/FileItemFactory.java
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* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/Nio2Channel.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/package-info.java
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* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/MultipartStream.java
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* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/FileItemStream.java
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1.424 libxml2 2.9.2

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```
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```

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```
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This is free software, and you are welcome to redistribute it  
under certain conditions; type `show c' for details.
```

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```
Yoyodyne, Inc., hereby disclaims all copyright interest in the program  
`Gnomovision' (which makes passes at compilers) written by James Hacker.
```

```
<signature of Ty Coon>, 1 April 1989  
Ty Coon, President of Vice
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The End

1.426 gorilla 1.7.4

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bmatsuo

shawnps

theory

jboverfelt

dsymonds

cd1

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Protocol Buffers for Go with Gadgets

Go support for Protocol Buffers - Google's data interchange format

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#

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```
#
# git shortlog -se | awk '{print $2 " " $3 " " $4}'
#
# and do appropriate cleanups.
```

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1.427 hk2-utils 2.5.0

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