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In your requests please include the following reference number 78EE117C99-1014782293

Contents

1.1 expat 2.2.4

1.1.1 Available under license

1.2 libevent 2.0.21

1.2.1 Available under license

1.3 tomcat-servlet-api 8.5.32

1.3.1 Available under license

1.4 jackson-jaxrs-json-provider 2.9.5

1.4.1 Available under license

1.5 jackson-databind 2.9.5

1.5.1 Available under license

1.6 javax-annotation-api 1.2

1.6.1 Available under license

1.7 libxml 2.9.7

1.7.1 Available under license

1.8 open-ldap 2.3.43

1.8.1 Available under license

1.9 expat 2.1.0

1.9.1 Available under license

1.10 expat 2.0.0

1.10.1 Available under license

1.11 jersey-guava 2.22

1.12 trove4j 2.1.0

1.12.1 Available under license

1.13 lua 5.3.4

1.14 bcel 2.7.2

1.14.1 Available under license

- 1.15 jansson 2.5**
 - 1.15.1 Available under license
- 1.16 mitchellh-mapstructure 1.1.2**
 - 1.16.1 Available under license
- 1.17 icu 1.6.0**
 - 1.17.1 Available under license
- 1.18 commons-codec 1.9**
 - 1.18.1 Available under license
- 1.19 jersey-media-jaxb 2.22**
- 1.20 libtool 2.4.6**
 - 1.20.1 Available under license
- 1.21 giflib 5.1.4**
 - 1.21.1 Available under license
- 1.22 jetty-io 9.2.13.v20150730**
- 1.23 lua-cjson 2.1.0**
 - 1.23.1 Available under license
- 1.24 tomcat-jsp-api 8.5.32**
- 1.25 jettison 1.3**
 - 1.25.1 Available under license
- 1.26 binutils 2.28**
 - 1.26.1 Available under license
- 1.27 apache-httpclient 4.3.2**
 - 1.27.1 Available under license
- 1.28 smartmontools 6.5_2**
 - 1.28.1 Available under license
- 1.29 jvm-hotspot-oracle 1.8.0u131**
 - 1.29.1 Available under license
- 1.30 json-smart 2.2.1**
- 1.31 jopt-simple 4.9**
- 1.32 zlib 1.2.8**
 - 1.32.1 Available under license
- 1.33 zlib 1.0.4**
 - 1.33.1 Available under license
- 1.34 tomcat 8.5.32**
 - 1.34.1 Available under license
- 1.35 eclipse-compiler 3.12.3.v20170228-1205**
 - 1.35.1 Available under license
- 1.36 errors 1.12**
 - 1.36.1 Available under license

- 1.37 cvs 1.11.22**
 - 1.37.1 Available under license
- 1.38 ASM 5.0.3**
 - 1.38.1 Available under license
- 1.39 guava 18.0**
 - 1.39.1 Available under license
- 1.40 jwalterweatherman 1.1.0**
 - 1.40.1 Available under license
- 1.41 activation 1.1**
 - 1.41.1 Available under license
- 1.42 sqlite 3.20.0**
 - 1.42.1 Available under license
- 1.43 linenoise 1.0**
 - 1.43.1 Available under license
- 1.44 logrus 1.4.1**
 - 1.44.1 Available under license
- 1.45 hiredis 0.13.3**
 - 1.45.1 Available under license
- 1.46 pcre 8.35**
 - 1.46.1 Available under license
- 1.47 telnetd FreeBSD-10.4**
 - 1.47.1 Available under license
- 1.48 postgres-sql 7.4.2**
 - 1.48.1 Available under license
- 1.49 libdouble-conversion 1.1.1**
 - 1.49.1 Available under license
- 1.50 javax-ws-rs-api 2.0.1**
 - 1.50.1 Available under license
- 1.51 jackson-annotations 2.6.1**
 - 1.51.1 Available under license
- 1.52 zlib 1.2.11**
 - 1.52.1 Available under license
- 1.53 wiremock-standalone 2.5.1**
- 1.54 protobuf-java 2.2.0**
- 1.55 jetty-http 9.2.1.v20140609**
 - 1.55.1 Available under license
- 1.56 curl 7.55.1**
 - 1.56.1 Available under license
- 1.57 bouncy-castle 1.51**

- 1.58 tinycdb 0.76**
 - 1.58.1 Available under license
- 1.59 less 481**
 - 1.59.1 Available under license
- 1.60 python 2.5.1**
 - 1.60.1 Available under license
- 1.61 gmp 6.1.2**
 - 1.61.1 Available under license
- 1.62 Isof 4.92.a,8**
 - 1.62.1 Available under license
- 1.63 x-text 0.3.0**
 - 1.63.1 Available under license
- 1.64 jackson 1.9.13**
 - 1.64.1 Available under license
- 1.65 viper 1.3.2**
- 1.66 slf4j-log4j 1.7.5**
 - 1.66.1 Available under license
- 1.67 jetty-servlet 9.2.13.v20150730**
- 1.68 tomcat-juli 8.5.32**
 - 1.68.1 Available under license
- 1.69 go-playground-validator 9.0.0**
 - 1.69.1 Available under license
- 1.70 zjsonpatch 0.2.1**
- 1.71 jetty-http 9.2.13.v20150730**
- 1.72 glib 2.46.2 4.e17**
 - 1.72.1 Available under license
- 1.73 ncurses 6.0.0s20171223_1**
 - 1.73.1 Available under license
- 1.74 openssh 7.3**
 - 1.74.1 Available under license
- 1.75 bouncycastle-fips 1.51**
 - 1.75.1 Available under license
- 1.76 libunistring 0.9.9**
 - 1.76.1 Available under license
- 1.77 c3p0 0.9.1.1**
 - 1.77.1 Available under license
- 1.78 taglibs-standard-spec 1.2.5**
 - 1.78.1 Available under license
- 1.79 nettle 3.4**

- 1.79.1 Available under license
- 1.80 afero 1.2.2**
 - 1.80.1 Available under license
- 1.81 commons-io 1.3.2**
 - 1.81.1 Available under license
- 1.82 jackson-dataformat-yaml 2.9.5**
 - 1.82.1 Available under license
- 1.83 jetty-servlets 9.2.1.v20140609**
- 1.84 pcre 8.41**
 - 1.84.1 Available under license
- 1.85 regexp 2.7.2**
 - 1.85.1 Available under license
- 1.86 tomcat-api 8.5.32**
 - 1.86.1 Available under license
- 1.87 jackson-databind 1.0.1**
 - 1.87.1 Available under license
- 1.88 tomcat-coyote 8.5.32**
- 1.89 tcp-wrappers 7.6.24**
 - 1.89.1 Available under license
- 1.90 lua 5.1.5**
 - 1.90.1 Available under license
- 1.91 tomcat-annotations-api 8.5.32**
 - 1.91.1 Available under license
- 1.92 berkeley-db 5.3.28**
 - 1.92.1 Available under license
- 1.93 bzip2 1.0.4**
 - 1.93.1 Available under license
- 1.94 libjpeg 8b**
 - 1.94.1 Notifications
 - 1.94.2 Available under license
- 1.95 stax-api 1.0.1**
 - 1.95.1 Available under license
- 1.96 xmlunit-legacy 2.3.0**
- 1.97 mpfr 3.1.6**
 - 1.97.1 Available under license
- 1.98 tcsh 6.20.00**
 - 1.98.1 Available under license
- 1.99 net-snmp 5.7.1**
 - 1.99.1 Notifications

1.99.2 Available under license

1.100 nghttp 1.31.1

1.100.1 Available under license

1.101 jackson-jaxrs-base 2.9.5

1.101.1 Available under license

1.102 gdb 6.1.1

1.102.1 Available under license

1.103 libnet 1.2

1.103.1 Available under license

1.104 tomcat-websocket 8.5.32

1.104.1 Available under license

1.105 python 2.6

1.105.1 Available under license

1.106 tokyo-cabinet 1.4.48

1.106.1 Available under license

1.107 miglayout 3.5.5

1.108 c-ares 1.10.0

1.108.1 Available under license

1.109 quartz 2.1.7

1.109.1 Available under license

1.110 openssh 5.4p1

1.110.1 Available under license

1.111 tomcat-jasper-el 8.5.32

1.111.1 Available under license

1.112 jetty-util 9.2.13.v20150730

1.113 httpcomponents-core 4.3.1

1.113.1 Available under license

1.114 cpp 5.4.0

1.114.1 Available under license

1.115 apr 1.5.2

1.115.1 Available under license

1.116 aop-alliance 2.4.0

1.117 libarchive 3.3.1

1.117.1 Available under license

1.118 sqlite 3.22.0

1.118.1 Available under license

1.119 libtasn 4.13

1.119.1 Available under license

1.120 go-cache 2.1.0

1.120.1 Available under license
1.121 jetty-xml 9.2.13.v20150730
1.122 tomcat-jni 8.5.32
1.122.1 Available under license
1.123 tomcat-dbcp 8.5.32
1.123.1 Available under license
1.124 libpgp-error 1.27
1.124.1 Available under license
1.125 rpcbind 0.2.4
1.125.1 Available under license
1.126 libpcap 1.4.0
1.126.1 Available under license
1.127 tcp-dump 4.4.0
1.127.1 Available under license
1.128 commons-lang3 3.4
1.128.1 Available under license
1.129 openjdk 1.7.0u65
1.129.1 Available under license
1.130 libbsd FreeBSD-10.4.0
1.130.1 Available under license
1.131 jackson 1.0.1
1.132 gnutls 3.5.13
1.132.1 Available under license
1.133 commons-lang3 2.6
1.133.1 Available under license
1.134 tomcat-el-api 8.5.32
1.134.1 Available under license
1.135 json-glib 1.0.0
1.135.1 Available under license
1.136 tomcat-util-scan 8.5.32
1.136.1 Available under license
1.137 llvm 6.0.0
1.137.1 Available under license
1.138 websocket 1.1.FR
1.138.1 Available under license
1.139 libtirpc FreeBSD-10.4.0
1.139.1 Available under license
1.140 zip 3.0_1
1.140.1 Available under license

- 1.141 libffi 3.2.1_2**
 - 1.141.1 Available under license
- 1.142 tomcat-jdbc 8.5.32**
 - 1.142.1 Available under license
- 1.143 bolt 1.2.1**
 - 1.143.1 Available under license
- 1.144 libcx 6.0.0**
 - 1.144.1 Available under license
- 1.145 expat 2.0.1**
 - 1.145.1 Available under license
- 1.146 tomcat-storeconfig 8.5.32**
 - 1.146.1 Available under license
- 1.147 resourcelocator 1.0.1**
 - 1.147.1 Available under license
- 1.148 go-toml 1.3.0**
 - 1.148.1 Available under license
- 1.149 python 2.7.13**
 - 1.149.1 Available under license
- 1.150 xz 5.2.3**
 - 1.150.1 Available under license
- 1.151 unbound 1.8.1rc1**
 - 1.151.1 Available under license
- 1.152 tomcat-i18n-fr 8.5.32**
 - 1.152.1 Available under license
- 1.153 libmpc 1.0.3**
 - 1.153.1 Available under license
- 1.154 json-java 20131018**
- 1.155 antlr-runtime 4.5.1-1**
- 1.156 servlet-api 3.1.0**
 - 1.156.1 Available under license
- 1.157 commons-daemon 1.1.0**
 - 1.157.1 Available under license
- 1.158 tomcat-catalina-ha 8.5.32**
 - 1.158.1 Available under license
- 1.159 expat 2.2.1**
 - 1.159.1 Available under license
- 1.160 poco 1.9.0 all**
 - 1.160.1 Available under license
- 1.161 python 2.6.4**

1.161.1 Available under license
1.162 file 5.29
1.163 tiger-types 1.4
1.163.1 Available under license
1.164 tcl 8.4.20
1.164.1 Available under license
1.165 datatype 1.3.04
1.165.1 Available under license
1.166 file 5.19
1.166.1 Available under license
1.167 pcre 8.40
1.167.1 Available under license
1.168 tdb 1.3.12,1
1.168.1 Available under license
1.169 tomcat-jasper 8.5.32
1.169.1 Available under license
1.170 gnu-crypto 2.0.1
1.170.1 Available under license
1.171 ecj 4.5
1.171.1 Available under license
1.172 hamcrest 1.3
1.172.1 Available under license
1.173 jackson-databind 2.6.1
1.173.1 Available under license
1.174 samba 4.5.8
1.174.1 Available under license
1.175 mpdecimal 2.4.2
1.175.1 Available under license
1.176 gc 5.4.0
1.176.1 Available under license
1.177 heimdal 1.5.2
1.177.1 Available under license
1.178 graphviz 2.38.0
1.178.1 Available under license
1.179 zlib 1.2.3
1.179.1 Available under license
1.180 libidn 1.33
1.180.1 Available under license
1.181 httpcomponents-core 4.4.3

1.181.1 Available under license
1.182 hk2-utils 2.4.0
1.183 fsnotify 1.4.7
1.183.1 Available under license
1.184 bison 3.0.4
1.184.1 Available under license
1.185 libedit 20170329-3.1
1.185.1 Available under license
1.186 expat 2.2.0
1.186.1 Available under license
1.187 tevent 0.9.34_1
1.187.1 Available under license
1.188 apr-util 1.5.4
1.188.1 Available under license
1.189 jemalloc 5.1.0
1.189.1 Available under license
1.190 perl 5.24.1
1.190.1 Available under license
1.191 cast 1.3.0
1.191.1 Available under license
1.192 tokyo-cabinet 1.4.47
1.192.1 Available under license
1.193 jsp 2.3.FR
1.193.1 Available under license
1.194 pillow 1.1.6
1.194.1 Available under license
1.195 elfutils 0.168
1.195.1 Available under license
1.196 freebsd 10.4
1.196.1 Available under license
1.197 popt 1.16_2
1.197.1 Available under license
1.198 handlebars 4.0.6
1.199 expat 2.2.3
1.199.1 Available under license
1.200 unbound 1.5.7
1.200.1 Available under license
1.201 properties 1.8.0
1.201.1 Available under license

1.202 p11-kit 0.23.7

1.202.1 Available under license

1.203 libjpeg 6b

1.203.1 Notifications

1.203.2 Available under license

1.204 sqlite 3.15.2

1.204.1 Available under license

1.205 subversion 1.9.7

1.205.1 Available under license

1.206 openssl 0.9.7e

1.206.1 Available under license

1.207 jetty-continuation 9.2.13.v20150730

1.208 jersey-media-sse 2.22

1.209 flex 2.5.37

1.209.1 Available under license

1.210 ipmi-tool 1.8.18

1.210.1 Available under license

1.211 jetty-security 9.2.13.v20150730

1.212 bind 9.11.2

1.212.1 Available under license

1.213 jersey-entity-filtering 2.22

1.214 xml-apis 1.3.04

1.214.1 Available under license

1.215 libarchive 3.3.2

1.215.1 Available under license

1.216 jersey-client 2.22

1.217 accessors-smart 1.1

1.218 jetty 9.2.13.v20150730

1.218.1 Available under license

1.219 hk2-locator 2.4.0

1.220 jersey 2.22

1.220.1 Available under license

1.221 commons-collections 3.2.2

1.221.1 Available under license

1.222 go-shellwords 1.0.3

1.222.1 Available under license

1.223 javax-mail-api 1.5.4

1.223.1 Available under license

1.224 diffutils 2.8

1.224.1 Available under license
1.225 tomcat-catalina-ant 8.5.32
1.225.1 Available under license
1.226 dash 0.5.8
1.226.1 Available under license
1.227 make 4.2
1.227.1 Available under license
1.228 libgcrypt 1.8.0
1.228.1 Available under license
1.229 xmlunit-core 2.3.0
1.230 libpng 1.5.4
1.230.1 Available under license
1.231 jackson 2.9.5
1.231.1 Available under license
1.232 commons-lang3 3.1
1.232.1 Available under license
1.233 expat 1.95.6
1.233.1 Available under license
1.234 cobra 0.0.4
1.234.1 Available under license
1.235 file 5.11
1.235.1 Available under license
1.236 libiconv 1.14_11
1.236.1 Available under license
1.237 jetty-servlets 9.2.13.v20150730
1.238 thrift 0.9.1
1.238.1 Available under license
1.239 gettext 0.19.8.1
1.239.1 Available under license
1.240 snake-yaml 1.18
1.240.1 Available under license
1.241 gorilla 1.7.1
1.241.1 Available under license
1.242 curl 7.64.1
1.242.1 Available under license
1.243 lucene 2.4.1
1.243.1 Available under license
1.244 tomcat-i18n-es 8.5.32
1.244.1 Available under license

1.245 gdbm 1.13.

1.245.1 Available under license

1.246 m4 1.4.18

1.246.1 Available under license

1.247 protobuf 3.5.1.1

1.247.1 Available under license

1.248 bean-validation-api 1.1.0

1.249 chardet 3.0.4

1.249.1 Available under license

1.250 goleveldb 1.0.0

1.250.1 Available under license

1.251 go-yaml 2.0

1.251.1 Available under license

1.252 sbsigntool 0.7

1.252.1 Available under license

1.253 expat 2.1.1

1.253.1 Available under license

1.254 tokyo-cabinet 1.2.11

1.254.1 Available under license

1.255 commons-collections 4.0

1.255.1 Available under license

1.256 python 3.6.5

1.256.1 Available under license

1.257 hcl 1.0.0

1.257.1 Available under license

1.258 beanutils 1.9.3

1.258.1 Available under license

1.259 openjdk 1.8.0u131

1.259.1 Available under license

1.260 sudo 1.8.28

1.260.1 Available under license

1.261 libxslt 1.1.32

1.261.1 Available under license

1.262 boost 1.66.0

1.262.1 Available under license

1.263 sqlite 3.7.8

1.263.1 Available under license

1.264 apache-log4j 1.2.17

1.264.1 Available under license

- 1.265 commons-beanutils 1.9.3**
 - 1.265.1 Available under license
- 1.266 json-c 0.12.1**
 - 1.266.1 Available under license
- 1.267 ldns 1.6.17**
 - 1.267.1 Available under license
- 1.268 jackson-annotations 2.9.5**
 - 1.268.1 Available under license
- 1.269 jersey-common 2.22**
- 1.270 ldb 1.2.840**
 - 1.270.1 Available under license
- 1.271 jackson-databind 1.9.13**
 - 1.271.1 Available under license
- 1.272 pflag 1.0.5**
 - 1.272.1 Available under license
- 1.273 jetty-webapp 9.2.13.v20150730**
- 1.274 zlib 1.1.3**
- 1.275 nginx 1.12.1**
 - 1.275.1 Available under license
- 1.276 stateless4j 2.5.0**
- 1.277 libpng 1.6.20**
 - 1.277.1 Available under license
- 1.278 openssh 7.3p1**
 - 1.278.1 Available under license
- 1.279 zlib 1.2.5**
 - 1.279.1 Available under license
- 1.280 go-proxy 1.1**
 - 1.280.1 Available under license
- 1.281 jersey-media-json-jackson 2.22**
- 1.282 javassist 3.20.0**
 - 1.282.1 Available under license
- 1.283 libunwind 20170615**
 - 1.283.1 Available under license
- 1.284 bzip2 1.0.6**
 - 1.284.1 Available under license
- 1.285 jersey-server 2.22**
- 1.286 httpcomponents-client 4.3.2**
 - 1.286.1 Available under license
- 1.287 httpcomponents-client 4.5.1**

- 1.288 javax-annotation 1.2.fr**
 - 1.288.1 Available under license
- 1.289 jetty-util 9.2.1.v20140609**
- 1.290 tomcat-websocket-api 8.5.32**
 - 1.290.1 Available under license
- 1.291 hk2-api 2.4.0**
 - 1.291.1 Available under license
- 1.292 redis 5.0.5**
 - 1.292.1 Available under license
- 1.293 ecj 3.12.3**
 - 1.293.1 Available under license
- 1.294 gdb 7.12.1**
 - 1.294.1 Available under license
- 1.295 taglibs-standard 1.2.5**
 - 1.295.1 Available under license
- 1.296 xalan 2.7.2**
 - 1.296.1 Available under license
- 1.297 jvm-hotspot-oracle 1.7.0u65**
 - 1.297.1 Available under license
- 1.298 tomcat-tribes 8.5.32**
 - 1.298.1 Available under license
- 1.299 jetty 9.2.1.v20140609**
 - 1.299.1 Available under license
- 1.300 talloc 2.1.10_1**
 - 1.300.1 Available under license
- 1.301 tomcat-i18n-ja 8.5.32**
 - 1.301.1 Available under license
- 1.302 openssl 1.0.1u**
 - 1.302.1 Available under license
- 1.303 xmlsec 1.2.25**
 - 1.303.1 Available under license
- 1.304 jackson 2.6.1**
 - 1.304.1 Available under license
- 1.305 keypoint-pngencoder 1.5**
 - 1.305.1 Available under license
- 1.306 slf4j 1.7.16**
 - 1.306.1 Available under license
- 1.307 ucl 1.03**
 - 1.307.1 Available under license

- 1.308 serf 1.3.7_1**
 - 1.308.1 Available under license
- 1.309 jersey-container-servlet-core 2.22**
- 1.310 javax-inject 2.4.0**
- 1.311 lzo 2.10_1**
 - 1.311.1 Available under license
- 1.312 apache-commons-logging 1.2**
 - 1.312.1 Available under license
- 1.313 libsoup 2.55.90**
 - 1.313.1 Available under license
- 1.314 stunnel 5.03**
 - 1.314.1 Available under license
- 1.315 readline 7.0.3**
 - 1.315.1 Available under license
- 1.316 jetty-io 9.2.1.v20140609**
 - 1.316.1 Available under license
- 1.317 golang-snappy 0.0.1**
 - 1.317.1 Available under license
- 1.318 aopalliance-repackaged 2.4.0**
- 1.319 ipmiutil 1.8.18**
 - 1.319.1 Available under license
- 1.320 jackson-module-jaxb-annotations 2.9.5**
- 1.321 tomcat-catalina 8.5.32**
 - 1.321.1 Available under license
- 1.322 clang 3.4.1**
 - 1.322.1 Available under license
- 1.323 slf4j 1.7.5**
 - 1.323.1 Available under license
- 1.324 bcpkix-jdk15on 1.51**
- 1.325 tomcat-jaspic-api 8.5.32**
- 1.326 openssl 0.9.8r**
 - 1.326.1 Available under license
- 1.327 jetty-continuation 9.2.1.v20140609**
 - 1.327.1 Available under license
- 1.328 libyaml 0.1.4**
 - 1.328.1 Available under license
- 1.329 openssl 1.0.0d**
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1.3 tomcat-servlet-api 8.5.32

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1.4 jackson-jaxrs-json-provider 2.9.5

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1.7 libxml 2.9.7

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1.8 open-ldap 2.3.43

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1.11 jersey-guava 2.22

1.12 trove4j 2.1.0

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1.13 lua 5.3.4

1.14 bcel 2.7.2

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1.15 jansson 2.5

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1.16 mitchellh-mapstructure 1.1.2

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1.17 icu 1.6.0

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1.18 commons-codec 1.9

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1.19 jersey-media-jaxb 2.22

1.20 libtool 2.4.6

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1.22 jetty-io 9.2.13.v20150730

1.23 lua-cjson 2.1.0

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/* Author: Mihai Bazon, September 2002

* <http://students.infoiasi.ro/~mishoo>

*

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* Version: 0.4

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1.24 tomcat-jsp-api 8.5.32

1.25 jettison 1.3

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1.26 binutils 2.28

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Finally, any free program is threatened constantly by software patents. We wish to avoid the danger that companies distributing free software will individually obtain patent licenses, thus in effect transforming the program into proprietary software. To prevent this, we have made it clear that any patent must be licensed for everyone's free use or not licensed at all.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License, which was designed for utility programs. This license, the GNU Library General Public License, applies to certain designated libraries. This license is quite different from the ordinary one; be sure to read it in full, and don't assume that anything in it is the same as in the ordinary license.

The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without

changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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0. This License Agreement applies to any software library which contains a notice placed by the copyright holder or other authorized party saying it may be distributed under the terms of this Library General Public License (also called "this License"). Each licensee is addressed as "you".

A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated

straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

1. You may copy and distribute verbatim copies of the Library's complete source code as you receive it, in any medium, provided that you conspicuously and appropriately publish on each copy an appropriate copyright notice and disclaimer of warranty; keep intact all the notices that refer to this License and to the absence of any warranty; and distribute a copy of this License along with the Library.

You may charge a fee for the physical act of transferring a copy, and you may at your option offer warranty protection in exchange for a fee.

2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

- a) The modified work must itself be a software library.
- b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.
- c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.
- d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of

its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form

under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

- a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)
- b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.
- c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.
- d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

- a) Accompany the combined library with a copy of the same work based on the Library, uncombined with any other library facilities. This must be distributed under the terms of the Sections above.
- b) Give prominent notice with the combined library of the fact that part of it is a work based on the Library, and explaining where to find the accompanying uncombined form of the same work.

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If any portion of this section is held invalid or unenforceable under any particular circumstance, the balance of the section is intended to apply, and the section as a whole is intended to apply in other circumstances.

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This section is intended to make thoroughly clear what is believed to be a consequence of the rest of this License.

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END OF TERMS AND CONDITIONS

Appendix: How to Apply These Terms to Your New Libraries

If you develop a new library, and you want it to be of the greatest possible use to the public, we recommend making it free software that everyone can redistribute and change. You can do so by permitting redistribution under these terms (or, alternatively, under the terms of the ordinary General Public License).

To apply these terms, attach the following notices to the library. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

```
<one line to give the library's name and a brief idea of what it does.>  
Copyright (C) <year> <name of author>
```

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Also add information on how to contact you by electronic and paper mail.

You should also get your employer (if you work as a programmer) or your school, if any, to sign a "copyright disclaimer" for the library, if necessary. Here is a sample; alter the names:

Yoyodyne, Inc., hereby disclaims all copyright interest in the library `Frob' (a library for tweaking knobs) written by James Random Hacker.

<signature of Ty Coon>, 1 April 1990
Ty Coon, President of Vice

That's all there is to it!

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[This is the first released version of the Lesser GPL. It also counts as the successor of the GNU Library Public License, version 2, hence the version number 2.1.]

Preamble

The licenses for most software are designed to take away your freedom to share and change it. By contrast, the GNU General Public Licenses are intended to guarantee your freedom to share and change free software--to make sure the software is free for all its users.

This license, the Lesser General Public License, applies to some specially designated software packages--typically libraries--of the

Free Software Foundation and other authors who decide to use it. You can use it too, but we suggest you first think carefully about whether this license or the ordinary General Public License is the better strategy to use in any particular case, based on the explanations below.

When we speak of free software, we are referring to freedom of use, not price. Our General Public Licenses are designed to make sure that you have the freedom to distribute copies of free software (and charge for this service if you wish); that you receive source code or can get it if you want it; that you can change the software and use pieces of it in new free programs; and that you are informed that you can do these things.

To protect your rights, we need to make restrictions that forbid distributors to deny you these rights or to ask you to surrender these rights. These restrictions translate to certain responsibilities for you if you distribute copies of the library or if you modify it.

For example, if you distribute copies of the library, whether gratis or for a fee, you must give the recipients all the rights that we gave you. You must make sure that they, too, receive or can get the source code. If you link other code with the library, you must provide complete object files to the recipients, so that they can relink them with the library after making changes to the library and recompiling it. And you must show them these terms so they know their rights.

We protect your rights with a two-step method: (1) we copyright the library, and (2) we offer you this license, which gives you legal permission to copy, distribute and/or modify the library.

To protect each distributor, we want to make it very clear that there is no warranty for the free library. Also, if the library is modified by someone else and passed on, the recipients should know that what they have is not the original version, so that the original author's reputation will not be affected by problems that might be introduced by others.

Finally, software patents pose a constant threat to the existence of any free program. We wish to make sure that a company cannot effectively restrict the users of a free program by obtaining a restrictive license from a patent holder. Therefore, we insist that any patent license obtained for a version of the library must be consistent with the full freedom of use specified in this license.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License. This license, the GNU Lesser General Public License, applies to certain designated libraries, and is quite different from the ordinary General Public License. We use

this license for certain libraries in order to permit linking those libraries into non-free programs.

When a program is linked with a library, whether statically or using a shared library, the combination of the two is legally speaking a combined work, a derivative of the original library. The ordinary General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

We call this license the "Lesser" General Public License because it does Less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, whereas the latter must be combined with the library in order to run.

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Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

1. You may copy and distribute verbatim copies of the Library's complete source code as you receive it, in any medium, provided that you conspicuously and appropriately publish on each copy an appropriate copyright notice and disclaimer of warranty; keep intact all the notices that refer to this License and to the absence of any warranty; and distribute a copy of this License along with the Library.

You may charge a fee for the physical act of transferring a copy, and you may at your option offer warranty protection in exchange for a fee.

2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

- a) The modified work must itself be a software library.
- b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.
- c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.
- d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so

that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object

file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

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@center Version 2.1, February 1999

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@appendixsubsec Preamble

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@appendixsubsec TERMS AND CONDITIONS FOR COPYING, DISTRIBUTION AND MODIFICATION
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@enumerate 0

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@end enumerate

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@iftex
@heading END OF TERMS AND CONDITIONS
@end iftex
@ifinfo
@center END OF TERMS AND CONDITIONS
@end ifinfo
```

```
@page
@appendixsubsec How to Apply These Terms to Your New Libraries
```

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```
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@var{one line to give the library's name and an idea of what it does.}
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```

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```
@end smallexample
```

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```
@smallexample
```

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'Frob' (a library for tweaking knobs) written by James Random Hacker.

@var{signature of Ty Coon}, 1 April 1990

Ty Coon, President of Vice

@end smallexample

That's all there is to it!

(This file is under construction.) -*- text -*-

If you've contributed to gas and your name isn't listed here, it is
not meant as a slight. I just don't know about it. Email me,
nickc@redhat.com and I'll correct the situation.

This file will eventually be deleted: The general info will go into
the documentation, and info on specific files will go into an AUTHORS
file, as requested by the FSF.

+++++

Dean Elsner wrote the original gas for vax. [more details?]

Jay Fenlason maintained gas for a while, adding support for
gdb-specific debug information and the 68k series machines, most of
the preprocessing pass, and extensive changes in messages.c,
input-file.c, write.c.

K. Richard Pixley maintained gas for a while, adding various
enhancements and many bug fixes, including merging support for several
processors, breaking gas up to handle multiple object file format
backends (including heavy rewrite, testing, an integration of the coff
and b.out backends), adding configuration including heavy testing and
verification of cross assemblers and file splits and renaming,
converted gas to strictly ansi C including full prototypes, added
support for m680[34]0 & cpu32, considerable work on i960 including a
coff port (including considerable amounts of reverse engineering), a
sparc opcode file rewrite, decstation, rs6000, and hp300hpux host
ports, updated "know" assertions and made them work, much other
reorganization, cleanup, and lint.

Ken Raeburn wrote the high-level BFD interface code to replace most of
the code in format-specific I/O modules.

The original Vax-VMS support was contributed by David L. Kashtan.
Eric Youngdale and Pat Rankin have done much work with it since.

The Intel 80386 machine description was written by Eliot Dresselhaus.

Minh Tran-Le at IntelliCorp contributed some AIX 386 support.

The Motorola 88k machine description was contributed by Devon Bowen of Buffalo University and Torbjorn Granlund of the Swedish Institute of Computer Science.

Keith Knowles at the Open Software Foundation wrote the original MIPS back end (tc-mips.c, tc-mips.h), and contributed Rose format support that hasn't been merged in yet. Ralph Campbell worked with the MIPS code to support a.out format.

Support for the Zilog Z8k and Hitachi H8/300, H8/500 and SH processors (tc-z8k, tc-h8300, tc-h8500, tc-sh), and IEEE 695 object file format (obj-ieee), was written by Steve Chamberlain of Cygnus Solutions. Steve also modified the COFF back end (obj-coffbfd) to use BFD for some low-level operations, for use with the Hitachi, 29k and Zilog targets.

John Gilmore built the AMD 29000 support, added .include support, and simplified the configuration of which versions accept which pseudo-ops. He updated the 68k machine description so that Motorola's opcodes always produced fixed-size instructions (e.g. jsr), while synthetic instructions remained shrinkable (jbsr). John fixed many bugs, including true tested cross-compilation support, and one bug in relaxation that took a week and required the proverbial one-bit fix.

Ian Lance Taylor of Cygnus Solutions merged the Motorola and MIT syntaxes for the 68k, completed support for some COFF targets (68k, i386 SVR3, and SCO Unix), wrote the ECOFF support based on Michael Meissner's mips-tfile program, wrote the PowerPC and RS/6000 support, and made a few other minor patches. He handled the binutils releases for versions 2.7 through 2.9.

David Edelsohn contributed fixes for the PowerPC and AIX support.

Steve Chamberlain made gas able to generate listings.

Support for the HP9000/300 was contributed by Glenn Engel of HP.

Support for ELF format files has been worked on by Mark Eichin of Cygnus Solutions (original, incomplete implementation), Pete Hoogenboom at the University of Utah (HPPA mainly), Michael Meissner of the Open Software Foundation (i386 mainly), and Ken Raeburn of Cygnus Solutions (sparc, initial 64-bit support).

Several engineers at Cygnus Solutions have also provided many small bug fixes and configuration enhancements.

The initial Alpha support was contributed by Carnegie-Mellon University. Additional work was done by Ken Raeburn of Cygnus Solutions. Richard Henderson then rewrote much of the Alpha support.

Ian Dall updated the support code for the National Semiconductor 32000 series, and added support for Mach 3 and NetBSD running on the PC532.

Klaus Kaempf ported the assembler and the binutils to openVMS/Alpha.

Steve Haworth contributed the support for the Texas Instruction c30 (tms320c30).

H.J. Lu has contributed many patches and much testing.

Alan Modra reworked much of the i386 backend, improving the error checking, updating the code, and improving the 16 bit support, using patches from the work of Martynas Kunigelis and H.J. Lu.

Many others have contributed large or small bugfixes and enhancements. If you've contributed significant work and are not mentioned on this list, and want to be, let us know. Some of the history has been lost; we aren't intentionally leaving anyone out.

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1.27 apache-httpclient 4.3.2

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Fluent API for Apache HttpClient

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1.28 smartmontools 6.5_2

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1.29 jvm-hotspot-oracle 1.8.0u131

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1.30 json-smart 2.2.1

1.31 jopt-simple 4.9

1.32 zlib 1.2.8

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```
/* zlib.h -- interface of the 'zlib' general purpose compression library
version 1.2.11, January 15th, 2017
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1.33 zlib 1.0.4

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1.34 tomcat 8.5.32

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- javaee_web_services_1_3.xsd
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- web-common_3_0.xsd
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- javaee_web_services_client_1_3.xsd
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- web-common_3_0.xsd
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Mere aggregation of another independent work with the Program (or its derivative) on a volume of a storage or distribution medium does not bring the other work under the scope of these terms.

3. You may copy and distribute the Program (or a portion or derivative of it, under Paragraph 2) in object code or executable form under the terms of Paragraphs 1 and 2 above provided that you also do one of the following:

a) accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Paragraphs 1 and 2 above; or,

b) accompany it with a written offer, valid for at least three years, to give any third party free (except for a nominal charge for the cost of distribution) a complete machine-readable copy of the corresponding source code, to be distributed under the terms of Paragraphs 1 and 2 above; or,

c) accompany it with the information you received as to where the corresponding source code may be obtained. (This alternative is allowed only for noncommercial distribution and only if you received the program in object code or executable form alone.)

Source code for a work means the preferred form of the work for making modifications to it. For an executable file, complete source code means all the source code for all modules it contains; but, as a special exception, it need not include source code for modules which are standard libraries that accompany the operating system on which the executable file runs, or for standard header files or definitions files that accompany that operating system.

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END OF TERMS AND CONDITIONS

Appendix: How to Apply These Terms to Your New Programs

If you develop a new program, and you want it to be of the greatest possible use to humanity, the best way to achieve this is to make it free software which everyone can redistribute and change under these terms.

To do so, attach the following notices to the program. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

```
<one line to give the program's name and a brief idea of what it does.>  
Copyright (C) 19yy <name of author>
```

```
This program is free software; you can redistribute it and/or modify  
it under the terms of the GNU General Public License as published by  
the Free Software Foundation; either version 1, or (at your option)  
any later version.
```

```
This program is distributed in the hope that it will be useful,  
but WITHOUT ANY WARRANTY; without even the implied warranty of  
MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the  
GNU General Public License for more details.
```

```
You should have received a copy of the GNU General Public License  
along with this program; if not, write to the Free Software  
Foundation, Inc.
```

Also add information on how to contact you by electronic and paper mail.

If the program is interactive, make it output a short notice like this when it starts in an interactive mode:

```
Gnomovision version 69, Copyright (C) 19xx name of author  
Gnomovision comes with ABSOLUTELY NO WARRANTY; for details type `show w'.  
This is free software, and you are welcome to redistribute it  
under certain conditions; type `show c' for details.
```

The hypothetical commands `show w` and `show c` should show the appropriate parts of the General Public License. Of course, the commands you use may be called something other than `show w` and `show c`; they could even be mouse-clicks or menu items--whatever suits your program.

You should also get your employer (if you work as a programmer) or your school, if any, to sign a "copyright disclaimer" for the program, if necessary. Here a sample; alter the names:

Yoyodyne, Inc., hereby disclaims all copyright interest in the program `Gnomovision' (a program to direct compilers to make passes at assemblers) written by James Hacker.

<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice

That's all there is to it!
[I have snipped the snail mail address of the FSF because it has changed in the past and is likely to change again. The current address should be at <http://www.gnu.org/>]

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Version 2, June 1991

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[This is the first released version of the library GPL. It is numbered 2 because it goes with version 2 of the ordinary GPL.]

Preamble

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For example, if you distribute copies of the library, whether gratis or for a fee, you must give the recipients all the rights that we gave you. You must make sure that they, too, receive or can get the source code. If you link a program with the library, you must provide complete object files to the recipients so that they can relink them with the library, after making changes to the library and recompiling it. And you must show them these terms so they know their rights.

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The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a

derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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(For example, a function in a library to compute square roots has

a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

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Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a

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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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- b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.
- c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.
- d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

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It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined

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Appendix: How to Apply These Terms to Your New Libraries

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To apply these terms, attach the following notices to the library. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

```
<one line to give the library's name and a brief idea of what it does.>  
Copyright (C) <year> <name of author>
```

```
This library is free software; you can redistribute it and/or  
modify it under the terms of the GNU Library General Public  
License as published by the Free Software Foundation; either  
version 2 of the License, or (at your option) any later version.
```

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Also add information on how to contact you by electronic and paper mail.

You should also get your employer (if you work as a programmer) or your
school, if any, to sign a "copyright disclaimer" for the library, if
necessary. Here is a sample; alter the names:

Yoyodyne, Inc., hereby disclaims all copyright interest in the
library `Frob' (a library for tweaking knobs) written by James Random Hacker.

<signature of Ty Coon>, 1 April 1990
Ty Coon, President of Vice

That's all there is to it!

1.38 ASM 5.0.3

1.38.1 Available under license :

No license file was found, but licenses were detected in source scan.

/**

* ASM: a very small and fast Java bytecode manipulation framework

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*

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* CONTRACT, STRICT LIABILITY, OR TORT (INCLUDING NEGLIGENCE OR OTHERWISE)
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* THE POSSIBILITY OF SUCH DAMAGE.
*/

Found in path(s):

* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/ClassVisitor.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/MethodWriter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/TableSwitchInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/util/CheckAnnotationAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/MethodOptimizer.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/Item.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/RemappingSignatureAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/util/TraceAnnotationVisitor.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/util/CheckFieldAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/Handle.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/util/CheckClassAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/LdcInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/LabelNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/InstructionAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/IntInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/FieldNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/analysis/BasicValue.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-

jar/org/objectweb/asm/util/CheckMethodAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/ParameterNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/MultiANewArrayInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/signature/SignatureWriter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/TableSwitchGenerator.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/util/Printer.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/IncInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/LineNumberNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/MethodInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/Context.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/AdviceAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/AnnotationNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/LocalVariablesSorter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/util/TraceSignatureVisitor.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/analysis/Interpreter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/ByteVector.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/util/TraceFieldVisitor.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/ClassConstantsCollector.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/TypeInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/InsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/ClassReader.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/MethodConstantsCollector.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/FieldConstantsCollector.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/MethodNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-

jar/org/objectweb/asm/AnnotationWriter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/FieldInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/RemappingAnnotationAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/analysis/Subroutine.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/ClassWriter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/VarInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/ConstantPool.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/LookupSwitchInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/AbstractInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/RemappingFieldAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/NameMapping.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/StaticInitMerger.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/JumpInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/Frame.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/Edge.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/RemappingMethodAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/Constant.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/SimpleRemapper.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/RemappingClassAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/Shrinker.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/MethodVisitor.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/Label.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/Handler.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/JarOptimizer.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-

jar/org/objectweb/asm/commons/CodeSizeEvaluator.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/util/CheckSignatureAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/analysis/AnalyzerException.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/SerialVersionUIDAdder.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/util/ASMifier.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/FrameNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/TypeAnnotationNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/InvokeDynamicInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/signature/SignatureVisitor.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/analysis/SourceInterpreter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/ClassOptimizer.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/LocalVariableNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/analysis/BasicVerifier.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/signature/SignatureReader.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/InsnList.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/AnnotationConstantsCollector.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/InnerClassNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/util/TraceMethodVisitor.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/Method.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/Attribute.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/analysis/Frame.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/TryCatchBlockSorter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/AnnotationVisitor.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/analysis/BasicInterpreter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-

jar/org/objectweb/asm/FieldWriter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/Opcodes.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/analysis/SimpleVerifier.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/analysis/SourceValue.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/analysis/Value.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/Type.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/analysis/SmallSet.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/Remapper.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/util/TraceClassVisitor.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/FieldVisitor.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/analysis/Analyzer.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/TryCatchBlockNode.java

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*/

/**

* Creates a new {@link AnalyzerAdapter}. *Subclasses must not use this
* constructor*. Instead, they must use the
* {@link #AnalyzerAdapter(int, String, int, String, String, MethodVisitor)}
* version.
*
* @param owner
* the owner's class name.
* @param access
* the method's access flags (see {@link Opcodes}).
* @param name
* the method's name.
* @param desc
* the method's descriptor (see {@link Type Type}).
* @param mv
* the method visitor to which this adapter delegates calls. May
* be `null`.
* @throws IllegalStateException
* If a subclass calls this constructor.
*/

Found in path(s):

* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/AnalyzerAdapter.java

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* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/xml/SAXClassAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/xml/SAXCodeAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/xml/SAXAnnotationAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/xml/ASMContentHandler.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/xml/SAXAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/xml/Processor.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/xml/SAXFieldAdapter.java

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*/

/**

* Constructs a new { @link LocalVariableAnnotationNode }. *<i>*Subclasses must
* not use this constructor*</i>*. Instead, they must use the
* { @link #LocalVariableAnnotationNode(int, TypePath, LabelNode[], LabelNode[], int[], String) }
* version.

*

* @param typeRef

* a reference to the annotated type. See { @link TypeReference }.

* @param typePath

* the path to the annotated type argument, wildcard bound, array
* element type, or static inner type within 'typeRef'. May be
* `<tt>null</tt>` if the annotation targets 'typeRef' as a whole.

* @param start

* the first instructions corresponding to the continuous ranges
* that make the scope of this local variable (inclusive).

* @param end

* the last instructions corresponding to the continuous ranges
* that make the scope of this local variable (exclusive). This
* array must have the same size as the 'start' array.

* @param index

* the local variable's index in each range. This array must have
* the same size as the 'start' array.

* @param desc

* the class descriptor of the annotation class.

*/

Found in path(s):

* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/LocalVariableAnnotationNode.java

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*/

/**

* Constructs a new { @link Textifier}. *<i>*Subclasses must not use this
* constructor*</i>*. Instead, they must use the { @link #Textifier(int)}
* version.

*

* @throws IllegalStateException

* If a subclass calls this constructor.

*/

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* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
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jar/org/objectweb/asm/util/ASMifiable.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/util/Textifiable.java
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- * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
- jar/org/objectweb/asm/TypeReference.java

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*/

/**

* Constructs a new {@link ClassNode}. *Subclasses must not use this
* constructor*. Instead, they must use the {@link #ClassNode(int)}
* version.

*

* @throws IllegalStateException

* If a subclass calls this constructor.

*/

Found in path(s):

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*/
/**
```

```
 * Creates a new JSRInliner. <i>Subclasses must not use this
 * constructor</i>. Instead, they must use the
 * {@link #JSRInlinerAdapter(int, MethodVisitor, int, String, String, String, String[])}
 * version.
 *
 * @param mv
 *     the <code>MethodVisitor</code> to send the resulting inlined
 *     method code to (use <code>>null</code> for none).
 * @param access
 *     the method's access flags (see { @link Opcodes}). This
 *     parameter also indicates if the method is synthetic and/or
 *     deprecated.
 * @param name
 *     the method's name.
 * @param desc
 *     the method's descriptor (see { @link Type}).
 * @param signature
 *     the method's signature. May be <tt>>null</tt>.
 * @param exceptions
 *     the internal names of the method's exception classes (see
 *     { @link Type#getInternalName() getInternalName}). May be
 *     <tt>>null</tt>.
 * @throws IllegalStateException
 *     If a subclass calls this constructor.
 */
```

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```
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
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*/
/**
 * Creates a new {@link GeneratorAdapter}. Subclasses must not use this
 * constructor. Instead, they must use the
 * {@link #GeneratorAdapter(int, MethodVisitor, int, String, String)}
 * version.
 *
 * @param mv
 *     the method visitor to which this adapter delegates calls.
 * @param access
 *     the method's access flags (see {@link Opcodes}).
 * @param name
 *     the method's name.
 * @param desc
 *     the method's descriptor (see {@link Type Type}).
 * @throws IllegalStateException
 *     If a subclass calls this constructor.
 */

```

Found in path(s):

```

* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
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* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/signature/package.html
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/commons/package.html
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/package.html
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/tree/package.html
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/util/package.html

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1.46 pcre 8.35

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1.48 postgresql 7.4.2

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1.49 libdouble-conversion 1.1.1

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1.50 javax-ws-rs-api 2.0.1

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version 1.2.11, January 15th, 2017

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1.53 wiremock-standalone 2.5.1

1.54 protobuf-java 2.2.0

1.55 jetty-http 9.2.1.v20140609

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1.56 curl 7.55.1

1.56.1 Available under license :

License Mixing

=====

libcurl can be built to use a fair amount of various third party libraries, libraries that are written and provided by other parties that are distributed using their own licenses. Even libcurl itself contains code that may cause problems to some. This document attempts to describe what licenses libcurl and the other libraries use and what possible dilemmas linking and mixing them all can lead to for end users.

I am not a lawyer and this is not legal advice!

One common dilemma is that [GPL](<https://www.gnu.org/licenses/gpl.html>) licensed code is not allowed to be linked with code licensed under the [Original BSD license](<https://spdx.org/licenses/BSD-4-Clause.html>) (with the announcement clause). You may still build your own copies that use them all, but distributing them as binaries would be to violate the GPL license - unless you accompany your license with an [exception](<https://www.gnu.org/licenses/gpl-faq.html#GPLIncompatibleLibs>). This particular problem was addressed when the [Modified BSD license](<https://opensource.org/licenses/BSD-3-Clause>) was created, which does not have the announcement clause that collides with GPL.

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GnuTLS

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c-ares

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zlib

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MIT Kerberos

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Heimdal

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1.57 bouncy-castle 1.51

1.58 tinycdb 0.76

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1.59 less 481

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1.60 python 2.5.1

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A. HISTORY OF THE SOFTWARE

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see <http://www.zope.com>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

All Python releases are Open Source (see <http://www.opensource.org> for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

Release	Derived from	Year	Owner	GPL-compatible? (1)
0.9.0 thru 1.2		1991-1995	CWI	yes
1.3 thru 1.5.2	1.2	1995-1999	CNRI	yes
1.6	1.5.2	2000	CNRI	no
2.0	1.6	2000	BeOpen.com	no
1.6.1	1.6	2001	CNRI	yes (2)
2.1	2.0+1.6.1	2001	PSF	no
2.0.1	2.0+1.6.1	2001	PSF	yes
2.1.1	2.1+2.0.1	2001	PSF	yes
2.2	2.1.1	2001	PSF	yes
2.1.2	2.1.1	2002	PSF	yes
2.1.3	2.1.2	2002	PSF	yes
2.2.1	2.2	2002	PSF	yes
2.2.2	2.2.1	2002	PSF	yes
2.2.3	2.2.2	2003	PSF	yes
2.3	2.2.2	2002-2003	PSF	yes
2.3.1	2.3	2002-2003	PSF	yes
2.3.2	2.3.1	2002-2003	PSF	yes
2.3.3	2.3.2	2002-2003	PSF	yes
2.3.4	2.3.3	2004	PSF	yes
2.3.5	2.3.4	2005	PSF	yes
2.4	2.3	2004	PSF	yes
2.4.1	2.4	2005	PSF	yes
2.4.2	2.4.1	2005	PSF	yes
2.4.3	2.4.2	2006	PSF	yes
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2.5.1	2.5	2007	PSF	yes

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Version 3, 29 June 2007

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Version 3, 29 June 2007

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1.62 Isof 4.92.a,8

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1.64 jackson 1.9.13

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Jackson JSON processor

Jackson is a high-performance, Free/Open Source JSON processing library.

It was originally written by Tatu Saloranta (tatu.saloranta@iki.fi), and has been in development since 2007.

It is currently developed by a community of developers, as well as supported commercially by FasterXML.com.

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1.65 viper 1.3.2

1.66 slf4j-log4j 1.7.5

1.66.1 Available under license :

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1.67 jetty-servlet 9.2.13.v20150730

1.68 tomcat-juli 8.5.32

1.68.1 Available under license :

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1.69 go-playground-validator 9.0.0

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1.70 zjsonpatch 0.2.1

1.71 jetty-http 9.2.13.v20150730

1.72 glib 2.46.2 4.e17

1.72.1 Available under license :

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1.73 ncurses 6.0.0s20171223_1

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1.74 openssh 7.3

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*


```

*/

#ifndef _BSD_WAITPID_H
#define _BSD_WAITPID_H

#ifndef HAVE_WAITPID
/* Clean out any potential issues */
#undef WIFEXITED
#undef WIFSTOPPED
#undef WIFSIGNALED

/* Define required functions to mimic a POSIX look and feel */
#define _W_INT(w) (*(int*)&(w)) /* convert union wait to int */
#define WIFEXITED(w) (!((_W_INT(w)) & 0377))
#define WIFSTOPPED(w) ((_W_INT(w)) & 0100)
#define WIFSIGNALED(w) (!WIFEXITED(w) && !WIFSTOPPED(w))
#define WEXITSTATUS(w) (int)(WIFEXITED(w) ? ((_W_INT(w)) >> 8) & 0377) : -1)
#define WTERMSIG(w) (int)(WIFSIGNALED(w) ? (_W_INT(w)) & 0177) : -1)
#define WCOREFLAG 0x80
#define WCOREDUMP(w) ((_W_INT(w)) & WCOREFLAG)

/* Prototype */
pid_t waitpid(int, int *, int);

#endif /* !HAVE_WAITPID */
#endif /* _BSD_WAITPID_H */
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```

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* @version 3.0 (December 2000)

*

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*

* @author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>

* @author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>

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*****/
```

```
$OpenBSD: LICENCE,v 1.20 2017/04/30 23:26:16 djm Exp $
```

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```

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```

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```

```
*/
```

```
#ifndef HAVE_SETRESGID
```

```
int setresgid(gid_t, gid_t, gid_t);
```

```
#endif
```

```
#ifndef HAVE_SETRESUID
```

```
int setresuid(uid_t, uid_t, uid_t);
```

```
#endif
```

```
/* $OpenBSD: poll.h,v 1.11 2003/12/10 23:10:08 millert Exp $ */
```

```
/*
```

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```

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*/

/* OPENBSD ORIGINAL: sys/sys/poll.h */

```
#if !defined(HAVE_POLL) && !defined(HAVE_POLL_H)
#ifdef _COMPAT_POLL_H_
#define _COMPAT_POLL_H_
```

```
typedef struct pollfd {
    int fd;
    short events;
    short revents;
} pollfd_t;
```

```
typedef unsigned int nfds_t;
```

```
#define POLLIN 0x0001
#define POLLOUT 0x0004
#define POLLERR 0x0008
#define POLLHUP 0x0010
#define POLLNVAL 0x0020
#if 0
/* the following are currently not implemented */
#define POLLPRI 0x0002
#define POLLRDNORM 0x0040
#define POLLNORM POLLRDNORM
#define POLLWRNORM POLLOUT
#define POLLRDBAND 0x0080
#define POLLWRBAND 0x0100
#endif
```

```
#define INFTIM (-1) /* not standard */
```

```
int poll(struct pollfd *, nfds_t, int);
#endif /* !_COMPAT_POLL_H_ */
#endif /* !HAVE_POLL_H */
```

1.75 bouncycastle-fips 1.51

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1.82 jackson-dataformat-yaml 2.9.5

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Jackson JSON processor

Jackson is a high-performance, Free/Open Source JSON processing library. It was originally written by Tatu Saloranta (tatu.saloranta@iki.fi), and has been in development since 2007.
It is currently developed by a community of developers, as well as supported commercially by FasterXML.com.

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1.83 jetty-servlets 9.2.1.v20140609

1.84 pcre 8.41

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1.85 regexp 2.7.2

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1.87 jackson-databind 1.0.1

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1.88 tomcat-coyote 8.5.32

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1.92 berkeley-db 5.3.28

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<head>
```

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<title>Berkeley DB: Berkeley DB Product License</title>
```

```
<meta name="description" content="Berkeley DB: An embedded database programmatic toolkit.">
```

```
<meta name="keywords"
```

```
content="embedded,database,programmatic,toolkit,btree,hash,hashing,transaction,transactions,locking,logging,access method,access methods,Java,C,C++">
```

```
</head>
```

```
<body bgcolor=white>
```

```
<p align=center><b>Berkeley DB Product License</b></p>
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```

```
</body>
```

```
</html>
```

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1.93 bzip2 1.0.4

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Julian Seward, Cambridge, UK.
jseward@bzip.org
bzip2/libbzip2 version 1.0.4 of 20 December 2006

1.94 libjpeg 8b

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1.94.2 Available under license :

The Independent JPEG Group's JPEG software

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README for release 9c of 14-Jan-2018

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DOCUMENTATION ROADMAP

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This file contains the following sections:

OVERVIEW General description of JPEG and the IJG software.
LEGAL ISSUES Copyright, lack of warranty, terms of distribution.
REFERENCES Where to learn more about JPEG.
ARCHIVE LOCATIONS Where to find newer versions of this software.
ACKNOWLEDGMENTS Special thanks.
FILE FORMAT WARS Software *not* to get.
TO DO Plans for future IJG releases.

Other documentation files in the distribution are:

User documentation:

install.txt How to configure and install the IJG software.
usage.txt Usage instructions for cjpeg, djpeg, jpegtran,
rdjpgcom, and wrjpgcom.
*.1 Unix-style man pages for programs (same info as usage.txt).
wizard.txt Advanced usage instructions for JPEG wizards only.
change.log Version-to-version change highlights.

Programmer and internal documentation:

libjpeg.txt How to use the JPEG library in your own programs.
example.c Sample code for calling the JPEG library.
structure.txt Overview of the JPEG library's internal structure.
filelist.txt Road map of IJG files.
coderrules.txt Coding style rules --- please read if you contribute code.

Please read at least the files install.txt and usage.txt. Some information can also be found in the JPEG FAQ (Frequently Asked Questions) article. See ARCHIVE LOCATIONS below to find out where to obtain the FAQ article.

If you want to understand how the JPEG code works, we suggest reading one or more of the REFERENCES, then looking at the documentation files (in roughly the order listed) before diving into the code.

OVERVIEW

=====

This package contains C software to implement JPEG image encoding, decoding, and transcoding. JPEG (pronounced "jay-peg") is a standardized compression method for full-color and grayscale images.

This software implements JPEG baseline, extended-sequential, and progressive compression processes. Provision is made for supporting all variants of these processes, although some uncommon parameter settings aren't implemented yet. We have made no provision for supporting the hierarchical or lossless processes defined in the standard.

We provide a set of library routines for reading and writing JPEG image files, plus two sample applications "cjpeg" and "djpeg", which use the library to perform conversion between JPEG and some other popular image file formats. The library is intended to be reused in other applications.

In order to support file conversion and viewing software, we have included considerable functionality beyond the bare JPEG coding/decoding capability; for example, the color quantization modules are not strictly part of JPEG decoding, but they are essential for output to colormapped file formats or colormapped displays. These extra functions can be compiled out of the

library if not required for a particular application.

We have also included "jpegtran", a utility for lossless transcoding between different JPEG processes, and "rdjpgcom" and "wrjpgcom", two simple applications for inserting and extracting textual comments in JFIF files.

The emphasis in designing this software has been on achieving portability and flexibility, while also making it fast enough to be useful. In particular, the software is not intended to be read as a tutorial on JPEG. (See the REFERENCES section for introductory material.) Rather, it is intended to be reliable, portable, industrial-strength code. We do not claim to have achieved that goal in every aspect of the software, but we strive for it.

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The Unix configuration script "configure" was produced with GNU Autoconf. It is copyright by the Free Software Foundation but is freely distributable. The same holds for its supporting scripts (config.guess, config.sub, ltmain.sh). Another support script, install-sh, is copyright by X Consortium but is also freely distributable.

The IJG distribution formerly included code to read and write GIF files. To avoid entanglement with the Unisys LZW patent (now expired), GIF reading support has been removed altogether, and the GIF writer has been simplified to produce "uncompressed GIFs". This technique does not use the LZW algorithm; the resulting GIF files are larger than usual, but are readable by all standard GIF decoders.

REFERENCES

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We recommend reading one or more of these references before trying to understand the innards of the JPEG software.

The best short technical introduction to the JPEG compression algorithm is Wallace, Gregory K. "The JPEG Still Picture Compression Standard", Communications of the ACM, April 1991 (vol. 34 no. 4), pp. 30-44. (Adjacent articles in that issue discuss MPEG motion picture compression, applications of JPEG, and related topics.) If you don't have the CACM issue handy, a PDF file containing a revised version of Wallace's article is available at <http://www.ijg.org/files/Wallace.JPEG.pdf>. The file (actually a preprint for an article that appeared in IEEE Trans. Consumer Electronics)

omits the sample images that appeared in CACM, but it includes corrections and some added material. Note: the Wallace article is copyright ACM and IEEE, and it may not be used for commercial purposes.

A somewhat less technical, more leisurely introduction to JPEG can be found in "The Data Compression Book" by Mark Nelson and Jean-loup Gailly, published by M&T Books (New York), 2nd ed. 1996, ISBN 1-55851-434-1. This book provides good explanations and example C code for a multitude of compression methods including JPEG. It is an excellent source if you are comfortable reading C code but don't know much about data compression in general. The book's JPEG sample code is far from industrial-strength, but when you are ready to look at a full implementation, you've got one here...

The best currently available description of JPEG is the textbook "JPEG Still Image Data Compression Standard" by William B. Pennebaker and Joan L. Mitchell, published by Van Nostrand Reinhold, 1993, ISBN 0-442-01272-1. Price US\$59.95, 638 pp. The book includes the complete text of the ISO JPEG standards (DIS 10918-1 and draft DIS 10918-2).

Although this is by far the most detailed and comprehensive exposition of JPEG publicly available, we point out that it is still missing an explanation of the most essential properties and algorithms of the underlying DCT technology.

If you think that you know about DCT-based JPEG after reading this book, then you are in delusion. The real fundamentals and corresponding potential of DCT-based JPEG are not publicly known so far, and that is the reason for all the mistaken developments taking place in the image coding domain.

The original JPEG standard is divided into two parts, Part 1 being the actual specification, while Part 2 covers compliance testing methods. Part 1 is titled "Digital Compression and Coding of Continuous-tone Still Images, Part 1: Requirements and guidelines" and has document numbers ISO/IEC IS 10918-1, ITU-T T.81. Part 2 is titled "Digital Compression and Coding of Continuous-tone Still Images, Part 2: Compliance testing" and has document numbers ISO/IEC IS 10918-2, ITU-T T.83.

IJG JPEG 8 introduced an implementation of the JPEG SmartScale extension which is specified in two documents: A contributed document at ITU and ISO with title "ITU-T JPEG-Plus Proposal for Extending ITU-T T.81 for Advanced Image Coding", April 2006, Geneva, Switzerland. The latest version of this document is Revision 3. And a contributed document ISO/IEC JTC1/SC29/WG1 N 5799 with title "Evolution of JPEG", June/July 2011, Berlin, Germany.

IJG JPEG 9 introduces a reversible color transform for improved lossless compression which is described in a contributed document ISO/IEC JTC1/SC29/WG1 N 6080 with title "JPEG 9 Lossless Coding", June/July 2012, Paris, France.

The JPEG standard does not specify all details of an interchangeable file format. For the omitted details we follow the "JFIF" conventions, version 2. JFIF version 1 has been adopted as Recommendation ITU-T T.871 (05/2011) :

Information technology - Digital compression and coding of continuous-tone still images: JPEG File Interchange Format (JFIF). It is available as a free download in PDF file format from <http://www.itu.int/rec/T-REC-T.871>. A PDF file of the older JFIF document is available at <http://www.w3.org/Graphics/JPEG/jfif3.pdf>.

The TIFF 6.0 file format specification can be obtained by FTP from <ftp://ftp.sgi.com/graphics/tiff/TIFF6.ps.gz>. The JPEG incorporation scheme found in the TIFF 6.0 spec of 3-June-92 has a number of serious problems. IJG does not recommend use of the TIFF 6.0 design (TIFF Compression tag 6). Instead, we recommend the JPEG design proposed by TIFF Technical Note #2 (Compression tag 7). Copies of this Note can be obtained from <http://www.ijg.org/files/>. It is expected that the next revision of the TIFF spec will replace the 6.0 JPEG design with the Note's design. Although IJG's own code does not support TIFF/JPEG, the free libtiff library uses our library to implement TIFF/JPEG per the Note.

ARCHIVE LOCATIONS

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The "official" archive site for this software is www.ijg.org. The most recent released version can always be found there in directory "files". This particular version will be archived as <http://www.ijg.org/files/jpegsr.v9c.tar.gz>, and in Windows-compatible "zip" archive format as <http://www.ijg.org/files/jpegsr9c.zip>.

The JPEG FAQ (Frequently Asked Questions) article is a source of some general information about JPEG. It is available on the World Wide Web at <http://www.faqs.org/faqs/jpeg-faq/> and other news.answers archive sites, including the official news.answers archive at rtfm.mit.edu: <ftp://rtfm.mit.edu/pub/usenet/news.answers/jpeg-faq/>. If you don't have Web or FTP access, send e-mail to mail-server@rtfm.mit.edu with body
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ACKNOWLEDGMENTS

=====

Thank to Juergen Bruder for providing me with a copy of the common DCT algorithm article, only to find out that I had come to the same result in a more direct and comprehensible way with a more generative approach.

Thank to Istvan Sebestyen and Joan L. Mitchell for inviting me to the ITU JPEG (Study Group 16) meeting in Geneva, Switzerland.

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Thank to Lars Goehler, Andreas Heinecke, Sebastian Fuss, Yvonne Roebert, Andrej Werner, and Ulf-Dietrich Braumann for support and public relations.

FILE FORMAT WARS

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The ISO/IEC JTC1/SC29/WG1 standards committee (previously known as JPEG, together with ITU-T SG16) currently promotes different formats containing the name "JPEG" which is misleading because these formats are incompatible with original DCT-based JPEG and are based on faulty technologies. IJG therefore does not and will not support such momentary mistakes (see REFERENCES).

There exist also distributions under the name "OpenJPEG" promoting such kind of formats which is misleading because they don't support original JPEG images.

We have no sympathy for the promotion of inferior formats. Indeed, one of the original reasons for developing this free software was to help force convergence on common, interoperable format standards for JPEG files.

Don't use an incompatible file format!

(In any case, our decoder will remain capable of reading existing JPEG image files indefinitely.)

The ISO committee pretends to be "responsible for the popular JPEG" in their public reports which is not true because they don't respond to actual requirements for the maintenance of the original JPEG specification. Furthermore, the ISO committee pretends to "ensure interoperability" with their standards which is not true because their "standards" support only application-specific and proprietary use cases and contain mathematically incorrect code.

There are currently different distributions in circulation containing the name "libjpeg" which is misleading because they don't have the features and are incompatible with formats supported by actual IJG libjpeg distributions. One of those fakes is released by members of the ISO committee and just uses the name of libjpeg for misdirection of people, similar to the abuse of the name JPEG as described above, while having nothing in common with actual IJG libjpeg distributions and containing mathematically incorrect code. The other one claims to be a "derivative" or "fork" of the original libjpeg, but violates the license conditions as described under LEGAL ISSUES above and violates basic C programming properties. We have no sympathy for the release of misleading, incorrect and illegal distributions derived from obsolete code bases. Don't use an obsolete code base!

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Please send bug reports, offers of help, etc. to jpeg-info@jpegclub.org.

1.95 stax-api 1.0.1

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@c Local Variables:

@c ispell-local-pdict: "ispell-dict"

@c End:

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Version 3, 29 June 2007

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1.98 tcsh 6.20.00

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```
/*
 * config.h -- configure various defines for tcsh
 *
 * All source files should #include this FIRST.
 *
 * Edit this to match your system type.
 */

#ifndef _h_config
#define _h_config
/***** System dependant compilation flags *****/
/*
 * POSIX This system supports IEEE Std 1003.1-1988 (POSIX).
 */
#undef POSIX

/*
 * POSIXJOBS This system supports the optional IEEE Std 1003.1-1988 (POSIX)
 * job control facilities.
 */
#undef POSIXJOBS

/*
 * VFORK This machine has a vfork().
 * It used to be that for job control to work, this define
 * was mandatory. This is not the case any more.
 * If you think you still need it, but you don't have vfork,
 * define this anyway and then do #define vfork fork.
 * I do this anyway on a Sun because of yellow pages brain damage,
 * [should not be needed under 4.1]
 * and on the iris4d cause SGI's fork is sufficiently "virtual"
 * that vfork isn't necessary. (Besides, SGI's vfork is weird).
 * Note that some machines eg. rs6000 have a vfork, but not
 * with the berkeley semantics, so we cannot use it there either.
 */
#define VFORK

/*
 * BSDJOBS You have BSD-style job control (both process groups and
 * a tty that deals correctly
 */
```

```

#define BSDJOBS

/*
 * BSDTIMES You have BSD-style process time stuff (like rusage)
 * This may or may not be true. For example, Apple Unix
 * (OREO) has BSDJOBS but not BSDTIMES.
 */
#define BSDTIMES

/*
 * BSDLIMIT You have BSD-style resource limit stuff (getrlimit/setrlimit)
 */
#define BSDLIMIT

/*
 * TERMIO You have struct termio instead of struct sgtyb.
 * This is usually the case for SYSV systems, where
 * BSD uses sgtyb. POSIX systems should define this
 * anyway, even though they use struct termios.
 */
#undef TERMIO

/*
 * SYSVREL Your machine is SYSV based (HPUX, A/UX)
 * NOTE: don't do this if you are on a Pyramid -- tcsh is
 * built in a BSD universe.
 * Set SYSVREL to 1, 2, 3, or 4, depending the version of System V
 * you are running. Or set it to 0 if you are not SYSV based
 */
#define SYSVREL 0

/*
 * YPBUGS Work around Sun YP bugs that cause expansion of ~username
 * to send command output to /dev/null
 */
#undef YPBUGS

/***** local defines *****/

#endif /* _h_config */
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1.99 net-snmp 5.7.1

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1.100 nghttp 1.31.1

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```
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set terminal pngcairo font 'Sans, 8' lw 1 size 1400,1024
set xtics rotate by -45
set style histogram errorbars gap 2 lw 1
set style fill solid border -1
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```

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1.101 jackson-jaxrs-base 2.9.5

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1.102 gdb 6.1.1

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To protect your rights, we need to make restrictions that forbid anyone to deny you these rights or to ask you to surrender the rights. These restrictions translate to certain responsibilities for you if you distribute copies of the library, or if you modify it.

For example, if you distribute copies of the library, whether gratis or for a fee, you must give the recipients all the rights that we gave you. You must make sure that they, too, receive or can get the source code. If you link a program with the library, you must provide complete object files to the recipients so that they can relink them with the library, after making changes to the library and recompiling it. And you must show them these terms so they know their rights.

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Also, for each distributor's protection, we want to make certain that everyone understands that there is no warranty for this free library. If the library is modified by someone else and passed on, we want its recipients to know that what they have is not the original version, so that any problems introduced by others will not reflect on the original authors' reputations.

Finally, any free program is threatened constantly by software patents. We wish to avoid the danger that companies distributing free software will individually obtain patent licenses, thus in effect transforming the program into proprietary software. To prevent this, we have made it clear that any patent must be licensed for everyone's free use or not licensed at all.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License, which was designed for utility programs. This license, the GNU Library General Public License, applies to certain designated libraries. This license is quite different from the ordinary one; be sure to read it in full, and don't assume that anything in it is the same as in the ordinary license.

The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in

a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

1. You may copy and distribute verbatim copies of the Library's complete source code as you receive it, in any medium, provided that you conspicuously and appropriately publish on each copy an appropriate copyright notice and disclaimer of warranty; keep intact all the notices that refer to this License and to the absence of any warranty; and distribute a copy of this License along with the Library.

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2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

- a) The modified work must itself be a software library.
- b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.
- c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.
- d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which

must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the

Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

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- b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.
- c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.
- d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library

facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

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END OF TERMS AND CONDITIONS

Appendix: How to Apply These Terms to Your New Libraries

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<one line to give the library's name and a brief idea of what it does.>

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Finally, software patents pose a constant threat to the existence of any free program. We wish to make sure that a company cannot effectively restrict the users of a free program by obtaining a restrictive license from a patent holder. Therefore, we insist that any patent license obtained for a version of the library must be consistent with the full freedom of use specified in this license.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License. This license, the GNU Lesser General Public License, applies to certain designated libraries, and is quite different from the ordinary General Public License. We use this license for certain libraries in order to permit linking those libraries into non-free programs.

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We call this license the "Lesser" General Public License because it does Less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, whereas the latter must be combined with the library in order to run.

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

- a) The modified work must itself be a software library.
- b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.
- c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.
- d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in

themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

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d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

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/* Native-dependent code for modern AMD64 BSD's.

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```
#ifndef AMD64_BSD_NAT_H  
#define AMD64_BSD_NAT_H
```

```
#include "x86-bsd-nat.h"
```

```
/* Helper functions. See definitions. */  
extern void amd64bsd_fetch_inferior_registers (struct regcache *regcache,
```

```

    int regnum);
extern void amd64bsd_store_inferior_registers (struct regcache *regcache,
    int regnum);

/* A prototype *BSD/AMD64 target. */

template<typename BaseTarget>
class amd64_bsd_nat_target : public x86bsd_nat_target<BaseTarget>
{
public:
    void fetch_registers (struct regcache *regcache, int regnum) override
    { amd64bsd_fetch_inferior_registers (regcache, regnum); }

    void store_registers (struct regcache *regcache, int regnum) override
    { amd64bsd_store_inferior_registers (regcache, regnum); }
};

#endif /* i386-bsd-nat.h */

```

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```
<one line to give the program's name and a brief idea of what it does.>  
Copyright (C) <year> <name of author>
```

```
This program is free software: you can redistribute it and/or modify  
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the Free Software Foundation, either version 3 of the License, or  
(at your option) any later version.
```

```
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MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the  
GNU General Public License for more details.
```

```
You should have received a copy of the GNU General Public License  
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```

Also add information on how to contact you by electronic and paper mail.

If the program does terminal interaction, make it output a short notice like this when it starts in an interactive mode:

<program> Copyright (C) <year> <name of author>

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Version 3, 29 June 2007

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```
#!/usr/bin/env python
```

```
# Copyright (C) 2011-2019 Free Software Foundation, Inc.
```

```
#
```

```
# This file is part of GDB.
```

```
#
```

```
# This program is free software; you can redistribute it and/or modify
```

```

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```

```

"""copyright.py

```

This script updates the list of years in the copyright notices in most files maintained by the GDB project.

Usage: cd src/gdb && python copyright.py

Always review the output of this script before committing it!

A useful command to review the output is:

```

% filterdiff -x \*.c -x \*.cc -x \*.h -x \*.exp updates.diff

```

This removes the bulk of the changes which are most likely to be correct.

```

"""

```

```

import datetime
import os
import os.path
import subprocess

```

```

def get_update_list():

```

```

    """Return the list of files to update.

```

```

    Assumes that the current working directory when called is the root
    of the GDB source tree (NOT the gdb/ subdirectory!). The names of
    the files are relative to that root directory.

```

```

    """

```

```

    result = []
    for gdb_dir in ('gdb', 'sim', 'include/gdb'):
        for root, dirs, files in os.walk(gdb_dir, topdown=True):
            for dirname in dirs:
                reldirname = "%s/%s" % (root, dirname)
                if (dirname in EXCLUDE_ALL_LIST
                    or reldirname in EXCLUDE_LIST
                    or reldirname in NOT_FSF_LIST
                    or reldirname in BY_HAND):
                    # Prune this directory from our search list.

```

```

        dirs.remove(dirname)
    for filename in files:
        relpath = "%s/%s" % (root, filename)
        if (filename in EXCLUDE_ALL_LIST
            or relpath in EXCLUDE_LIST
            or relpath in NOT_FSF_LIST
            or relpath in BY_HAND):
            # Ignore this file.
            pass
        else:
            result.append(relpath)
    return result

def update_files(update_list):
    """Update the copyright header of the files in the given list.

    We use gnulib's update-copyright script for that.
    """
    # We want to use year intervals in the copyright notices, and
    # all years should be collapsed to one single year interval,
    # even if there are "holes" in the list of years found in the
    # original copyright notice (OK'ed by the FSF, case [gnu.org #719834]).
    os.environ['UPDATE_COPYRIGHT_USE_INTERVALS'] = '2'

    # Perform the update, and save the output in a string.
    update_cmd = ['bash', 'gdb/gnulib/import/extra/update-copyright']
    update_cmd += update_list

    p = subprocess.Popen(update_cmd, stdout=subprocess.PIPE,
                          stderr=subprocess.STDOUT)
    update_out = p.communicate()[0]

    # Process the output. Typically, a lot of files do not have
    # a copyright notice :-(. The update-copyright script prints
    # a well defined warning when it did not find the copyright notice.
    # For each of those, do a sanity check and see if they may in fact
    # have one. For the files that are found not to have one, we filter
    # the line out from the output, since there is nothing more to do,
    # short of looking at each file and seeing which notice is appropriate.
    # Too much work! (~4,000 files listed as of 2012-01-03).
    update_out = update_out.splitlines()
    warning_string = ': warning: copyright statement not found'
    warning_len = len(warning_string)

    for line in update_out:
        if line.endswith('\n'):
            line = line[:-1]

```

```

if line.endswith(warning_string):
    filename = line[:-warning_len]
    if may_have_copyright_notice(filename):
        print line
else:
    # Unrecognized file format. !?!
    print "*** " + line

```

```

def may_have_copyright_notice(filename):
    """Check that the given file does not seem to have a copyright notice.

```

The filename is relative to the root directory.
This function assumes that the current working directory is that root directory.

The algorithm is fairly crude, meaning that it might return some false positives. I do not think it will return any false negatives... We might improve this function to handle more complex cases later...

```

"""
# For now, it may have a copyright notice if we find the word
# "Copyright" at the (reasonable) start of the given file, say
# 50 lines...
MAX_LINES = 50

```

```

fd = open(filename)

```

```

lineno = 1
for line in fd:
    if 'Copyright' in line:
        return True
    lineno += 1
    if lineno > 50:
        return False
return False

```

```

def main ():
    """The main subprogram."""
    if not os.path.isfile("gnulib/import/extra/update-copyright"):
        print "Error: This script must be called from the gdb directory."
    root_dir = os.path.dirname(os.getcwd())
    os.chdir(root_dir)

    update_list = get_update_list()
    update_files (update_list)

```

```

# Remind the user that some files need to be updated by HAND...

if MULTIPLE_COPYRIGHT_HEADERS:
    print
    print("\033[31m"
          "REMINDER: Multiple copyright headers must be updated by hand:"
          "\033[0m")
    for filename in MULTIPLE_COPYRIGHT_HEADERS:
        print " ", filename

if BY_HAND:
    print
    print "\033[31mREMINDER: The following files must be updated by hand." \
          "\033[0m"
    for filename in BY_HAND:
        print " ", filename

#####
#
# Some constants, placed at the end because they take up a lot of room.
# The actual value of these constants is not significant to the understanding
# of the script.
#
#####

# Files which should not be modified, either because they are
# generated, non-FSF, or otherwise special (e.g. license text,
# or test cases which must be sensitive to line numbering).
#
# Filenames are relative to the root directory.
EXCLUDE_LIST = (
    'gdb/nat/glibc_thread_db.h',
    'gdb/CONTRIBUTE',
    'gdb/gnulib/import'
)

# Files which should not be modified, either because they are
# generated, non-FSF, or otherwise special (e.g. license text,
# or test cases which must be sensitive to line numbering).
#
# Matches any file or directory name anywhere. Use with caution.
# This is mostly for files that can be found in multiple directories.
# Eg: We want all files named COPYING to be left untouched.

EXCLUDE_ALL_LIST = (
    "COPYING", "COPYING.LIB", "CVS", "configure", "copying.c",
    "fdl.texi", "gpl.texi", "aclocal.m4",
)

```

```

# The list of files to update by hand.
BY_HAND = (
    # Nothing at the moment :-).
)

# Files containing multiple copyright headers. This script is only
# fixing the first one it finds, so we need to finish the update
# by hand.
MULTIPLE_COPYRIGHT_HEADERS = (
    "gdb/doc/gdb.texinfo",
    "gdb/doc/refcard.tex",
    "gdb/gdbarch.sh",
)

# The list of file which have a copyright, but not head by the FSF.
# Filenames are relative to the root directory.
NOT_FSF_LIST = (
    "gdb/exc_request.defs",
    "gdb/gdbtk",
    "gdb/testsuite/gdb.gdbtk/",
    "sim/arm/armemu.h", "sim/arm/armos.c", "sim/arm/gdbhost.c",
    "sim/arm/dbg_hif.h", "sim/arm/dbg_conf.h", "sim/arm/communicate.h",
    "sim/arm/armos.h", "sim/arm/armcopro.c", "sim/arm/armemu.c",
    "sim/arm/kid.c", "sim/arm/thumbemu.c", "sim/arm/armdefs.h",
    "sim/arm/armopts.h", "sim/arm/dbg_cp.h", "sim/arm/dbg_rdi.h",
    "sim/arm/parent.c", "sim/arm/armsupp.c", "sim/arm/armrdi.c",
    "sim/arm/bag.c", "sim/arm/armvirt.c", "sim/arm/main.c", "sim/arm/bag.h",
    "sim/arm/communicate.c", "sim/arm/gdbhost.h", "sim/arm/armfpe.h",
    "sim/arm/arminit.c",
    "sim/common/cgen-fpu.c", "sim/common/cgen-fpu.h",
    "sim/common/cgen-accfc.c",
    "sim/mips/m16run.c", "sim/mips/sim-main.c",
    "sim/moxie/moxie-gdb.dts",
    # Not a single file in sim/ppc/ appears to be copyright FSF :-(.
    "sim/ppc/filter.h", "sim/ppc/gen-support.h", "sim/ppc/ld-insn.h",
    "sim/ppc/hw_sem.c", "sim/ppc/hw_disk.c", "sim/ppc/idecode_branch.h",
    "sim/ppc/sim-endian.h", "sim/ppc/table.c", "sim/ppc/hw_core.c",
    "sim/ppc/gen-support.c", "sim/ppc/gen-semantics.h", "sim/ppc/cpu.h",
    "sim/ppc/sim_callbacks.h", "sim/ppc/RUN", "sim/ppc/Makefile.in",
    "sim/ppc/emul_chirp.c", "sim/ppc/hw_nvram.c", "sim/ppc/dc-test.01",
    "sim/ppc/hw_phb.c", "sim/ppc/hw_eeprom.c", "sim/ppc/bits.h",
    "sim/ppc/hw_vm.c", "sim/ppc/cap.h", "sim/ppc/os_emul.h",
    "sim/ppc/options.h", "sim/ppc/gen-idecode.c", "sim/ppc/filter.c",
    "sim/ppc/corefile-n.h", "sim/ppc/std-config.h", "sim/ppc/ld-decode.h",
    "sim/ppc/filter_filename.h", "sim/ppc/hw_shm.c",
    "sim/ppc/pk_disklabel.c", "sim/ppc/dc-simple", "sim/ppc/misc.h",
    "sim/ppc/device_table.h", "sim/ppc/ld-insn.c", "sim/ppc/inline.c",

```



```

"sim/ppc/emul_bugapi.h", "sim/ppc/hw_cpu.h", "sim/ppc/debug.h",
"sim/ppc/hw_ide.c", "sim/ppc/debug.c", "sim/ppc/gen-itable.h",
"sim/ppc/interrupts.c", "sim/ppc/hw_glue.c", "sim/ppc/emul_unix.c",
"sim/ppc/sim_calls.c", "sim/ppc/dc-complex", "sim/ppc/ld-cache.c",
"sim/ppc/registers.h", "sim/ppc/dc-test.02", "sim/ppc/options.c",
"sim/ppc/igen.h", "sim/ppc/registers.c", "sim/ppc/device.h",
"sim/ppc/emul_chirp.h", "sim/ppc/hw_register.c", "sim/ppc/hw_init.c",
"sim/ppc/sim-endian-n.h", "sim/ppc/filter_filename.c",
"sim/ppc/bits.c", "sim/ppc/idecode_fields.h", "sim/ppc/hw_memory.c",
"sim/ppc/misc.c", "sim/ppc/double.c", "sim/ppc/psim.h",
"sim/ppc/hw_trace.c", "sim/ppc/emul_netbsd.h", "sim/ppc/psim.c",
"sim/ppc/ppc-instructions", "sim/ppc/tree.h", "sim/ppc/README",
"sim/ppc/gen-icache.h", "sim/ppc/gen-model.h", "sim/ppc/ld-cache.h",
"sim/ppc/mon.c", "sim/ppc/corefile.h", "sim/ppc/vm.c",
"sim/ppc/INSTALL", "sim/ppc/gen-model.c", "sim/ppc/hw_cpu.c",
"sim/ppc/corefile.c", "sim/ppc/hw_opic.c", "sim/ppc/gen-icache.c",
"sim/ppc/events.h", "sim/ppc/os_emul.c", "sim/ppc/emul_generic.c",
"sim/ppc/main.c", "sim/ppc/hw_com.c", "sim/ppc/gen-semantics.c",
"sim/ppc/emul_bugapi.c", "sim/ppc/device.c", "sim/ppc/emul_generic.h",
"sim/ppc/tree.c", "sim/ppc/mon.h", "sim/ppc/interrupts.h",
"sim/ppc/cap.c", "sim/ppc/cpu.c", "sim/ppc/hw_phb.h",
"sim/ppc/device_table.c", "sim/ppc/lf.c", "sim/ppc/lf.c",
"sim/ppc/dc-stupid", "sim/ppc/hw_pal.c", "sim/ppc/ppc-spr-table",
"sim/ppc/emul_unix.h", "sim/ppc/words.h", "sim/ppc/basics.h",
"sim/ppc/hw_htab.c", "sim/ppc/lf.h", "sim/ppc/ld-decode.c",
"sim/ppc/sim-endian.c", "sim/ppc/gen-itable.c",
"sim/ppc/idecode_expression.h", "sim/ppc/table.h", "sim/ppc/dgen.c",
"sim/ppc/events.c", "sim/ppc/gen-idecode.h", "sim/ppc/emul_netbsd.c",
"sim/ppc/igen.c", "sim/ppc/vm_n.h", "sim/ppc/vm.h",
"sim/ppc/hw_iobus.c", "sim/ppc/inline.h",
"sim/testsuite/sim/bfin/s21.s", "sim/testsuite/sim/mips/mips32-dsp2.s",
)

```

```

if __name__ == "__main__":

```

```

    main()

```

```

/* Native-dependent code for modern i386 BSD's.

```

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```
#ifndef I386_BSD_NAT_H
#define I386_BSD_NAT_H

/* Helper functions. See definitions. */
extern void i386bsd_fetch_inferior_registers (struct regcache *regcache,
      int regnum);
extern void i386bsd_store_inferior_registers (struct regcache *regcache,
      int regnum);

/* A prototype *BSD/i386 target. */

template<typename BaseTarget>
class i386_bsd_nat_target : public x86bsd_nat_target<BaseTarget>
{
public:
void fetch_registers (struct regcache *regcache, int regnum) override
{ i386bsd_fetch_inferior_registers (regcache, regnum); }

void store_registers (struct regcache *regcache, int regnum) override
{ i386bsd_store_inferior_registers (regcache, regnum); }
};

#endif /* i386-bsd-nat.h */
/* Native-dependent code for x86 BSD's.
```

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```

#ifndef X86_BSD_NAT_H
#define X86_BSD_NAT_H

#include "x86-nat.h"

/* Low level x86 XSAVE info. */
extern size_t x86bsd_xsave_len;

/* A prototype *BSD/x86 target. */

template<typename BaseTarget>
class x86bsd_nat_target : public x86_nat_target<BaseTarget>
{
    using base_class = x86_nat_target<BaseTarget>;
public:
#ifdef HAVE_PT_GETDBREGS
    void mourn_inferior () override
    {
        x86_cleanup_dregs ();
        base_class::mourn_inferior ();
    }
#endif /* HAVE_PT_GETDBREGS */
};

#endif /* x86-bsd-nat.h */
/* BSD Kernel Data Access Library (libkvm) interface.

```

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```

#define _KMEMUSER
#include "defs.h"
#include "cli/cli-cmds.h"
#include "command.h"

```

```

#include "frame.h"
#include "regcache.h"
#include "target.h"
#include "process-stratum-target.h"
#include "value.h"
#include "gdbcore.h"
#include "inferior.h"    /* for get_exec_file */
#include "gdbthread.h"

#include <fcntl.h>
#include <kvm.h>
#ifdef HAVE_NLIST_H
#include <nlist.h>
#endif
#include <paths.h>
#include "readline/readline.h"
#include <sys/param.h>
#include <sys/proc.h>
#ifdef HAVE_SYS_USER_H
#include <sys/user.h>
#endif

#include "bsd-kvm.h"

/* Kernel memory device file. */
static const char *bsd_kvm_corefile;

/* Kernel memory interface descriptor. */
static kvm_t *core_kd;

/* Address of process control block. */
static struct pcb *bsd_kvm_paddr;

/* Pointer to architecture-specific function that reconstructs the
   register state from PCB and supplies it to REGCACHE. */
static int (*bsd_kvm_supply_pcb)(struct regcache *regcache, struct pcb *pcb);

/* This is the ptid we use while we're connected to kvm. The kvm
   target currently doesn't export any view of the running processes,
   so this represents the kernel task. */
static ptid_t bsd_kvm_ptid;

/* The libkvm target. */

static const target_info bsd_kvm_target_info = {
    "kvm",
    N_("Kernel memory interface"),
    N_("Use a kernel virtual memory image as a target.\n")

```

```

Optionally specify the filename of a core dump.")
};

class bsd_kvm_target final : public process_stratum_target
{
public:
    bsd_kvm_target () = default;

    const target_info &info () const override
    { return bsd_kvm_target_info; }

    void close () override;

    void fetch_registers (struct regcache *, int) override;
    enum target_xfer_status xfer_partial (enum target_object object,
        const char *annex,
        gdb_byte *readbuf,
        const gdb_byte *writebuf,
        ULONGEST offset, ULONGEST len,
        ULONGEST *xfered_len) override;

    void files_info () override;
    bool thread_alive (ptid_t ptid) override;
    const char *pid_to_str (ptid_t) override;

    bool has_memory () override { return true; }
    bool has_stack () override { return true; }
    bool has_registers () override { return true; }
};

/* Target ops for libkvm interface. */
static bsd_kvm_target bsd_kvm_ops;

static void
bsd_kvm_target_open (const char *arg, int from_tty)
{
    char errbuf[_POSIX2_LINE_MAX];
    char *execfile = NULL;
    kvm_t *temp_kd;
    char *filename = NULL;

    target_preopen (from_tty);

    if (arg)
        {
            char *temp;

            filename = tilde_expand (arg);

```

```

    if (filename[0] != '/')
    {
        temp = concat (current_directory, "/", filename, (char *)NULL);
        xfree (filename);
        filename = temp;
    }
}

execfile = get_exec_file (0);
temp_kd = kvm_openfiles (execfile, filename, NULL,
    write_files ? O_RDWR : O_RDONLY, errbuf);
if (temp_kd == NULL)
    error ("%s", errbuf);

bsd_kvm_corefile = filename;
unpush_target (&bsd_kvm_ops);
core_kd = temp_kd;
push_target (&bsd_kvm_ops);

add_thread_silent (bsd_kvm_ptid);
inferior_ptid = bsd_kvm_ptid;

target_fetch_registers (get_current_regcache (), -1);

reinit_frame_cache ();
print_stack_frame (get_selected_frame (NULL), 0, SRC_AND_LOC, 1);
}

void
bsd_kvm_target::close ()
{
    if (core_kd)
    {
        if (kvm_close (core_kd) == -1)
            warning ("%s", kvm_geterr(core_kd));
        core_kd = NULL;
    }

    inferior_ptid = null_ptid;
    discard_all_inferiors ();
}

static LONGEST
bsd_kvm_xfer_memory (CORE_ADDR addr, ULONGEST len,
    gdb_byte *readbuf, const gdb_byte *writebuf)
{
    ssize_t nbytes = len;

```

```

if (readbuf)
    nbytes = kvm_read (core_kd, addr, readbuf, nbytes);
if (writebuf && nbytes > 0)
    nbytes = kvm_write (core_kd, addr, writebuf, nbytes);
return nbytes;
}

enum target_xfer_status
bsd_kvm_target::xfer_partial (enum target_object object,
    const char *annex, gdb_byte *readbuf,
    const gdb_byte *writebuf,
    ULONGEST offset, ULONGEST len, ULONGEST *xfered_len)
{
switch (object)
    {
case TARGET_OBJECT_MEMORY:
    {
LONGEST ret = bsd_kvm_xfer_memory (offset, len, readbuf, writebuf);

if (ret < 0)
    return TARGET_XFER_E_IO;
else if (ret == 0)
    return TARGET_XFER_EOF;
else
    {
*xfered_len = (ULONGEST) ret;
return TARGET_XFER_OK;
    }
    }

default:
    return TARGET_XFER_E_IO;
    }
}

void
bsd_kvm_target::files_info ()
{
if (bsd_kvm_corefile && strcmp (bsd_kvm_corefile, _PATH_MEM) != 0)
    printf_filtered (_("\tUsing the kernel crash dump %s.\n"),
        bsd_kvm_corefile);
else
    printf_filtered (_("\tUsing the currently running kernel.\n"));
}

/* Fetch process control block at address PADDR. */

static int

```

```

bsd_kvm_fetch_pcb (struct regcache *regcache, struct pcb *paddr)
{
    struct pcb pcb;

    if (kvm_read (core_kd, (unsigned long) paddr, &pcb, sizeof pcb) == -1)
        error ("%s", kvm_geterr (core_kd));

    gdb_assert (bsd_kvm_supply_pcb);
    return bsd_kvm_supply_pcb (regcache, &pcb);
}

void
bsd_kvm_target::fetch_registers (struct regcache *regcache, int regnum)
{
    struct nlist nl[2];

    if (bsd_kvm_paddr)
    {
        bsd_kvm_fetch_pcb (regcache, bsd_kvm_paddr);
        return;
    }

    /* On dumping core, BSD kernels store the faulting context (PCB)
       in the variable "dumppcb". */
    memset (nl, 0, sizeof nl);
    nl[0].n_name = "_dumppcb";

    if (kvm_nlist (core_kd, nl) == -1)
        error ("%s", kvm_geterr (core_kd));

    if (nl[0].n_value != 0)
    {
        /* Found dumppcb. If it contains a valid context, return
           immediately. */
        if (bsd_kvm_fetch_pcb (regcache, (struct pcb *) nl[0].n_value))
            return;
    }

    /* Traditional BSD kernels have a process proc0 that should always
       be present. The address of proc0's PCB is stored in the variable
       "proc0paddr". */

    memset (nl, 0, sizeof nl);
    nl[0].n_name = "_proc0paddr";

    if (kvm_nlist (core_kd, nl) == -1)
        error ("%s", kvm_geterr (core_kd));
}

```



```

if (nl[0].n_value != 0)
{
    struct pcb *paddr;

    /* Found proc0paddr. */
    if (kvm_read (core_kd, nl[0].n_value, &paddr, sizeof paddr) == -1)
error ("%s"), kvm_geterr (core_kd));

    bsd_kvm_fetch_pcb (regcache, paddr);
    return;
}

#ifdef HAVE_STRUCT_THREAD_TD_PCB
/* In FreeBSD kernels for 5.0-RELEASE and later, the PCB no longer
lives in `struct proc' but in `struct thread'. The `struct
thread' for the initial thread for proc0 can be found in the
variable "thread0". */

memset (nl, 0, sizeof nl);
nl[0].n_name = "_thread0";

if (kvm_nlist (core_kd, nl) == -1)
    error ("%s"), kvm_geterr (core_kd));

if (nl[0].n_value != 0)
{
    struct pcb *paddr;

    /* Found thread0. */
    nl[0].n_value += offsetof (struct thread, td_pcb);
    if (kvm_read (core_kd, nl[0].n_value, &paddr, sizeof paddr) == -1)
error ("%s"), kvm_geterr (core_kd));

    bsd_kvm_fetch_pcb (regcache, paddr);
    return;
}
#endif

/* i18n: PCB == "Process Control Block". */
error (_("Cannot find a valid PCB"));
}

/* Kernel memory interface commands. */
struct cmd_list_element *bsd_kvm_cmdlist;

static void
bsd_kvm_cmd (const char *arg, int fromtty)

```

```

{
/* ??? Should this become an alias for "target kvm"? */
}

#ifndef HAVE_STRUCT_THREAD_TD_PCB

static void
bsd_kvm_proc_cmd (const char *arg, int fromtty)
{
CORE_ADDR addr;

if (arg == NULL)
error_no_arg (_("proc address"));

if (core_kd == NULL)
error (_("No kernel memory image.));

addr = parse_and_eval_address (arg);
#ifdef HAVE_STRUCT_LWP
addr += offsetof (struct lwp, l_addr);
#else
addr += offsetof (struct proc, p_addr);
#endif

if (kvm_read (core_kd, addr, &bsd_kvm_paddr, sizeof bsd_kvm_paddr) == -1)
error ((("%s"), kvm_geterr (core_kd));

target_fetch_registers (get_current_regcache (), -1);

reinit_frame_cache ();
print_stack_frame (get_selected_frame (NULL), 0, SRC_AND_LOC, 1);
}

#endif

static void
bsd_kvm_pcb_cmd (const char *arg, int fromtty)
{
if (arg == NULL)
/* i18n: PCB == "Process Control Block". */
error_no_arg (_("pcb address"));

if (core_kd == NULL)
error (_("No kernel memory image.));

bsd_kvm_paddr = (struct pcb *) (u_long) parse_and_eval_address (arg);

target_fetch_registers (get_current_regcache (), -1);

```

```

reinit_frame_cache ();
print_stack_frame (get_selected_frame (NULL), 0, SRC_AND_LOC, 1);
}

```

```

bool
bsd_kvm_target::thread_alive (ptid_t ptid)
{
return true;
}

```

```

const char *
bsd_kvm_target::pid_to_str (ptid_t ptid)
{
static char buf[64];
xsnprintf (buf, sizeof buf, "<kvm>");
return buf;
}

```

```

/* Add the libkvm interface to the list of all possible targets and
register CUPPLY_PCB as the architecture-specific process control
block interpreter. */

```

```

void
bsd_kvm_add_target (int (*supply_pcb)(struct regcache *, struct pcb *))
{
gdb_assert (bsd_kvm_supply_pcb == NULL);
bsd_kvm_supply_pcb = supply_pcb;

```

```

add_target (bsd_kvm_target_info, bsd_kvm_target_open);

```

```

add_prefix_cmd ("kvm", class_obscure, bsd_kvm_cmd, _("\"
Generic command for manipulating the kernel memory interface.\"),
&bsd_kvm_cmdlist, "kvm ", 0, &cmdlist);

```

```

#ifdef HAVE_STRUCT_THREAD_TD_PCB
add_cmd ("proc", class_obscure, bsd_kvm_proc_cmd,
_("Set current context from proc address"), &bsd_kvm_cmdlist);
#endif

```

```

add_cmd ("pcb", class_obscure, bsd_kvm_pcb_cmd,
/* i18n: PCB == "Process Control Block". */
_("Set current context from pcb address"), &bsd_kvm_cmdlist);

```

```

/* Some notes on the ptid usage on this target.

```

The pid field represents the kvm inferior instance. Currently, we don't support multiple kvm inferiors, but we start at 1 anyway. The lwp field is set to != 0, in case the core wants to

refer to the whole kvm inferior with ptid(1,0,0).

If kvm is made to export running processes as gdb threads,
the following form can be used:

ptid (1, 1, 0) -> kvm inferior 1, in kernel

ptid (1, 1, 1) -> kvm inferior 1, process 1

ptid (1, 1, 2) -> kvm inferior 1, process 2

ptid (1, 1, n) -> kvm inferior 1, process n */

```
bsd_kvm_ptid = ptid_t (1, 1, 0);  
}
```

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```
BEGIN {
  FS="\"";
  print "/* ==> Do not modify this file!! \" \
  \"-*- buffer-read-only: t -*- vi\" \
  \":set ro:\";
  print \" It is created automatically by copying.awk.\";
  print \" Modify copying.awk instead. <== */\";
  print \"\"
  print \"#include \\\"defs.h\\\"\"
  print \"#include \\\"command.h\\\"\"
  print \"#include \\\"gdbcmd.h\\\"\"
  print \"\"
  print \"static void show_copying_command (const char *, int);\"
  print \"\"
  print \"static void show_warranty_command (const char *, int);\"
  print \"\"
  print \"static void\";
  print \"show_copying_command (const char *ignore, int from_tty)\";
  print \"{\";
}
NR == 1,/^[ ]*15\\. Disclaimer of Warranty\\. [ ]*$/ {
  if ($0 ~ //)
  {
    printf \" printf_filtered (\\\"\\n\\\");\\n\";
  }
  else if ($0 !~ /^[ ]*15\\. Disclaimer of Warranty\\. [ ]*$/ )
  {
    printf \" printf_filtered (\\\"\";
    for (i = 1; i < NF; i++)
    printf \"%s\\\"\", $i;
    printf \"%s\\n\\\");\\n\", $NF;
  }
}
```

```

/^[ ]*15\. Disclaimer of Warranty\.[ ]*$/ {
    print "}";
    print "";
    print "static void";
    print "show_warranty_command (const char *ignore, int from_tty)";
    print "{";
}
/^[ ]*15\. Disclaimer of Warranty\.[ ]*$/,/^[ ]*END OF TERMS AND CONDITIONS[ ]*$/ {
    if (! ($0 ~ /^[ ]*END OF TERMS AND CONDITIONS[ ]*$/))
    {
        printf " printf_filtered (\\"";
        for (i = 1; i < NF; i++)
        printf "%s\\\\" , $i;
        printf "%s\\n\\n";\n", $NF;
    }
}
END {
    print "}";
    print "";
    print "void"
    print "_initialize_copying (void)";
    print "{";
    print " add_cmd (\\"copying\\", no_set_class, show_copying_command, ";
    print " _(\\"Conditions for redistributing copies of GDB.\\"), ";
    print " &showlist);";
    print " add_cmd (\\"warranty\\", no_set_class, show_warranty_command, ";
    print " _(\\"Various kinds of warranty you do not have.\\"), ";
    print " &showlist);";
    print "";
    print " /* For old-timers, allow \\"info copying\\", etc. */";
    print " add_info (\\"copying\\", show_copying_command, ";
    print " _(\\"Conditions for redistributing copies of GDB.\\"));";
    print " add_info (\\"warranty\\", show_warranty_command, ";
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    print "}";
}
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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

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- c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.
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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in

themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the

Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

- a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under

Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

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END OF TERMS AND CONDITIONS

Appendix: How to Apply These Terms to Your New Libraries

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To apply these terms, attach the following notices to the library. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

```
<one line to give the library's name and a brief idea of what it does.>  
Copyright (C) <year> <name of author>
```

This library is free software; you can redistribute it and/or modify it under the terms of the GNU Library General Public License as published by the Free Software Foundation; either version 2 of the License, or (at your option) any later version.

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Also add information on how to contact you by electronic and paper mail.

You should also get your employer (if you work as a programmer) or your school, if any, to sign a "copyright disclaimer" for the library, if necessary. Here is a sample; alter the names:

Yoyodyne, Inc., hereby disclaims all copyright interest in the library `Frob' (a library for tweaking knobs) written by James Random Hacker.

<signature of Ty Coon>, 1 April 1990
Ty Coon, President of Vice

That's all there is to it!

@ignore

@c Set file name and title for man page.

@setfilename gpl

@settitle GNU General Public License

@c man begin SEEALSO

gfdl(7), fsf-funding(7).

@c man end

@c man begin COPYRIGHT

Copyright @copyright{ } 2007 Free Software Foundation, Inc. @url{http://fsf.org/}

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@c man end

@end ignore

@node Copying

@c man begin DESCRIPTION

@appendix GNU GENERAL PUBLIC LICENSE

@c The GNU General Public License.

@center Version 3, 29 June 2007

@c This file is intended to be included within another document,

@c hence no sectioning command or @node.

@display

Copyright @copyright{ } 2007 Free Software Foundation, Inc. @url{http://fsf.org/}

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@heading Preamble

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For example, if you distribute copies of such a program, whether gratis or for a fee, you must pass on to the recipients the same freedoms that you received. You must make sure that they, too, receive or can get the source code. And you must show them these terms so they know their rights.

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For the developers' and authors' protection, the GPL clearly explains that there is no warranty for this free software. For both users' and authors' sake, the GPL requires that modified versions be marked as changed, so that their problems will not be attributed erroneously to authors of previous versions.

Some devices are designed to deny users access to install or run modified versions of the software inside them, although the manufacturer can do so. This is fundamentally incompatible with the aim of protecting users' freedom to change the software. The systematic pattern of such abuse occurs in the area of products for individuals to use, which is precisely where it is most unacceptable. Therefore, we have designed this version of the GPL to prohibit the practice for those products. If such problems arise substantially in other domains, we stand ready to extend this provision to those

domains in future versions of the GPL, as needed to protect the freedom of users.

Finally, every program is threatened constantly by software patents. States should not allow patents to restrict development and use of software on general-purpose computers, but in those that do, we wish to avoid the special danger that patents applied to a free program could make it effectively proprietary. To prevent this, the GPL assures that patents cannot be used to render the program non-free.

The precise terms and conditions for copying, distribution and modification follow.

@heading TERMS AND CONDITIONS

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@item Definitions.

``This License" refers to version 3 of the GNU General Public License.

``Copyright" also means copyright-like laws that apply to other kinds of works, such as semiconductor masks.

``The Program" refers to any copyrightable work licensed under this License. Each licensee is addressed as ``you". ``Licensees" and ``recipients" may be individuals or organizations.

To ``modify" a work means to copy from or adapt all or part of the work in a fashion requiring copyright permission, other than the making of an exact copy. The resulting work is called a ``modified version" of the earlier work or a work ``based on" the earlier work.

A ``covered work" means either the unmodified Program or a work based on the Program.

To ``propagate" a work means to do anything with it that, without permission, would make you directly or secondarily liable for infringement under applicable copyright law, except executing it on a computer or modifying a private copy. Propagation includes copying, distribution (with or without modification), making available to the public, and in some countries other activities as well.

To ``convey" a work means any kind of propagation that enables other parties to make or receive copies. Mere interaction with a user through a computer network, with no transfer of a copy, is not conveying.

An interactive user interface displays ``Appropriate Legal Notices" to

the extent that it includes a convenient and prominently visible feature that (1) displays an appropriate copyright notice, and (2) tells the user that there is no warranty for the work (except to the extent that warranties are provided), that licensees may convey the work under this License, and how to view a copy of this License. If the interface presents a list of user commands or options, such as a menu, a prominent item in the list meets this criterion.

@item Source Code.

The ``source code" for a work means the preferred form of the work for making modifications to it. ``Object code" means any non-source form of a work.

A ``Standard Interface" means an interface that either is an official standard defined by a recognized standards body, or, in the case of interfaces specified for a particular programming language, one that is widely used among developers working in that language.

The ``System Libraries" of an executable work include anything, other than the work as a whole, that (a) is included in the normal form of packaging a Major Component, but which is not part of that Major Component, and (b) serves only to enable use of the work with that Major Component, or to implement a Standard Interface for which an implementation is available to the public in source code form. A ``Major Component", in this context, means a major essential component (kernel, window system, and so on) of the specific operating system (if any) on which the executable work runs, or a compiler used to produce the work, or an object code interpreter used to run it.

The ``Corresponding Source" for a work in object code form means all the source code needed to generate, install, and (for an executable work) run the object code and to modify the work, including scripts to control those activities. However, it does not include the work's System Libraries, or general-purpose tools or generally available free programs which are used unmodified in performing those activities but which are not part of the work. For example, Corresponding Source includes interface definition files associated with source files for the work, and the source code for shared libraries and dynamically linked subprograms that the work is specifically designed to require, such as by intimate data communication or control flow between those subprograms and other parts of the work.

The Corresponding Source need not include anything that users can regenerate automatically from other parts of the Corresponding Source.

The Corresponding Source for a work in source code form is that same work.

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@item Protecting Users' Legal Rights From Anti-Circumvention Law.

No covered work shall be deemed part of an effective technological measure under any applicable law fulfilling obligations under article 11 of the WIPO copyright treaty adopted on 20 December 1996, or similar laws prohibiting or restricting circumvention of such measures.

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You may convey verbatim copies of the Program's source code as you receive it, in any medium, provided that you conspicuously and appropriately publish on each copy an appropriate copyright notice; keep intact all notices stating that this License and any

non-permissive terms added in accord with section 7 apply to the code; keep intact all notices of the absence of any warranty; and give all recipients a copy of this License along with the Program.

You may charge any price or no price for each copy that you convey, and you may offer support or warranty protection for a fee.

@item Conveying Modified Source Versions.

You may convey a work based on the Program, or the modifications to produce it from the Program, in the form of source code under the terms of section 4, provided that you also meet all of these conditions:

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The work must carry prominent notices stating that you modified it, and giving a relevant date.

@item

The work must carry prominent notices stating that it is released under this License and any conditions added under section 7. This requirement modifies the requirement in section 4 to ``keep intact all notices".

@item

You must license the entire work, as a whole, under this License to anyone who comes into possession of a copy. This License will therefore apply, along with any applicable section 7 additional terms, to the whole of the work, and all its parts, regardless of how they are packaged. This License gives no permission to license the work in any other way, but it does not invalidate such permission if you have separately received it.

@item

If the work has interactive user interfaces, each must display Appropriate Legal Notices; however, if the Program has interactive interfaces that do not display Appropriate Legal Notices, your work need not make them do so.

@end enumerate

A compilation of a covered work with other separate and independent works, which are not by their nature extensions of the covered work, and which are not combined with it such as to form a larger program, in or on a volume of a storage or distribution medium, is called an ``aggregate" if the compilation and its resulting copyright are not used to limit the access or legal rights of the compilation's users beyond what the individual works permit. Inclusion of a covered work

in an aggregate does not cause this License to apply to the other parts of the aggregate.

@item Conveying Non-Source Forms.

You may convey a covered work in object code form under the terms of sections 4 and 5, provided that you also convey the machine-readable Corresponding Source under the terms of this License, in one of these ways:

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@item

Convey the object code in, or embodied in, a physical product (including a physical distribution medium), accompanied by the Corresponding Source fixed on a durable physical medium customarily used for software interchange.

@item

Convey the object code in, or embodied in, a physical product (including a physical distribution medium), accompanied by a written offer, valid for at least three years and valid for as long as you offer spare parts or customer support for that product model, to give anyone who possesses the object code either (1) a copy of the Corresponding Source for all the software in the product that is covered by this License, on a durable physical medium customarily used for software interchange, for a price no more than your reasonable cost of physically performing this conveying of source, or (2) access to copy the Corresponding Source from a network server at no charge.

@item

Convey individual copies of the object code with a copy of the written offer to provide the Corresponding Source. This alternative is allowed only occasionally and noncommercially, and only if you received the object code with such an offer, in accord with subsection 6b.

@item

Convey the object code by offering access from a designated place (gratis or for a charge), and offer equivalent access to the Corresponding Source in the same way through the same place at no further charge. You need not require recipients to copy the Corresponding Source along with the object code. If the place to copy the object code is a network server, the Corresponding Source may be on a different server (operated by you or a third party) that supports equivalent copying facilities, provided you maintain clear directions next to the object code saying where to find the Corresponding Source. Regardless of what server hosts the Corresponding Source, you remain obligated to ensure that it is available for as long as needed to

satisfy these requirements.

@item

Convey the object code using peer-to-peer transmission, provided you inform other peers where the object code and Corresponding Source of the work are being offered to the general public at no charge under subsection 6d.

@end enumerate

A separable portion of the object code, whose source code is excluded from the Corresponding Source as a System Library, need not be included in conveying the object code work.

A "User Product" is either (1) a "consumer product", which means any tangible personal property which is normally used for personal, family, or household purposes, or (2) anything designed or sold for incorporation into a dwelling. In determining whether a product is a consumer product, doubtful cases shall be resolved in favor of coverage. For a particular product received by a particular user, "normally used" refers to a typical or common use of that class of product, regardless of the status of the particular user or of the way in which the particular user actually uses, or expects or is expected to use, the product. A product is a consumer product regardless of whether the product has substantial commercial, industrial or non-consumer uses, unless such uses represent the only significant mode of use of the product.

"Installation Information" for a User Product means any methods, procedures, authorization keys, or other information required to install and execute modified versions of a covered work in that User Product from a modified version of its Corresponding Source. The information must suffice to ensure that the continued functioning of the modified object code is in no case prevented or interfered with solely because modification has been made.

If you convey an object code work under this section in, or with, or specifically for use in, a User Product, and the conveying occurs as part of a transaction in which the right of possession and use of the User Product is transferred to the recipient in perpetuity or for a fixed term (regardless of how the transaction is characterized), the Corresponding Source conveyed under this section must be accompanied by the Installation Information. But this requirement does not apply if neither you nor any third party retains the ability to install modified object code on the User Product (for example, the work has been installed in ROM).

The requirement to provide Installation Information does not include a

requirement to continue to provide support service, warranty, or updates for a work that has been modified or installed by the recipient, or for the User Product in which it has been modified or installed. Access to a network may be denied when the modification itself materially and adversely affects the operation of the network or violates the rules and protocols for communication across the network.

Corresponding Source conveyed, and Installation Information provided, in accord with this section must be in a format that is publicly documented (and with an implementation available to the public in source code form), and must require no special password or key for unpacking, reading or copying.

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1.103 libnet 1.2

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EOF

1.104 tomcat-websocket 8.5.32

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1.105 python 2.6

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A. HISTORY OF THE SOFTWARE

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Python was created in the early 1990s by Guido van Rossum at Stichting

Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see <http://www.zope.com>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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0.9.0 thru 1.2		1991-1995	CWI	yes
1.3 thru 1.5.2	1.2	1995-1999	CNRI	yes
1.6	1.5.2	2000	CNRI	no
2.0	1.6	2000	BeOpen.com	no
1.6.1	1.6	2001	CNRI	yes (2)
2.1	2.0+1.6.1	2001	PSF	no
2.0.1	2.0+1.6.1	2001	PSF	yes
2.1.1	2.1+2.0.1	2001	PSF	yes
2.2	2.1.1	2001	PSF	yes
2.1.2	2.1.1	2002	PSF	yes
2.1.3	2.1.2	2002	PSF	yes
2.2.1	2.2	2002	PSF	yes
2.2.2	2.2.1	2002	PSF	yes
2.2.3	2.2.2	2003	PSF	yes
2.3	2.2.2	2002-2003	PSF	yes
2.3.1	2.3	2002-2003	PSF	yes
2.3.2	2.3.1	2002-2003	PSF	yes
2.3.3	2.3.2	2002-2003	PSF	yes
2.3.4	2.3.3	2004	PSF	yes
2.3.5	2.3.4	2005	PSF	yes
2.4	2.3	2004	PSF	yes
2.4.1	2.4	2005	PSF	yes

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2.4.3	2.4.2	2006	PSF	yes
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1.107 miglayout 3.5.5

1.108 c-ares 1.10.0

1.108.1 Available under license :

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1.109 quartz 2.1.7

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1.110 openssh 5.4p1

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```
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 */

#ifndef _BSD_WAITPID_H
#define _BSD_WAITPID_H

#ifndef HAVE_WAITPID
/* Clean out any potential issues */
#undef WIFEXITED
#undef WIFSTOPPED
#undef WIFSIGNALED

/* Define required functions to mimic a POSIX look and feel */
#define _W_INT(w) (*(int*)&(w)) /* convert union wait to int */
#define WIFEXITED(w) (!((_W_INT(w)) & 0377))
#define WIFSTOPPED(w) ((_W_INT(w)) & 0100)
#define WIFSIGNALED(w) (!WIFEXITED(w) && !WIFSTOPPED(w))
#define WEXITSTATUS(w) (int)(WIFEXITED(w) ? ((_W_INT(w) >> 8) & 0377) : -1)
#define WTERMSIG(w) (int)(WIFSIGNALED(w) ? (_W_INT(w) & 0177) : -1)
#define WCOREFLAG 0x80
#define WCOREDUMP(w) ((_W_INT(w)) & WCOREFLAG)

```

```

/* Prototype */
pid_t waitpid(int, int *, int);

#endif /* !HAVE_WAITPID */
#endif /* _BSD_WAITPID_H */
/*
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*/

#ifndef HAVE_SETRESGID
int setresgid(gid_t, gid_t, gid_t);
#endif
#ifndef HAVE_SETRESUID
int setresuid(uid_t, uid_t, uid_t);
#endif
This file is part of the OpenSSH software.

```

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* @version 3.0 (December 2000)

*

* Optimised ANSI C code for the Rijndael cipher (now AES)

*

* @author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>

* @author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>

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/* OPENBSD ORIGINAL: sys/sys/poll.h */

#if !defined(HAVE_POLL) && !defined(HAVE_POLL_H)
#ifdef _COMPAT_POLL_H_
#define _COMPAT_POLL_H_

typedef struct pollfd {
int fd;
short events;
short revents;
} pollfd_t;

```

```

typedef unsigned int nfd_t;

#define POLLIN 0x0001
#define POLLOUT 0x0004
#define POLLERR 0x0008
#define POLLHUP 0x0010
#define POLLNVAL 0x0020
#if 0
/* the following are currently not implemented */
#define POLLPRI 0x0002
#define POLLRDNORM 0x0040
#define POLLNORM POLLRDNORM
#define POLLWRNORM POLLOUT
#define POLLRDBAND 0x0080
#define POLLWRBAND 0x0100
#endif

#define INFTIM (-1) /* not standard */

int poll(struct pollfd *, nfd_t, int);
#endif /* !_COMPAT_POLL_H_ */
#endif /* !HAVE_POLL_H */

```

1.111 tomcat-jasper-el 8.5.32

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1.115 apr 1.5.2

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```
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From network_io/unix/inet_ntop.c, network_io/unix/inet_pton.c:

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From dso/aix/dso.c:

* Based on libdl (dlfcn.c/dlfcn.h) which is
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From strings/apr_strnatcmp.c, include/apr_strings.h:

strnatcmp.c -- Perform 'natural order' comparisons of strings in C.
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From strings/apr_sprintf.c:

```
*  
* cvt - IEEE floating point formatting routines.  
*   Derived from UNIX V7, Copyright(C) Caldera International Inc.  
*
```

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1.118 sqlite 3.22.0

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c) Convey individual copies of the object code with a copy of the written offer to provide the Corresponding Source. This alternative is allowed only occasionally and noncommercially, and only if you received the object code with such an offer, in accord with subsection 6b.

d) Convey the object code by offering access from a designated place (gratis or for a charge), and offer equivalent access to the Corresponding Source in the same way through the same place at no further charge. You need not require recipients to copy the Corresponding Source along with the object code. If the place to copy the object code is a network server, the Corresponding Source may be on a different server (operated by you or a third party) that supports equivalent copying facilities, provided you maintain clear directions next to the object code saying where to find the Corresponding Source. Regardless of what server hosts the Corresponding Source, you remain obligated to ensure that it is available for as long as needed to satisfy these requirements.

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Version 2.1, February 1999

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

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5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or

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b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

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1.123 tomcat-dbcP 8.5.32

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1.124 libgpg-error 1.27

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Includes `lib/servlet-api-2.4.jar` from Apache Tomcat

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The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively



promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

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## Constant-time SSSE3 AES core implementation.

## version 0.1

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## By Mike Hamburg (Stanford University), 2009

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## 1.133 commons-lang3 2.6

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## 1.134 tomcat-el-api 8.5.32

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```

## 1.136 tomcat-util-scan 8.5.32

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## 1.137 Ivm 6.0.0

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```
; RUN: opt %s -inline -S | FileCheck %s
```

```
define internal void @innerSmall() "min-legal-vector-width"="128" {  
  ret void  
}
```

```
define internal void @innerLarge() "min-legal-vector-width"="512" {  
  ret void  
}
```

```
define internal void @innerNoAttribute() {  
  ret void  
}
```

```
; We should not add an attribute during inlining. No attribute means unknown.
```

```
; Inlining doesn't change the fact that we don't know anything about this
```

```
; function.
```

```
define void @outerNoAttribute() {  
  call void @innerLarge()  
  ret void  
}
```

```
define void @outerConflictingAttributeSmall() "min-legal-vector-width"="128" {  
  call void @innerLarge()  
  ret void  
}
```

```
define void @outerConflictingAttributeLarge() "min-legal-vector-width"="512" {  
  call void @innerSmall()  
  ret void  
}
```

```
; We should remove the attribute after inlining since the callee's
```

```
; vector width requirements are unknown.
```

```
define void @outerAttribute() "min-legal-vector-width"="128" {  
  call void @innerNoAttribute()  
  ret void  
}
```

```
; CHECK: define void @outerNoAttribute() {
```

```
; CHECK: define void @outerConflictingAttributeSmall() #0
```

```
; CHECK: define void @outerConflictingAttributeLarge() #0
```

```
; CHECK: define void @outerAttribute() {
```

```
; CHECK: attributes #0 = { "min-legal-vector-width"="512" }
```

```
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```

```
-----  
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<none yet>

; RUN: llc -march=hexagon < %s

; REQUIRES: asserts

; The two loads based on %struct.0, loading two different data types  
; cause LSR to assume type "void" for the memory type. This would then  
; cause an assert in isLegalAddressingMode. Make sure we no longer crash.

target triple = "hexagon"

%struct.0 = type { i8\*, i8, %union.anon.0 }

%union.anon.0 = type { i8\* }

define hidden fastcc void @fred() unnamed\_addr #0 {

entry:

br i1 undef, label %while.end, label %while.body.lr.ph

while.body.lr.ph: ; preds = %entry

br label %while.body

while.body: ; preds = %exit.2, %while.body.lr.ph

%lsr.iv = phi %struct.0\* [ %cgep22, %exit.2 ], [ undef, %while.body.lr.ph ]

switch i32 undef, label %exit [

i32 1, label %sw.bb.i

i32 2, label %sw.bb3.i

]

sw.bb.i: ; preds = %while.body

unreachable

sw.bb3.i: ; preds = %while.body

unreachable

exit: ; preds = %while.body

switch i32 undef, label %exit.2 [

i32 1, label %sw.bb.i17

i32 2, label %sw.bb3.i20

]

sw.bb.i17: ; preds = %exit

```

%0 = bitcast %struct.0* %lstr.iv to i32*
%1 = load i32, i32* %0, align 4
unreachable

sw.bb3.i20:                                ; preds = %exit
%2 = bitcast %struct.0* %lstr.iv to i8**
%3 = load i8*, i8** %2, align 4
unreachable

exit.2:                                     ; preds = %exit
%cgep22 = getelementptr %struct.0, %struct.0* %lstr.iv, i32 1
br label %while.body

while.end:                                  ; preds = %entry
ret void
}

attributes #0 = { nounwind optimize "target-cpu"="hexagonv55" }
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```

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```
; RUN: llc -mtriple=aarch64-apple-ios %s -o - | FileCheck %s
```

```
define <16 x double> @test_sitofp_fixed(<16 x i32> %in) {  
; CHECK-LABEL: test_sitofp_fixed:
```

```
    ; First, extend each i32 to i64
```

```
    ; CHECK-DAG: sshll2.2d [[BLOCK0_HI:v[0-9]+]], v0, #0
```

```
    ; CHECK-DAG: sshll2.2d [[BLOCK1_HI:v[0-9]+]], v1, #0
```

```
    ; CHECK-DAG: sshll2.2d [[BLOCK2_HI:v[0-9]+]], v2, #0
```

```
    ; CHECK-DAG: sshll2.2d [[BLOCK3_HI:v[0-9]+]], v3, #0
```

```
    ; CHECK-DAG: sshll.2d [[BLOCK0_LO:v[0-9]+]], v0, #0
```

```
    ; CHECK-DAG: sshll.2d [[BLOCK1_LO:v[0-9]+]], v1, #0
```

```
    ; CHECK-DAG: sshll.2d [[BLOCK2_LO:v[0-9]+]], v2, #0
```

```
    ; CHECK-DAG: sshll.2d [[BLOCK3_LO:v[0-9]+]], v3, #0
```

```
    ; Next, convert each to double.
```

```
    ; CHECK-DAG: scvtf.2d v0, [[BLOCK0_LO]]
```

```
    ; CHECK-DAG: scvtf.2d v1, [[BLOCK0_HI]]
```

```
    ; CHECK-DAG: scvtf.2d v2, [[BLOCK1_LO]]
```

```
    ; CHECK-DAG: scvtf.2d v3, [[BLOCK1_HI]]
```

```
    ; CHECK-DAG: scvtf.2d v4, [[BLOCK2_LO]]
```

```
    ; CHECK-DAG: scvtf.2d v5, [[BLOCK2_HI]]
```

```
    ; CHECK-DAG: scvtf.2d v6, [[BLOCK3_LO]]
```

```
    ; CHECK-DAG: scvtf.2d v7, [[BLOCK3_HI]]
```

```
; CHECK: ret
```

```
    %flt = sitofp <16 x i32> %in to <16 x double>
```

```
    %res = fdiv <16 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0,  
double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0>
```

```
    ret <16 x double> %res
```

```

}

; This one is small enough to satisfy isSimple, but still illegally large.
define <4 x double> @test_sitofp_fixed_shortish(<4 x i64> %in) {
; CHECK-LABEL: test_sitofp_fixed_shortish:

; CHECK-DAG: scvtf.2d v0, v0
; CHECK-DAG: scvtf.2d v1, v1

; CHECK: ret
%flt = sitofp <4 x i64> %in to <4 x double>
%res = fdiv <4 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0>
ret <4 x double> %res
}

```

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```
; RUN: llc < %s -mtriple=x86_64-linux-android -mattr=+mmx -enable-legalize-types-checking | FileCheck %s  
;  
; D31946  
; Check that we dont end up with the ""LLVM ERROR: Cannot select" error.  
; Additionally ensure that the output code actually put fp128 values in SSE registers.
```

```
declare fp128 @llvm.fabs.f128(fp128)  
declare fp128 @llvm.copysign.f128(fp128, fp128)
```

```
define fp128 @TestSelect(fp128 %a, fp128 %b) {  
  %cmp = fcmp ogt fp128 %a, %b  
  %sub = fsub fp128 %a, %b  
  %res = select i1 %cmp, fp128 %sub, fp128 0xL00000000000000000000000000000000  
  ret fp128 %res  
; CHECK-LABEL: TestSelect:  
; CHECK      movaps 16(%rsp), %xmm1  
; CHECK-NEXT callq __subtf3  
; CHECK-NEXT testl %ebx, %ebx  
; CHECK-NEXT jg .LBB0_2  
; CHECK-NEXT # %bb.1:  
; CHECK-NEXT movaps .LCPI0_0(%rip), %xmm0  
; CHECK-NEXT .LBB0_2:  
; CHECK-NEXT addq $32, %rsp  
; CHECK-NEXT popq %rbx
```

```
; CHECK-NEXT retq
}
```

```
define fp128 @TestFabs(fp128 %a) {
    %res = call fp128 @llvm.fabs.f128(fp128 %a)
    ret fp128 %res
; CHECK-LABEL: TestFabs:
; CHECK    andps .LCPI1_0(%rip), %xmm0
; CHECK-NEXT retq
}
```

```
define fp128 @TestCopysign(fp128 %a, fp128 %b) {
    %res = call fp128 @llvm.copysign.f128(fp128 %a, fp128 %b)
    ret fp128 %res
; CHECK-LABEL: TestCopysign:
; CHECK    andps .LCPI2_1(%rip), %xmm0
; CHECK-NEXT orps %xmm1, %xmm0
; CHECK-NEXT retq
}
```

```
define fp128 @TestFneg(fp128 %a) {
    %mul = fmul fp128 %a, %a
    %res = fsub fp128 0xL00000000000000008000000000000000, %mul
    ret fp128 %res
; CHECK-LABEL: TestFneg:
; CHECK    movaps %xmm0, %xmm1
; CHECK-NEXT callq __multf3
; CHECK-NEXT xorps .LCPI3_0(%rip), %xmm0
; CHECK-NEXT popq %rax
; CHECK-NEXT retq
}
```

=====  
LLVM Release License  
=====

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Developed by:

LLVM Team

University of Illinois at Urbana-Champaign

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| Program       | Directory                                 |
|---------------|-------------------------------------------|
| -----         | -----                                     |
| Google Test   | llvm/utils/unittest/googletest            |
| OpenBSD regex | llvm/lib/Support/{reg*, COPYRIGHT.regex } |

```

pyyaml tests    llvm/test/YAMLParse/{*.data, LICENSE.TXT}
ARM contributions llvm/lib/Target/ARM/LICENSE.TXT
md5 contributions llvm/lib/Support/MD5.cpp llvm/include/llvm/Support/MD5.h
; NOTE: Assertions have been autogenerated by utils/update_analyze_test_checks.py
; RUN: opt < %s -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx2 | FileCheck %s --check-
prefixes=CHECK,VEC256,AVX
; RUN: opt < %s -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -
mattr=+avx512vl,+avx512bw,+avx512dq,+prefer-256-bit | FileCheck %s --check-
prefixes=CHECK,VEC256,SKX256
; RUN: opt < %s -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -
mattr=+avx512vl,+avx512bw,+avx512dq,-prefer-256-bit | FileCheck %s --check-prefixes=CHECK,VEC512

```

```

define void @zext256() "min-legal-vector-width"="256" {
; VEC256-LABEL: 'zext256'
; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = zext <8 x i16> undef to <8 x
i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x
i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x
i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = zext <16 x i16> undef to <16 x
i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x
i16>
; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; VEC512-LABEL: 'zext256'
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x
i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x
i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x
i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x
i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x
i16>
; VEC512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
%A = zext <8 x i16> undef to <8 x i64>
%B = zext <8 x i32> undef to <8 x i64>
%C = zext <16 x i8> undef to <16 x i32>
%D = zext <16 x i16> undef to <16 x i32>
%E = zext <32 x i8> undef to <32 x i16>
ret void
}

```

```

define void @zext512() "min-legal-vector-width"="512" {

```

```

; AVX-LABEL: 'zext512'
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = zext <8 x i16> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = zext <16 x i16> undef to <16 x
i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX256-LABEL: 'zext512'
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x
i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x
i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x
i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; VEC512-LABEL: 'zext512'
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x
i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x
i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x
i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x
i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x
i16>
; VEC512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
%A = zext <8 x i16> undef to <8 x i64>
%B = zext <8 x i32> undef to <8 x i64>
%C = zext <16 x i8> undef to <16 x i32>
%D = zext <16 x i16> undef to <16 x i32>
%E = zext <32 x i8> undef to <32 x i16>
ret void
}

define void @sext256() "min-legal-vector-width"="256" {
; VEC256-LABEL: 'sext256'
; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %B = sext <8 x i16> undef to <8 x
i64>

```

```

; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x
i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x
i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %E = sext <16 x i16> undef to <16 x
i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x
i16>
; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; VEC512-LABEL: 'sext256'
; VEC512-NEXT: Cost Model: Found an estimated cost of 24 for instruction: %A = sext <8 x i8> undef to <8 x
i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x
i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x
i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x
i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x
i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x
i16>
; VEC512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
%A = sext <8 x i8> undef to <8 x i64>
%B = sext <8 x i16> undef to <8 x i64>
%C = sext <8 x i32> undef to <8 x i64>
%D = sext <16 x i8> undef to <16 x i32>
%E = sext <16 x i16> undef to <16 x i32>
%F = sext <32 x i8> undef to <32 x i16>
ret void
}

define void @sext512() "min-legal-vector-width"="512" {
; AVX-LABEL: 'sext512'
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX256-LABEL: 'sext512'
; SKX256-NEXT: Cost Model: Found an estimated cost of 24 for instruction: %A = sext <8 x i8> undef to <8 x
i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x

```



```

i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x
i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x
i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; VEC512-LABEL: 'sext512'
; VEC512-NEXT: Cost Model: Found an estimated cost of 24 for instruction: %A = sext <8 x i8> undef to <8 x
i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x
i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x
i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x
i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x
i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x
i16>
; VEC512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
%A = sext <8 x i8> undef to <8 x i64>
%B = sext <8 x i16> undef to <8 x i64>
%C = sext <8 x i32> undef to <8 x i64>
%D = sext <16 x i8> undef to <16 x i32>
%E = sext <16 x i16> undef to <16 x i32>
%F = sext <32 x i8> undef to <32 x i16>
ret void
}
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=avx512vl,avx512bw,avx512dq,prefer-256-bit |
FileCheck %s

```

; This file primarily contains tests for specific places in X86ISelLowering.cpp that needed be made aware of the legalizer not allowing 512-bit vectors due to prefer-256-bit even though AVX512 is enabled.

```

define void @add256(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="256" {
; CHECK-LABEL: add256:
; CHECK:      # %bb.0:
; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:  vpaddd (%rsi), %ymm0, %ymm0
; CHECK-NEXT:  vpaddd 32(%rsi), %ymm1, %ymm1

```





```

%a = sext <32 x i16> %A to <32 x i32>
%b = sext <32 x i16> %B to <32 x i32>
%m = mul nsw <32 x i32> %a, %b
%odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32
12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
%even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32
13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
%ret = add <16 x i32> %odd, %even
store <16 x i32> %ret, <16 x i32>* %CPtr
ret void
}

```

```

define void @pmaddwd_32_512(<32 x i16>* %APtr, <32 x i16>* %BPtr, <16 x i32>* %CPtr) "min-legal-vector-
width"="512" {

```

```

; CHECK-LABEL: pmaddwd_32_512:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT:  vpmaddwd (%rsi), %zmm0, %zmm0
; CHECK-NEXT:  vmovdqa64 %zmm0, (%rdx)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%A = load <32 x i16>, <32 x i16>* %APtr
%B = load <32 x i16>, <32 x i16>* %BPtr
%a = sext <32 x i16> %A to <32 x i32>
%b = sext <32 x i16> %B to <32 x i32>
%m = mul nsw <32 x i32> %a, %b
%odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32
12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
%even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32
13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
%ret = add <16 x i32> %odd, %even
store <16 x i32> %ret, <16 x i32>* %CPtr
ret void
}

```

```

define void @psubus_64i8_max_256(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-
width"="256" {

```

```

; CHECK-LABEL: psubus_64i8_max_256:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:  vpsubusb (%rsi), %ymm0, %ymm0
; CHECK-NEXT:  vpsubusb 32(%rsi), %ymm1, %ymm1
; CHECK-NEXT:  vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT:  vmovdqa %ymm0, (%rdx)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%x = load <64 x i8>, <64 x i8>* %xptr

```

```

%y = load <64 x i8>, <64 x i8>* %yptr
%cmp = icmp ult <64 x i8> %x, %y
%max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
%res = sub <64 x i8> %max, %y
store <64 x i8> %res, <64 x i8>* %zptr
ret void
}

```

```

define void @psubus_64i8_max_512(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-
width"="512" {
; CHECK-LABEL: psubus_64i8_max_512:
; CHECK:      # %bb.0:
; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT: vpsubusb (%rsi), %zmm0, %zmm0
; CHECK-NEXT: vmovdqa64 %zmm0, (%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <64 x i8>, <64 x i8>* %xptr
%y = load <64 x i8>, <64 x i8>* %yptr
%cmp = icmp ult <64 x i8> %x, %y
%max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
%res = sub <64 x i8> %max, %y
store <64 x i8> %res, <64 x i8>* %zptr
ret void
}

```

```

define i32 @_Z9test_charPcS_i_256(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-
width"="256" {
; CHECK-LABEL: _Z9test_charPcS_i_256:
; CHECK:      # %bb.0: # %entry
; CHECK-NEXT: movl %edx, %eax
; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT: xorl %ecx, %ecx
; CHECK-NEXT: vpxor %xmm1, %xmm1, %xmm1
; CHECK-NEXT: vpxor %xmm2, %xmm2, %xmm2
; CHECK-NEXT: .p2align 4, 0x90
; CHECK-NEXT: .LBB8_1: # %vector.body
; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
; CHECK-NEXT: vpmovsxbw (%rdi,%rcx), %ymm3
; CHECK-NEXT: vpmovsxbw 16(%rdi,%rcx), %ymm4
; CHECK-NEXT: vpmovsxbw (%rsi,%rcx), %ymm5
; CHECK-NEXT: vpmaddwd %ymm3, %ymm5, %ymm3
; CHECK-NEXT: vpadd %ymm1, %ymm3, %ymm1
; CHECK-NEXT: vpmovsxbw 16(%rsi,%rcx), %ymm3
; CHECK-NEXT: vpmaddwd %ymm4, %ymm3, %ymm3
; CHECK-NEXT: vpadd %ymm2, %ymm3, %ymm2
; CHECK-NEXT: addq $32, %rcx
; CHECK-NEXT: cmpq %rcx, %rax

```

```

; CHECK-NEXT:   jne .LBB8_1
; CHECK-NEXT:   # %bb.2: # %middle.block
; CHECK-NEXT:   vpaddd %ymm0, %ymm1, %ymm1
; CHECK-NEXT:   vpaddd %ymm0, %ymm2, %ymm0
; CHECK-NEXT:   vpaddd %ymm0, %ymm1, %ymm0
; CHECK-NEXT:   vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT:   vpaddd %ymm1, %ymm0, %ymm0
; CHECK-NEXT:   vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT:   vpaddd %ymm1, %ymm0, %ymm0
; CHECK-NEXT:   vpshufd {{.*#+}} xmm1 = xmm0[1,1,2,3]
; CHECK-NEXT:   vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT:   vmovd %xmm0, %eax
; CHECK-NEXT:   vzeroupper
; CHECK-NEXT:   retq

```

entry:

```

%3 = zext i32 %2 to i64
br label %vector.body

```

vector.body:

```

%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%4 = getelementptr inbounds i8, i8* %0, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mul nsw <32 x i32> %9, %6
%11 = add nsw <32 x i32> %10, %vec.phi
%index.next = add i64 %index, 32
%12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body

```

middle.block:

```

%rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20,
i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
undef, i32 undef, i32 undef>
%bin.rdx1 = add <32 x i32> %11, %rdx.shuf1
%rdx.shuf = shufflevector <32 x i32> %bin.rdx1, <32 x i32> undef, <32 x i32> <i32 8, i32 9, i32 10, i32 11, i32
12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx = add <32 x i32> %bin.rdx1, %rdx.shuf
%rdx.shuf15 = shufflevector <32 x i32> %bin.rdx, <32 x i32> undef, <32 x i32> <i32 4, i32 5, i32 6, i32 7, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>

```

```

i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx32 = add <32 x i32> %bin.rdx, %rdx.shuf15
%rdx.shuf17 = shufflevector <32 x i32> %bin.rdx32, <32 x i32> undef, <32 x i32> <i32 2, i32 3, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx18 = add <32 x i32> %bin.rdx32, %rdx.shuf17
%rdx.shuf19 = shufflevector <32 x i32> %bin.rdx18, <32 x i32> undef, <32 x i32> <i32 1, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx20 = add <32 x i32> %bin.rdx18, %rdx.shuf19
%13 = extractelement <32 x i32> %bin.rdx20, i32 0
ret i32 %13
}

```

```

define i32 @_Z9test_charPcS_i_512(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-
width"="512" {

```

```

; CHECK-LABEL: _Z9test_charPcS_i_512:
; CHECK:      # %bb.0: # %entry
; CHECK-NEXT: movl %edx, %eax
; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT: xorl %ecx, %ecx
; CHECK-NEXT: vpxor %xmm1, %xmm1, %xmm1
; CHECK-NEXT: .p2align 4, 0x90
; CHECK-NEXT: .LBB9_1: # %vector.body
; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
; CHECK-NEXT: vpmovsxbw (%rdi,%rcx), %zmm2
; CHECK-NEXT: vpmovsxbw (%rsi,%rcx), %zmm3
; CHECK-NEXT: vpmaddwd %zmm2, %zmm3, %zmm2
; CHECK-NEXT: vpadd %zmm1, %zmm2, %zmm1
; CHECK-NEXT: addq $32, %rcx
; CHECK-NEXT: cmpq %rcx, %rax
; CHECK-NEXT: jne .LBB9_1
; CHECK-NEXT: # %bb.2: # %middle.block
; CHECK-NEXT: vpadd %zmm0, %zmm1, %zmm0
; CHECK-NEXT: vextracti64x4 $1, %zmm0, %ymm1
; CHECK-NEXT: vpadd %zmm1, %zmm0, %zmm0
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT: vpadd %zmm1, %zmm0, %zmm0
; CHECK-NEXT: vpslufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT: vpadd %zmm1, %zmm0, %zmm0
; CHECK-NEXT: vpslufd {{.*#+}} xmm1 = xmm0[1,1,2,3]
; CHECK-NEXT: vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vmovd %xmm0, %eax
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq

```





```
ret i32 %13
}
```

```
@a = global [1024 x i8] zeroinitializer, align 16
```

```
@b = global [1024 x i8] zeroinitializer, align 16
```

```
define i32 @sad_16i8_256() "min-legal-vector-width"="256" {
```

```
; CHECK-LABEL: sad_16i8_256:
```

```
; CHECK: # %bb.0: # %entry
```

```
; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
```

```
; CHECK-NEXT: movq $-1024, %rax # imm = 0xFC00
```

```
; CHECK-NEXT: vpxor %xmm1, %xmm1, %xmm1
```

```
; CHECK-NEXT: .p2align 4, 0x90
```

```
; CHECK-NEXT: .LBB10_1: # %vector.body
```

```
; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
```

```
; CHECK-NEXT: vmovdqu a+1024(%rax), %xmm2
```

```
; CHECK-NEXT: vpsadbw b+1024(%rax), %xmm2, %xmm2
```

```
; CHECK-NEXT: vpadd %ymm1, %ymm2, %ymm1
```

```
; CHECK-NEXT: addq $4, %rax
```

```
; CHECK-NEXT: jne .LBB10_1
```

```
; CHECK-NEXT: # %bb.2: # %middle.block
```

```
; CHECK-NEXT: vpadd %ymm0, %ymm1, %ymm0
```

```
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm1
```

```
; CHECK-NEXT: vpadd %ymm1, %ymm0, %ymm0
```

```
; CHECK-NEXT: vpsltd { {.*#+} } xmm1 = xmm0[2,3,0,1]
```

```
; CHECK-NEXT: vpadd %ymm1, %ymm0, %ymm0
```

```
; CHECK-NEXT: vpsltd { {.*#+} } xmm1 = xmm0[1,1,2,3]
```

```
; CHECK-NEXT: vpadd %xmm1, %xmm0, %xmm0
```

```
; CHECK-NEXT: vmovd %xmm0, %eax
```

```
; CHECK-NEXT: vzeroupper
```

```
; CHECK-NEXT: retq
```

```
entry:
```

```
br label %vector.body
```

```
vector.body:
```

```
%index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
```

```
%vec.phi = phi <16 x i32> [ zeroinitializer, %entry ], [ %10, %vector.body ]
```

```
%0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
```

```
%1 = bitcast i8* %0 to <16 x i8>*
```

```
%wide.load = load <16 x i8>, <16 x i8>* %1, align 4
```

```
%2 = zext <16 x i8> %wide.load to <16 x i32>
```

```
%3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
```

```
%4 = bitcast i8* %3 to <16 x i8>*
```

```
%wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4
```

```
%5 = zext <16 x i8> %wide.load1 to <16 x i32>
```

```
%6 = sub nsw <16 x i32> %2, %5
```

```
%7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>
```

```

%8 = sub nsw <16 x i32> zeroinitializer, %6
%9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
%10 = add nsw <16 x i32> %9, %vec.phi
%index.next = add i64 %index, 4
%11 = icmp eq i64 %index.next, 1024
br i1 %11, label %middle.block, label %vector.body

```

middle.block:

```

%.lcssa = phi <16 x i32> [ %10, %vector.body ]
%rdx.shuf = shufflevector <16 x i32> %.lcssa, <16 x i32> undef, <16 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12,
i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx = add <16 x i32> %.lcssa, %rdx.shuf
%rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> <i32 4, i32 5, i32 6, i32 7, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef>
%bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> <i32 2, i32 3, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef>
%bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> <i32 1, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef>
%bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
ret i32 %12
}

```

```

define i32 @sad_16i8_512() "min-legal-vector-width"="512" {
; CHECK-LABEL: sad_16i8_512:
; CHECK:      # %bb.0: # %entry
; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT: movq $-1024, %rax # imm = 0xFC00
; CHECK-NEXT: .p2align 4, 0x90
; CHECK-NEXT: .LBB11_1: # %vector.body
; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
; CHECK-NEXT: vmovdqu a+1024(%rax), %xmm1
; CHECK-NEXT: vpsadbw b+1024(%rax), %xmm1, %xmm1
; CHECK-NEXT: vpadd %zmm0, %zmm1, %zmm0
; CHECK-NEXT: addq $4, %rax
; CHECK-NEXT: jne .LBB11_1
; CHECK-NEXT: # %bb.2: # %middle.block
; CHECK-NEXT: vextracti64x4 $1, %zmm0, %ymm1
; CHECK-NEXT: vpadd %zmm1, %zmm0, %zmm0
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT: vpadd %zmm1, %zmm0, %zmm0
; CHECK-NEXT: vpslufd {{.*#+}} %xmm1 = %xmm0[2,3,0,1]
; CHECK-NEXT: vpadd %zmm1, %zmm0, %zmm0

```

```
; CHECK-NEXT: vpsfufd {.*#+} xmm1 = xmm0[1,1,2,3]
; CHECK-NEXT: vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vmovd %xmm0, %eax
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
```

```
entry:
```

```
br label %vector.body
```

```
vector.body:
```

```
%index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
```

```
%vec.phi = phi <16 x i32> [ zeroinitializer, %entry ], [ %10, %vector.body ]
```

```
%0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
```

```
%1 = bitcast i8* %0 to <16 x i8>*
```

```
%wide.load = load <16 x i8>, <16 x i8>* %1, align 4
```

```
%2 = zext <16 x i8> %wide.load to <16 x i32>
```

```
%3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
```

```
%4 = bitcast i8* %3 to <16 x i8>*
```

```
%wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4
```

```
%5 = zext <16 x i8> %wide.load1 to <16 x i32>
```

```
%6 = sub nsw <16 x i32> %2, %5
```

```
%7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>
```

```
%8 = sub nsw <16 x i32> zeroinitializer, %6
```

```
%9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
```

```
%10 = add nsw <16 x i32> %9, %vec.phi
```

```
%index.next = add i64 %index, 4
```

```
%11 = icmp eq i64 %index.next, 1024
```

```
br i1 %11, label %middle.block, label %vector.body
```

```
middle.block:
```

```
%.lcssa = phi <16 x i32> [ %10, %vector.body ]
```

```
%rdx.shuf = shufflevector <16 x i32> %.lcssa, <16 x i32> undef, <16 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
```

```
%bin.rdx = add <16 x i32> %.lcssa, %rdx.shuf
```

```
%rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> <i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
```

```
%bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
```

```
%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> <i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
```

```
%bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
```

```
%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> <i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
```

```
%bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
```

```
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
```

```
ret i32 %12
```

```

}

define void @sbto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: sbto16f32_256:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vpmovw2m %ymm0, %k0
; CHECK-NEXT:  kshiftrw $8, %k0, %k1
; CHECK-NEXT:  vpmovm2d %k1, %ymm0
; CHECK-NEXT:  vcvtdq2ps %ymm0, %ymm0
; CHECK-NEXT:  vpmovm2d %k0, %ymm1
; CHECK-NEXT:  vcvtdq2ps %ymm1, %ymm1
; CHECK-NEXT:  vmovaps %ymm1, (%rdi)
; CHECK-NEXT:  vmovaps %ymm0, 32(%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @sbto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: sbto16f32_512:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vpmovw2m %ymm0, %k0
; CHECK-NEXT:  vpmovm2d %k0, %zmm0
; CHECK-NEXT:  vcvtdq2ps %zmm0, %zmm0
; CHECK-NEXT:  vmovaps %zmm0, (%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @sbto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: sbto16f64_256:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vpmovw2m %ymm0, %k0
; CHECK-NEXT:  kshiftrw $8, %k0, %k1
; CHECK-NEXT:  vpmovm2d %k1, %ymm0
; CHECK-NEXT:  vcvtdq2pd %xmm0, %ymm1
; CHECK-NEXT:  vextracti128 $1, %ymm0, %xmm0
; CHECK-NEXT:  vcvtdq2pd %xmm0, %ymm0
; CHECK-NEXT:  vpmovm2d %k0, %ymm2
; CHECK-NEXT:  vcvtdq2pd %xmm2, %ymm3
; CHECK-NEXT:  vextracti128 $1, %ymm2, %xmm2

```

```

; CHECK-NEXT:   vcvtdq2pd %xmm2, %ymm2
; CHECK-NEXT:   vmovaps %ymm2, 32(%rdi)
; CHECK-NEXT:   vmovaps %ymm3, (%rdi)
; CHECK-NEXT:   vmovaps %ymm0, 96(%rdi)
; CHECK-NEXT:   vmovaps %ymm1, 64(%rdi)
; CHECK-NEXT:   vzeroupper
; CHECK-NEXT:   retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

define void @sbto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: sbto16f64_512:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vpmovw2m %ymm0, %k0
; CHECK-NEXT:  vpmovm2d %k0, %zmm0
; CHECK-NEXT:  vcvtdq2pd %ymm0, %zmm1
; CHECK-NEXT:  vextracti64x4 $1, %zmm0, %ymm0
; CHECK-NEXT:  vcvtdq2pd %ymm0, %zmm0
; CHECK-NEXT:  vmovaps %zmm0, 64(%rdi)
; CHECK-NEXT:  vmovaps %zmm1, (%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

define void @ubto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: ubto16f32_256:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vpmovw2m %ymm0, %k0
; CHECK-NEXT:  kshiftrw $8, %k0, %k1
; CHECK-NEXT:  vpmovm2d %k1, %ymm0
; CHECK-NEXT:  vpsrld $31, %ymm0, %ymm0
; CHECK-NEXT:  vcvtdq2ps %ymm0, %ymm0
; CHECK-NEXT:  vpmovm2d %k0, %ymm1
; CHECK-NEXT:  vpsrld $31, %ymm1, %ymm1
; CHECK-NEXT:  vcvtdq2ps %ymm1, %ymm1
; CHECK-NEXT:  vmovaps %ymm1, (%rdi)
; CHECK-NEXT:  vmovaps %ymm0, 32(%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x float>

```

```

store <16 x float> %1, <16 x float>* %res
ret void
}

define void @ubto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: ubto16f32_512:
; CHECK:      # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: vpmovm2d %k0, %zmm0
; CHECK-NEXT: vpsrld $31, %zmm0, %zmm0
; CHECK-NEXT: vcvtdq2ps %zmm0, %zmm0
; CHECK-NEXT: vmovaps %zmm0, (%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @ubto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: ubto16f64_256:
; CHECK:      # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: kshiftrw $8, %k0, %k1
; CHECK-NEXT: vpmovm2d %k1, %ymm0
; CHECK-NEXT: vpsrld $31, %ymm0, %ymm0
; CHECK-NEXT: vcvtdq2pd %xmm0, %ymm1
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm0
; CHECK-NEXT: vcvtdq2pd %xmm0, %ymm0
; CHECK-NEXT: vpmovm2d %k0, %ymm2
; CHECK-NEXT: vpsrld $31, %ymm2, %ymm2
; CHECK-NEXT: vcvtdq2pd %xmm2, %ymm3
; CHECK-NEXT: vextracti128 $1, %ymm2, %xmm2
; CHECK-NEXT: vcvtdq2pd %xmm2, %ymm2
; CHECK-NEXT: vmovaps %ymm2, 32(%rdi)
; CHECK-NEXT: vmovaps %ymm3, (%rdi)
; CHECK-NEXT: vmovaps %ymm0, 96(%rdi)
; CHECK-NEXT: vmovaps %ymm1, 64(%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

define void @ubto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {

```

```

; CHECK-LABEL: ubto16f64_512:
; CHECK:      # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: vpmovm2d %k0, %zmm0
; CHECK-NEXT: vpsrld $31, %zmm0, %zmm0
; CHECK-NEXT: vcvtdq2pd %ymm0, %zmm1
; CHECK-NEXT: vextracti64x4 $1, %zmm0, %ymm0
; CHECK-NEXT: vcvtdq2pd %ymm0, %zmm0
; CHECK-NEXT: vmovaps %zmm0, 64(%rdi)
; CHECK-NEXT: vmovaps %zmm1, (%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

```

```

define <16 x i16> @test_16f32toub_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-
width"="256" {

```

```

; CHECK-LABEL: test_16f32toub_256:
; CHECK:      # %bb.0:
; CHECK-NEXT: vcvttps2dq (%rdi), %ymm1
; CHECK-NEXT: vpslld $31, %ymm1, %ymm1
; CHECK-NEXT: vpmovd2m %ymm1, %k0
; CHECK-NEXT: vcvttps2dq 32(%rdi), %ymm1
; CHECK-NEXT: vpslld $31, %ymm1, %ymm1
; CHECK-NEXT: vpmovd2m %ymm1, %k1
; CHECK-NEXT: kunpckbw %k0, %k1, %k1
; CHECK-NEXT: vmovdqu16 %ymm0, %ymm0 {%k1} {z}
; CHECK-NEXT: retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptoui <16 x float> %a to <16 x i1>
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select
}

```

```

define <16 x i16> @test_16f32toub_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-
width"="512" {

```

```

; CHECK-LABEL: test_16f32toub_512:
; CHECK:      # %bb.0:
; CHECK-NEXT: vcvttps2dq (%rdi), %zmm1
; CHECK-NEXT: vpslld $31, %zmm1, %zmm1
; CHECK-NEXT: vpmovd2m %zmm1, %k1
; CHECK-NEXT: vmovdqu16 %ymm0, %ymm0 {%k1} {z}
; CHECK-NEXT: retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptoui <16 x float> %a to <16 x i1>

```

```

%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select
}

```

```

define <16 x i16> @test_16f32tosb_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-
width"="256" {
; CHECK-LABEL: test_16f32tosb_256:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vcvttps2dq (%rdi), %ymm1
; CHECK-NEXT:  vpmovd2m %ymm1, %k0
; CHECK-NEXT:  vcvttps2dq 32(%rdi), %ymm1
; CHECK-NEXT:  vpmovd2m %ymm1, %k1
; CHECK-NEXT:  kunpckbw %k0, %k1, %k1
; CHECK-NEXT:  vmovdqu16 %ymm0, %ymm0 {%k1} {z}
; CHECK-NEXT:  retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptosi <16 x float> %a to <16 x i1>
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select
}

```

```

define <16 x i16> @test_16f32tosb_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-
width"="512" {
; CHECK-LABEL: test_16f32tosb_512:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vcvttps2dq (%rdi), %zmm1
; CHECK-NEXT:  vpmovd2m %zmm1, %k1
; CHECK-NEXT:  vmovdqu16 %ymm0, %ymm0 {%k1} {z}
; CHECK-NEXT:  retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptosi <16 x float> %a to <16 x i1>
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select
}

```

```

define void @mul256(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="256" {
; CHECK-LABEL: mul256:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:  vmovdqa (%rsi), %ymm2
; CHECK-NEXT:  vmovdqa 32(%rsi), %ymm3
; CHECK-NEXT:  vpunpckhbw {{.*#+}} %ymm4 =
ymm2[8],ymm0[8],ymm2[9],ymm0[9],ymm2[10],ymm0[10],ymm2[11],ymm0[11],ymm2[12],ymm0[12],ymm2[13
],ymm0[13],ymm2[14],ymm0[14],ymm2[15],ymm0[15],ymm2[24],ymm0[24],ymm2[25],ymm0[25],ymm2[26],ym
m0[26],ymm2[27],ymm0[27],ymm2[28],ymm0[28],ymm2[29],ymm0[29],ymm2[30],ymm0[30],ymm2[31],ymm0[
31]
; CHECK-NEXT:  vpunpckhbw {{.*#+}} %ymm5 =

```



```

ymm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-NEXT: vpmullw %ymm4, %ymm5, %ymm4
; CHECK-NEXT: vmovdqa {{.*#+}} ymm5 =
[255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255]
; CHECK-NEXT: vpand %ymm5, %ymm4, %ymm4
; CHECK-NEXT: vpunpcklbw {{.*#+}} ymm2 =
ymm2[0],ymm0[0],ymm2[1],ymm0[1],ymm2[2],ymm0[2],ymm2[3],ymm0[3],ymm2[4],ymm0[4],ymm2[5],ymm0[
5],ymm2[6],ymm0[6],ymm2[7],ymm0[7],ymm2[16],ymm0[16],ymm2[17],ymm0[17],ymm2[18],ymm0[18],ymm2[
19],ymm0[19],ymm2[20],ymm0[20],ymm2[21],ymm0[21],ymm2[22],ymm0[22],ymm2[23],ymm0[23]
; CHECK-NEXT: vpunpcklbw {{.*#+}} ymm0 =
ymm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-NEXT: vpmullw %ymm2, %ymm0, %ymm0
; CHECK-NEXT: vpand %ymm5, %ymm0, %ymm0
; CHECK-NEXT: vpackuswb %ymm4, %ymm0, %ymm0
; CHECK-NEXT: vpunpckhbw {{.*#+}} ymm2 =
ymm3[8],ymm0[8],ymm3[9],ymm0[9],ymm3[10],ymm0[10],ymm3[11],ymm0[11],ymm3[12],ymm0[12],ymm3[13
],ymm0[13],ymm3[14],ymm0[14],ymm3[15],ymm0[15],ymm3[24],ymm0[24],ymm3[25],ymm0[25],ymm3[26],ym
m0[26],ymm3[27],ymm0[27],ymm3[28],ymm0[28],ymm3[29],ymm0[29],ymm3[30],ymm0[30],ymm3[31],ymm0[
31]
; CHECK-NEXT: vpunpckhbw {{.*#+}} ymm4 =
ymm1[8],ymm0[8],ymm1[9],ymm0[9],ymm1[10],ymm0[10],ymm1[11],ymm0[11],ymm1[12],ymm0[12],ymm1[13
],ymm0[13],ymm1[14],ymm0[14],ymm1[15],ymm0[15],ymm1[24],ymm0[24],ymm1[25],ymm0[25],ymm1[26],ym
m0[26],ymm1[27],ymm0[27],ymm1[28],ymm0[28],ymm1[29],ymm0[29],ymm1[30],ymm0[30],ymm1[31],ymm0[
31]
; CHECK-NEXT: vpmullw %ymm2, %ymm4, %ymm2
; CHECK-NEXT: vpand %ymm5, %ymm2, %ymm2
; CHECK-NEXT: vpunpcklbw {{.*#+}} ymm3 =
ymm3[0],ymm0[0],ymm3[1],ymm0[1],ymm3[2],ymm0[2],ymm3[3],ymm0[3],ymm3[4],ymm0[4],ymm3[5],ymm0[
5],ymm3[6],ymm0[6],ymm3[7],ymm0[7],ymm3[16],ymm0[16],ymm3[17],ymm0[17],ymm3[18],ymm0[18],ymm3[
19],ymm0[19],ymm3[20],ymm0[20],ymm3[21],ymm0[21],ymm3[22],ymm0[22],ymm3[23],ymm0[23]
; CHECK-NEXT: vpunpcklbw {{.*#+}} ymm1 =
ymm1[0],ymm0[0],ymm1[1],ymm0[1],ymm1[2],ymm0[2],ymm1[3],ymm0[3],ymm1[4],ymm0[4],ymm1[5],ymm0[
5],ymm1[6],ymm0[6],ymm1[7],ymm0[7],ymm1[16],ymm0[16],ymm1[17],ymm0[17],ymm1[18],ymm0[18],ymm1[
19],ymm0[19],ymm1[20],ymm0[20],ymm1[21],ymm0[21],ymm1[22],ymm0[22],ymm1[23],ymm0[23]
; CHECK-NEXT: vpmullw %ymm3, %ymm1, %ymm1
; CHECK-NEXT: vpand %ymm5, %ymm1, %ymm1
; CHECK-NEXT: vpackuswb %ymm2, %ymm1, %ymm1
; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT: vmovdqa %ymm0, (%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%d = load <64 x i8>, <64 x i8>* %a
%e = load <64 x i8>, <64 x i8>* %b
%f = mul <64 x i8> %d, %e
store <64 x i8> %f, <64 x i8>* %c
ret void
}

```

```

define void @mul512(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="512" {
; CHECK-LABEL: mul512:
; CHECK:      # %bb.0:
; CHECK-NEXT:  vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT:  vmovdqa64 (%rsi), %zmm1
; CHECK-NEXT:  vpunpckhbw {{.*#+}} zmm2 =
zmm1[8],zmm0[8],zmm1[9],zmm0[9],zmm1[10],zmm0[10],zmm1[11],zmm0[11],zmm1[12],zmm0[12],zmm1[13],
zmm0[13],zmm1[14],zmm0[14],zmm1[15],zmm0[15],zmm1[24],zmm0[24],zmm1[25],zmm0[25],zmm1[26],zmm0
[26],zmm1[27],zmm0[27],zmm1[28],zmm0[28],zmm1[29],zmm0[29],zmm1[30],zmm0[30],zmm1[31],zmm0[31],z
mm1[40],zmm0[40],zmm1[41],zmm0[41],zmm1[42],zmm0[42],zmm1[43],zmm0[43],zmm1[44],zmm0[44],zmm1[
45],zmm0[45],zmm1[46],zmm0[46],zmm1[47],zmm0[47],zmm1[56],zmm0[56],zmm1[57],zmm0[57],zmm1[58],z
mm0[58],zmm1[59],zmm0[59],zmm1[60],zmm0[60],zmm1[61],zmm0[61],zmm1[62],zmm0[62],zmm1[63],zmm0[
63]
; CHECK-NEXT:  vpunpckhbw {{.*#+}} zmm3 =
zmm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,4
2,42,43,43,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63]
; CHECK-NEXT:  vpmullw %zmm2, %zmm3, %zmm2
; CHECK-NEXT:  vmovdqa64 {{.*#+}} zmm3 =
[255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,2
55,255,255,255,255]
; CHECK-NEXT:  vpandq %zmm3, %zmm2, %zmm2
; CHECK-NEXT:  vpunpcklbw {{.*#+}} zmm1 =
zmm1[0],zmm0[0],zmm1[1],zmm0[1],zmm1[2],zmm0[2],zmm1[3],zmm0[3],zmm1[4],zmm0[4],zmm1[5],zmm0[5]
,zmm1[6],zmm0[6],zmm1[7],zmm0[7],zmm1[16],zmm0[16],zmm1[17],zmm0[17],zmm1[18],zmm0[18],zmm1[19]
,zmm0[19],zmm1[20],zmm0[20],zmm1[21],zmm0[21],zmm1[22],zmm0[22],zmm1[23],zmm0[23],zmm1[32],zmm
0[32],zmm1[33],zmm0[33],zmm1[34],zmm0[34],zmm1[35],zmm0[35],zmm1[36],zmm0[36],zmm1[37],zmm0[37],
zmm1[38],zmm0[38],zmm1[39],zmm0[39],zmm1[48],zmm0[48],zmm1[49],zmm0[49],zmm1[50],zmm0[50],zmm1
[51],zmm0[51],zmm1[52],zmm0[52],zmm1[53],zmm0[53],zmm1[54],zmm0[54],zmm1[55],zmm0[55]
; CHECK-NEXT:  vpunpcklbw {{.*#+}} zmm0 =
zmm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23,32,32,33,33,34,34,35,35,36,3
6,37,37,38,38,39,39,48,48,49,49,50,50,51,51,52,52,53,53,54,54,55,55]
; CHECK-NEXT:  vpmullw %zmm1, %zmm0, %zmm0
; CHECK-NEXT:  vpandq %zmm3, %zmm0, %zmm0
; CHECK-NEXT:  vpackuswb %zmm2, %zmm0, %zmm0
; CHECK-NEXT:  vmovdqa64 %zmm0, (%rdx)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%d = load <64 x i8>, <64 x i8>* %a
%e = load <64 x i8>, <64 x i8>* %b
%f = mul <64 x i8> %d, %e
store <64 x i8> %f, <64 x i8>* %c
ret void
}

```

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Yixuan Qiu <yixuanq@gmail.com>  
Yusuke Suzuki <utatane.tea@gmail.com>  
Zbigniew Skowron <zbychs@gmail.com>  
; RUN: opt -consthoist -S -o - %s | FileCheck %s  
target datalayout = "e-m:e-p:32:32-i64:64-v128:64:128-a:0:32-n32-S64"  
target triple = "thumbv6m-none--musleabi"

; Check that for i8 type, the maximum legal offset is 31.  
; Also check that an constant used as value to be stored rather than  
; pointer in a store instruction is hoisted.  
; CHECK: foo\_i8  
; CHECK-DAG: %[[C1:const[0-9]?]] = bitcast i32 805874720 to i32  
; CHECK-DAG: %[[C2:const[0-9]?]] = bitcast i32 805874688 to i32  
; CHECK-DAG: %[[C3:const[0-9]?]] = bitcast i32 805873720 to i32  
; CHECK-DAG: %[[C4:const[0-9]?]] = bitcast i32 805873688 to i32  
; CHECK: %0 = inttoptr i32 %[[C2]] to i8\*  
; CHECK-NEXT: %1 = load volatile i8, i8\* %0  
; CHECK-NEXT: %[[M1:const\_mat[0-9]?]] = add i32 %[[C2]], 4  
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to i8\*  
; CHECK-NEXT: %3 = load volatile i8, i8\* %2

```

; CHECK-NEXT: %[[M2:const_mat[0-9]?]] = add i32 %[[C2]], 31
; CHECK-NEXT: %4 = inttoptr i32 %[[M2]] to i8*
; CHECK-NEXT: %5 = load volatile i8, i8* %4
; CHECK-NEXT: %6 = inttoptr i32 %[[C1]] to i8*
; CHECK-NEXT: %7 = load volatile i8, i8* %6
; CHECK-NEXT: %[[M3:const_mat[0-9]?]] = add i32 %[[C1]], 7
; CHECK-NEXT: %8 = inttoptr i32 %[[M3]] to i8*
; CHECK-NEXT: %9 = load volatile i8, i8* %8
; CHECK-NEXT: %10 = inttoptr i32 %[[C4]] to i8*
; CHECK-NEXT: store i8 %9, i8* %10
; CHECK-NEXT: %[[M4:const_mat[0-9]?]] = add i32 %[[C4]], 31
; CHECK-NEXT: %11 = inttoptr i32 %[[M4]] to i8*
; CHECK-NEXT: store i8 %7, i8* %11
; CHECK-NEXT: %12 = inttoptr i32 %[[C3]] to i8*
; CHECK-NEXT: store i8 %5, i8* %12
; CHECK-NEXT: %[[M5:const_mat[0-9]?]] = add i32 %[[C3]], 7
; CHECK-NEXT: %13 = inttoptr i32 %[[M5]] to i8*
; CHECK-NEXT: store i8 %3, i8* %13
; CHECK-NEXT: %[[M6:const_mat[0-9]?]] = add i32 %[[C1]], 80
; CHECK-NEXT: %14 = inttoptr i32 %[[M6]] to i8*
; CHECK-NEXT: store i8* %14, i8** @goo

```

```
@goo = global i8* undef
```

```

define void @foo_i8() {
entry:
%0 = load volatile i8, i8* inttoptr (i32 805874688 to i8*)
%1 = load volatile i8, i8* inttoptr (i32 805874692 to i8*)
%2 = load volatile i8, i8* inttoptr (i32 805874719 to i8*)
%3 = load volatile i8, i8* inttoptr (i32 805874720 to i8*)
%4 = load volatile i8, i8* inttoptr (i32 805874727 to i8*)
store i8 %4, i8* inttoptr(i32 805873688 to i8*)
store i8 %3, i8* inttoptr(i32 805873719 to i8*)
store i8 %2, i8* inttoptr(i32 805873720 to i8*)
store i8 %1, i8* inttoptr(i32 805873727 to i8*)
store i8* inttoptr(i32 805874800 to i8*), i8** @goo
ret void
}

```

```

; Check that for i16 type, the maximum legal offset is 62.
; CHECK: foo_i16
; CHECK-DAG: %[[C1:const[0-9]?]] = bitcast i32 805874752 to i32
; CHECK-DAG: %[[C2:const[0-9]?]] = bitcast i32 805874688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to i16*
; CHECK-NEXT: %1 = load volatile i16, i16* %0, align 2
; CHECK-NEXT: %[[M1:const_mat[0-9]?]] = add i32 %[[C2]], 4
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to i16*
; CHECK-NEXT: %3 = load volatile i16, i16* %2, align 2

```

```

; CHECK-NEXT: %[[M2:const_mat[0-9]?]] = add i32 %[[C2]], 32
; CHECK-NEXT: %4 = inttoptr i32 %[[M2]] to i16*
; CHECK-NEXT: %5 = load volatile i16, i16* %4, align 2
; CHECK-NEXT: %[[M3:const_mat[0-9]?]] = add i32 %[[C2]], 62
; CHECK-NEXT: %6 = inttoptr i32 %[[M3]] to i16*
; CHECK-NEXT: %7 = load volatile i16, i16* %6, align 2
; CHECK-NEXT: %8 = inttoptr i32 %[[C1]] to i16*
; CHECK-NEXT: %9 = load volatile i16, i16* %8, align 2
; CHECK-NEXT: %[[M4:const_mat[0-9]?]] = add i32 %[[C1]], 22
; CHECK-NEXT: %10 = inttoptr i32 %[[M4]] to i16*
; CHECK-NEXT: %11 = load volatile i16, i16* %10, align 2

```

```

define void @foo_i16() {
entry:
%0 = load volatile i16, i16* inttoptr (i32 805874688 to i16*), align 2
%1 = load volatile i16, i16* inttoptr (i32 805874692 to i16*), align 2
%2 = load volatile i16, i16* inttoptr (i32 805874720 to i16*), align 2
%3 = load volatile i16, i16* inttoptr (i32 805874750 to i16*), align 2
%4 = load volatile i16, i16* inttoptr (i32 805874752 to i16*), align 2
%5 = load volatile i16, i16* inttoptr (i32 805874774 to i16*), align 2
ret void
}

```

```

; Check that for i32 type, the maximum legal offset is 124.
; CHECK: foo_i32
; CHECK-DAG: %[[C1:const[0-9]?]] = bitcast i32 805874816 to i32
; CHECK-DAG: %[[C2:const[0-9]?]] = bitcast i32 805874688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to i32*
; CHECK-NEXT: %1 = load volatile i32, i32* %0, align 4
; CHECK-NEXT: %[[M1:const_mat[0-9]?]] = add i32 %[[C2]], 4
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to i32*
; CHECK-NEXT: %3 = load volatile i32, i32* %2, align 4
; CHECK-NEXT: %[[M2:const_mat[0-9]?]] = add i32 %[[C2]], 124
; CHECK-NEXT: %4 = inttoptr i32 %[[M2]] to i32*
; CHECK-NEXT: %5 = load volatile i32, i32* %4, align 4
; CHECK-NEXT: %6 = inttoptr i32 %[[C1]] to i32*
; CHECK-NEXT: %7 = load volatile i32, i32* %6, align 4
; CHECK-NEXT: %[[M3:const_mat[0-9]?]] = add i32 %[[C1]], 8
; CHECK-NEXT: %8 = inttoptr i32 %[[M3]] to i32*
; CHECK-NEXT: %9 = load volatile i32, i32* %8, align 4
; CHECK-NEXT: %[[M4:const_mat[0-9]?]] = add i32 %[[C1]], 12
; CHECK-NEXT: %10 = inttoptr i32 %[[M4]] to i32*
; CHECK-NEXT: %11 = load volatile i32, i32* %10, align 4

```

```

define void @foo_i32() {
entry:
%0 = load volatile i32, i32* inttoptr (i32 805874688 to i32*), align 4
%1 = load volatile i32, i32* inttoptr (i32 805874692 to i32*), align 4

```

```
%2 = load volatile i32, i32* inttoptr (i32 805874812 to i32*), align 4
%3 = load volatile i32, i32* inttoptr (i32 805874816 to i32*), align 4
%4 = load volatile i32, i32* inttoptr (i32 805874824 to i32*), align 4
%5 = load volatile i32, i32* inttoptr (i32 805874828 to i32*), align 4
ret void
}
```

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-----

```
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\*/

```
; RUN: llc < %s -mtriple=s390x-linux-gnu -mcpu=zEC12 -verify-machineinstrs | FileCheck %s
```

```
;
```

```
; Test that early if conversion produces LOCR with operands of the right
```

```
; register classes.
```

```
define void @autogen_SD4739(i8*) {
```

```
; CHECK-NOT: Expected a GR32Bit register, but got a GRX32Bit register
```

```
BB:
```

```
%L34 = load i8, i8* %0
```

```
%Cmp56 = icmp sgt i8 undef, %L34
```

```
br label %CF246
```

```
CF246:                                ; preds = %CF246, %BB
```

```
%S1163 = select i1 %Cmp56, i8 %L34, i8 undef
```

```
br i1 undef, label %CF246, label %CF248
```

```
CF248:                                ; preds = %CF248, %CF246
```

```
store i8 %S1163, i8* %0
```

```
br label %CF248
```

```
}
```

## 1.138 websocket 1.1.FR

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## 1.148 go-toml 1.3.0

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In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (see <http://www.digicool.com>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Digital Creations is a sponsoring member of the PSF.

All Python releases are Open Source (see <http://www.opensource.org> for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

| Release from   | Derived from | Year      | Owner      | GPL-compatible? (1) |
|----------------|--------------|-----------|------------|---------------------|
| 0.9.0 thru 1.2 |              | 1991-1995 | CWI        | yes                 |
| 1.3 thru 1.5.2 | 1.2          | 1995-1999 | CNRI       | yes                 |
| 1.6            | 1.5.2        | 2000      | CNRI       | no                  |
| 2.0            | 1.6          | 2000      | BeOpen.com | no                  |
| 1.6.1          | 1.6          | 2001      | CNRI       | no                  |
| 2.1            | 2.0+1.6.1    | 2001      | PSF        | no                  |
| 2.0.1          | 2.0+1.6.1    | 2001      | PSF        | yes                 |
| 2.1.1          | 2.1+2.0.1    | 2001      | PSF        | yes                 |
| 2.2            | 2.1.1        | 2001      | PSF        | yes                 |
| 2.2.1          | 2.2          | 2002      | PSF        | yes                 |

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## 1.151 unbound 1.8.1rc1

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# 1.161 python 2.6.4

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### A. HISTORY OF THE SOFTWARE

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see <http://www.zope.com>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

All Python releases are Open Source (see <http://www.opensource.org> for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

| Release        | Derived from | Year      | Owner      | GPL-compatible? (1) |
|----------------|--------------|-----------|------------|---------------------|
| 0.9.0 thru 1.2 |              | 1991-1995 | CWI        | yes                 |
| 1.3 thru 1.5.2 | 1.2          | 1995-1999 | CNRI       | yes                 |
| 1.6            | 1.5.2        | 2000      | CNRI       | no                  |
| 2.0            | 1.6          | 2000      | BeOpen.com | no                  |
| 1.6.1          | 1.6          | 2001      | CNRI       | yes (2)             |
| 2.1            | 2.0+1.6.1    | 2001      | PSF        | no                  |
| 2.0.1          | 2.0+1.6.1    | 2001      | PSF        | yes                 |
| 2.1.1          | 2.1+2.0.1    | 2001      | PSF        | yes                 |
| 2.2            | 2.1.1        | 2001      | PSF        | yes                 |



|       |       |           |     |     |
|-------|-------|-----------|-----|-----|
| 2.1.2 | 2.1.1 | 2002      | PSF | yes |
| 2.1.3 | 2.1.2 | 2002      | PSF | yes |
| 2.2.1 | 2.2   | 2002      | PSF | yes |
| 2.2.2 | 2.2.1 | 2002      | PSF | yes |
| 2.2.3 | 2.2.2 | 2003      | PSF | yes |
| 2.3   | 2.2.2 | 2002-2003 | PSF | yes |
| 2.3.1 | 2.3   | 2002-2003 | PSF | yes |
| 2.3.2 | 2.3.1 | 2002-2003 | PSF | yes |
| 2.3.3 | 2.3.2 | 2002-2003 | PSF | yes |
| 2.3.4 | 2.3.3 | 2004      | PSF | yes |
| 2.3.5 | 2.3.4 | 2005      | PSF | yes |
| 2.4   | 2.3   | 2004      | PSF | yes |
| 2.4.1 | 2.4   | 2005      | PSF | yes |
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| 2.5   | 2.4   | 2006      | PSF | yes |
| 2.5.1 | 2.5   | 2007      | PSF | yes |
| 2.5.2 | 2.5.1 | 2008      | PSF | yes |
| 2.5.3 | 2.5.2 | 2008      | PSF | yes |
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| 2.6.1 | 2.6   | 2008      | PSF | yes |

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Thanks to the many outside volunteers who have worked under Guido's direction to make these releases possible.

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*
* This module serves to replace the original parser module written
* by Guido. The functionality is not matched precisely, but the
* original may be implemented on top of this. This is desirable
* since the source of the text to be parsed is now divorced from
* this interface.
*
* Unlike the prior interface, the ability to give a parse tree
* produced by Python code as a tuple to the compiler is enabled by
* this module. See the documentation for more details.
*
* I've added some annotations that help with the lint code-checking
* program, but they're not complete by a long shot. The real errors
* that lint detects are gone, but there are still warnings with
* Py_[X]DECREF() and Py_[X]INCREf() macros. The lint annotations
* look like "NOTE(...)".
*/
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A C-program for MT19937, with initialization improved 2002/1/26.

Coded by Takuji Nishimura and Makoto Matsumoto.

Before using, initialize the state by using `init_genrand(seed)`  
or `init_by_array(init_key, key_length)`.

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## History and License

\*\*\*\*\*

### History of the software

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl/>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us/>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation; see <http://www.zope.com/>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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| 0.9.0 thru 1.2 | n/a          | 1991-1995 | CWI   | yes             |
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|-------|-----------|-----------|------------|-----|--|
| 1.6   | 1.5.2     | 2000      | CNRI       | no  |  |
| 2.0   | 1.6       | 2000      | BeOpen.com | no  |  |
| 1.6.1 | 1.6       | 2001      | CNRI       | no  |  |
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| 2.0.1 | 2.0+1.6.1 | 2001      | PSF        | yes |  |
| 2.1.1 | 2.1+2.0.1 | 2001      | PSF        | yes |  |
| 2.2   | 2.1.1     | 2001      | PSF        | yes |  |
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| 2.2.1 | 2.2       | 2002      | PSF        | yes |  |
| 2.2.2 | 2.2.1     | 2002      | PSF        | yes |  |
| 2.2.3 | 2.2.2     | 2002-2003 | PSF        | yes |  |
| 2.3   | 2.2.2     | 2002-2003 | PSF        | yes |  |
| 2.3.1 | 2.3       | 2002-2003 | PSF        | yes |  |
| 2.3.2 | 2.3.1     | 2003      | PSF        | yes |  |
| 2.3.3 | 2.3.2     | 2003      | PSF        | yes |  |
| 2.3.4 | 2.3.3     | 2004      | PSF        | yes |  |
| 2.3.5 | 2.3.4     | 2005      | PSF        | yes |  |
| 2.4   | 2.3       | 2004      | PSF        | yes |  |
| 2.4.1 | 2.4       | 2005      | PSF        | yes |  |
| 2.4.2 | 2.4.1     | 2005      | PSF        | yes |  |
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| 2.5   | 2.4       | 2006      | PSF        | yes |  |

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|-------|-------|------|-----|-----|--|
| 2.5.1 | 2.5   | 2007 | PSF | yes |  |
| 2.5.2 | 2.5.1 | 2008 | PSF | yes |  |
| 2.5.3 | 2.5.2 | 2008 | PSF | yes |  |
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#### Mersenne Twister

-----

The `:mod:'_random'` module includes code based on a download from <http://www.math.keio.ac.jp/matsumoto/MT2002/emt19937ar.html>. The following are the verbatim comments from the original code::

A C-program for MT19937, with initialization improved 2002/1/26.  
Coded by Takuji Nishimura and Makoto Matsumoto.

Before using, initialize the state by using `init_genrand(seed)`  
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Any feedback is very welcome.

<http://www.math.keio.ac.jp/matumoto/emt.html>

email: [matumoto@math.keio.ac.jp](mailto:matumoto@math.keio.ac.jp)

Sockets

-----

The `:mod:'socket'` module uses the functions, `:func:'getaddrinfo'`, and `:func:'getnameinfo'`, which are coded in separate source files from the WIDE Project, <http://www.wide.ad.jp/>. ::

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Floating point exception control  
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MD5 message digest algorithm  
-----

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L. Peter Deutsch  
ghost@aladdin.com

Independent implementation of MD5 (RFC 1321).

This code implements the MD5 Algorithm defined in RFC 1321, whose text is available at

<http://www.ietf.org/rfc/rfc1321.txt>

The code is derived from the text of the RFC, including the test suite (section A.5) but excluding the rest of Appendix A. It does not include any code or documentation that is identified in the RFC as being copyrighted.

The original and principal author of md5.h is L. Peter Deutsch <ghost@aladdin.com>. Other authors are noted in the change history that follows (in reverse chronological order):

- 2002-04-13 lpd Removed support for non-ANSI compilers; removed references to Ghostscript; clarified derivation from RFC 1321; now handles byte order either statically or dynamically.
- 1999-11-04 lpd Edited comments slightly for automatic TOC extraction.

1999-10-18 lpd Fixed typo in header comment (ansi2knr rather than md5);  
added conditionalization for C++ compilation from Martin  
Purschke <purschke@bnl.gov>.  
1999-05-03 lpd Original version.

#### Asynchronous socket services

-----

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UUencode and UUdecode functions  
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Modified by Jack Jansen, CWI, July 1995:

- Use binascii module to do the actual line-by-line conversion between ascii and binary. This results in a 1000-fold speedup. The C version is still 5 times faster, though.
- Arguments more compliant with python standard

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-----

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test\_epoll

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Select kqueue

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## 1.163 tiger-types 1.4

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## 1.170 gnu-crypto 2.0.1

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## 1.174 samba 4.5.8

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./dns/e164.py PYTHON 2006,2007,2009,2011  
./dns/edns.py PYTHON 2009,2011  
./dns/entropy.py PYTHON 2009,2011  
./dns/exception.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
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./dns/hash.py PYTHON 2011  
./dns/inet.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
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./dns/ipv6.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
./dns/message.py PYTHON 2001,2002,2003,2004,2005,2006,2007,2009,2010,2011  
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./dns/namedict.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
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./dns/opcode.py PYTHON 2001,2002,2003,2004,2005,2006,2007,2009,2010,2011  
./dns/query.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
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./dns/rdata.py PYTHON 2001,2002,2003,2004,2005,2006,2007,2009,2010,2011  
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./dns/rdtypes/ANY/CNAME.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
./dns/rdtypes/ANY/DLV.py PYTHON 2009,2010,2011  
./dns/rdtypes/ANY/DNAME.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
./dns/rdtypes/ANY/DNSKEY.py PYTHON 2004,2005,2006,2007,2009,2010,2011  
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./dns/rdtypes/IN/APL.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
./dns/rdtypes/IN/DHCID.py PYTHON 2006,2007,2009,2010,2011  
./dns/rdtypes/IN/IPSECKEY.py PYTHON 2006,2007,2009,2010,2011  
./dns/rdtypes/IN/KX.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
./dns/rdtypes/IN/NAPTR.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
./dns/rdtypes/IN/NSAP.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
./dns/rdtypes/IN/NSAP\_PTR.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
./dns/rdtypes/IN/PX.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
./dns/rdtypes/IN/SRV.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
./dns/rdtypes/IN/WKS.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
./dns/rdtypes/IN/\_\_init\_\_.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
./dns/rdtypes/\_\_init\_\_.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
./dns/rdtypes/dsbase.py PYTHON 2010,2011  
./dns/rdtypes/mxbase.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
./dns/rdtypes/nsbase.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
./dns/rdtypes/txtbase.py PYTHON 2006,2007,2009,2010,2011  
./dns/renderer.py PYTHON 2001,2002,2003,2004,2005,2006,2007,2009,2010,2011  
./dns/resolver.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
./dns/reversename.py PYTHON 2006,2007,2009,2010,2011  
./dns/rrset.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
./dns/set.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
./dns/tokenizer.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
./dns/tsig.py PYTHON 2001,2002,2003,2004,2005,2006,2007,2009,2010,2011  
./dns/tsigkeyring.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
./dns/ttl.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
./dns/update.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
./dns/version.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011

./dns/wiredata.py PYTHON 2011  
./dns/zone.py PYTHON 2003,2004,2005,2006,2007,2009,2010,2011  
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./examples/e164.py X 2006,2007  
./examples/mx.py X 2003,2004,2005,2006,2007  
./examples/name.py X 2003,2004,2005,2006,2007  
./examples/reverse.py X 2003,2004,2005,2006,2007  
./examples/reverse\_name.py X 2006,2007  
./examples/xfr.py X 2003,2004,2005,2006,2007  
./examples/zonediff.py X 2010,2011  
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./tests/Makefile MAKE 2003,2004,2005,2006,2007,2009,2010,2011  
./tests/bugs.py PYTHON 2006,2007,2009,2010,2011  
./tests/dnssec.py PYTHON 2011  
./tests/example X 2003,2004,2005,2006,2007  
./tests/example1.good X 2003,2004,2005,2006,2007  
./tests/example2.good X 2003,2004,2005,2006,2007  
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-- Tom St Denis

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Version 3, 29 June 2007

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```

```
@vskip 20pt plus 1fil
```

```
@end macro
```

```
@macro copyrightstart{ }
```

```
@end macro
```

```
@macro copyrightend{ }
```

```
@end macro
```

```
@node Copyrights and Licenses, , Acknowledgments, Top
```

```
@comment node-name, next, previous, up
```

```
@appendix Copyrights and Licenses
```

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AES in libcrypto

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rijndael-alg-fst.c

@version 3.0 (December 2000)

Optimised ANSI C code for the Rijndael cipher (now AES)

@author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>

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DES core in libhcrypto

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D3DES (V5.09) -

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Thanks to: Dan Hoey for his excellent Initial and Inverse permutation code; Jim Gillogly & Phil Karn for the DES key schedule code; Dennis Ferguson, Eric Young and Dana How for comparing notes; and Ray Lau, for humouring me on.

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lib/hcrypto/test\_dh.c

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## 1.178 graphviz 2.38.0

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p {
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    margin-top: 0.5em;
    margin-bottom: 0.5em;
```

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}
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margin-left: 0.5in;
margin-top: 0.05em;
margin-bottom: 0.05em;
}
</style>
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becomes

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Ty Coon, President of Vice

That's all there is to it!

```
digraph G {
  rankdir="LR";
  label = "label demo";
  labeljust="left";
  subgraph cluster0 {
    a;
    b;
    a->b;
    label = "cluster0";
    labelloc="bottom"
  }
```

```
subgraph cluster1 {
  c;
  d;
  c->d;
  label = "cluster1";
  labelloc="top"
}
```

This package was originally debianized by Stephen M Moraco <stephen@debian.org>  
on Fri, 31 Dec 1999 15:37:17 -0700.

It was downloaded from <http://www.graphviz.org/>

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The Debian packaging was started by Stephen M Moraco <stephen@debian.org> and then was taken over in late 2006 by Cyril Brulebois, placing his work under the same terms as graphviz itself.

2006-2008, Cyril Brulebois <cyril.brulebois@enst-bretagne.fr>

```
digraph G {
  rankdir="LR";
  label = "label demo";
  subgraph cluster0 {
    a;
    b;
    a->b;
    label = "cluster0";
    labeljust="left";
  }
  subgraph cluster1 {
    c;
    d;
    c->d;
    label = "cluster1";
  }
}
```

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# 1.179 zlib 1.2.3

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```
/* zlib.h -- interface of the 'zlib' general purpose compression library
   version 1.2.11, January 15th, 2017
```

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jloup@gzip.org      madler@alumni.caltech.edu

```
*/
```

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## 1.182 hk2-utils 2.4.0

## 1.183 fsnotify 1.4.7

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## 1.184 bison 3.0.4

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```
#!/usr/bin/perl -0777 -pi
```

```
# Update b4_copyright invocations or b4_copyright_years definitions to  
# include the current year.
```

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```
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```



```

use strict;
use warnings;

my $margin = 72;

my $this_year = $ENV{UPDATE_COPYRIGHT_YEAR};
if (!$this_year || $this_year !~ m/^\d{4}$/)
{
    my ($sec, $min, $hour, $mday, $month, $year) = localtime (time ());
    $this_year = $year + 1900;
}
my $old_re = <<'EOF'
(
    (?^\n)
    #BEFORE
    (?
        b4_copyright\(\[[^\]]*\]
        | m4_(?:push|pop)def\(\[b4_copyright_years]
    )
    #AFTER
)
, \s*
(
    \[ \s* (?:\d{4}(?:,\s*|-))* (\d{4}) \s* ]
)
)?
\
EOF
;

while (/( $old_re )/gx)
{
    my $start = pos() - length ($1);
    my $b4_copyright_line = $2;
    my $year_lines = $3;
    my $final_year = $4;
    $year_lines .= ')';

    # If there was a second argument, it contains years, so update them.
    if ($final_year)
    {
        $b4_copyright_line .= ';';
        if ($final_year != $this_year)
        {
            # Update the year.
            $year_lines =~ s/$final_year/$final_year, $this_year/;
        }
    }
}

```

```

# Normalize all whitespace.
$year_lines =~ s/\s+/ /g;

# Put spaces after commas.
$year_lines =~ s/, ?/, /g;

# Compress to intervals.
$year_lines =~
s/
(\d{4})
(?:
(,|-)
((?){
  if ($2 eq '-') { '\d{4}'; }
  elsif (!$3)    { $1 + 1; }
  else          { $3 + 1; }
}))
)+
/$1-$3/gx;

# Format within margin.
my $year_lines_new;
my $indent = index ($b4_copyright_line, '[');
--$indent if ($b4_copyright_line =~ m/^\n/);
while (length $year_lines)
{
  my $text_margin = $margin - $indent;
  if (($year_lines =~ s/^(.{1,$text_margin})(?: |$)//)
      || ($year_lines =~ s/^([\S]+)(?: |$)//))
  {
    my $line = "\n" . ('x'$indent) . $1;
    ++$indent if (!$year_lines_new);
    $year_lines_new .= $line;
  }
  else
  {
    # Should be unreachable, but we don't want an infinite
    # loop if it can be reached.
    die;
  }
}

# Replace the old invocation. Should never die.
die if (!s/$old_re/G/$b4_copyright_line$year_lines_new/x);

# Prepare for the next search.
pos () = $start + length (" $b4_copyright_line$year_lines_new");

```

```

    }
}

while (/(\bb4_copyright\()/g)
{
    my $start = pos () - length ($1);
    my $end = pos ();
    my $re = $old_re;
    pos () = $start;
    $re =~ s/\#BEFORE\G/;
    if (!$re/x)
    {
        my $line = (substr ($_, 0, $start) =~ s/\n\n/g) + 1;
        print STDERR
            "$ARGV:$line: warning: failed to update a b4_copyright\n";
    }
    pos () = $end;
}

```

```

while (/(\[b4_copyright_years])/g)
{
    my $start = pos () - length ($1);
    my $end = pos ();
    my $re = $old_re;
    $re =~ s/\#AFTER\G/;
    if (!$re/x)
    {
        # The substr operation blows away pos (), so restoring pos ()
        # at the end is necessary.
        my $line = (substr ($_, 0, $start) =~ s/\n\n/g) + 1;
        print STDERR
            "$ARGV:$line: warning: failed to update a"
            ". " b4_copyright_years\n";
    }
    pos () = $end;
}

```

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Version 3, 29 June 2007

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## 1.189 jemalloc 5.1.0

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-----

## 1.190 perl 5.24.1

### 1.190.1 Available under license :

```
#!/perl
```

```
=head1 NAME
```

```
copyright.t
```

```
=head1 DESCRIPTION
```

Tests that the latest copyright years in the top-level README file and the C<perl -v> output match each other.

If the test fails, update at least one of README and perl.c so that they match reality.

Optionally you can pass the C<--now> option to check they are at the current

year. This isn't checked by default, so that it doesn't fail for people working on older releases. It should be run before making a new release.

```
=cut
```

```
use strict;
```

```
use Config;
```

```
BEGIN { require './test.pl' }
```

```
if ( $Config{usecrosscompile} ) {
```

```
    skip_all( "Not all files are available during cross-compilation" );
```

```
}
```

```
my ($opt) = @ARGV;
```

```
my $readme_year = readme_year();
```

```
my $v_year = v_year();
```

```
# Check that both copyright dates are up-to-date, but only if requested, so
```

```
# that tests still pass for people intentionally working on older versions:
```

```
if ($opt eq '--now')
```

```
{
```

```
    my $current_year = (gmtime)[5] + 1900;
```

```
    is $v_year, $current_year, 'perl -v copyright includes current year';
```

```
    is $readme_year, $current_year, 'README copyright includes current year';
```

```
}
```

```
# Otherwise simply check that the two copyright dates match each other:
```

```
else
```

```
{
```

```
    is $readme_year, $v_year, 'README and perl -v copyright dates match';
```

```
}
```

```
done_testing;
```

```
sub readme_year
```

```
# returns the latest copyright year from the top-level README file
```

```
{
```

```
    open my $readme, '<', './README' or die "Opening README failed: $!";
```

```
# The copyright message is the first paragraph:
```

```
    local $/ = "";
```

```
    my $copyright_msg = <$readme>;
```

```
    my ($year) = $copyright_msg =~ /\b(\d{4,})/s
```

```
        or die "Year not found in README copyright message '$copyright_msg'";
```

```
$year;  
}
```

```
sub v_year  
# returns the latest copyright year shown in perl -v  
{  
  
my $output = runperl switches => ['-v'];  
my ($year) = $output =~ /copyright 1987.*\b(\d{4,})/i  
    or die "Copyright statement not found in perl -v output '$output'";  
  
$year;  
}
```

```
$year;  
}
```

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```
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under certain conditions; type `show c' for details.
```

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```
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program `Gnomovision' (a program to direct compilers to make passes
at assemblers) written by James Hacker.
```

```
<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice
```

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The End

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abstract: 'Build and install Perl modules'

author:

- 'Ken Williams <kwilliams@cpan.org>'

- "Development questions, bug reports, and patches should be sent to the\nModule-Build mailing list at <module-build@perl.org>."

build\_requires:

File::Temp: 0.15

Test::Harness: 3.16

Test::More: 0.49

generated\_by: 'Module::Build version 0.3608'

license: gpl

meta-spec:

url: <http://module-build.sourceforge.net/META-spec-v1.4.html>

version: 1.4

name: Module-Build

resources:

MailingList: <mailto:module-build@perl.org>

license: <http://dev.perl.org/licenses/>

repository: <http://github.com/dagolden/module-build/>

version: 3

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Julian Seward, [jseward@bzip.org](mailto:jseward@bzip.org)  
bzip2/libbzip2 version 1.0.6 of 6 September 2010

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## 1.191 cast 1.3.0

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## 1.193 jsp 2.3.FR

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GIMP Palette

Name: custompalette

Columns: 4

#

0 0 0 Index 3

65 38 30 Index 4

103 62 49 Index 6

79 73 72 Index 7

114 101 97 Index 8

208 127 100 Index 9

151 144 142 Index 10

221 207 199 Index 11

GIMP Palette

Name: badpalettefile

Columns: 4

#

0 0 0 Index 3

012345678901234567890123456789012345678901234567890123456789012345678901234567890123456789012

34567890123456789

103 62 49 Index 6

79 73 72 Index 7

114 101 97 Index 8

208 127 100 Index 9

151 144 142 Index 10

221 207 199 Index 11

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#### GIMP Palette

Name: badpaletteentry

Columns: 4

#

0 0 0 Index 3

65 38

103 62 49 Index 6

79 73 72 Index 7

114 101 97 Index 8

208 127 100 Index 9

151 144 142 Index 10

221 207 199 Index 11

#### GIMP Palette

Name: Test

Columns: 0

#

# 1.195 elfutils 0.168

## 1.195.1 Available under license :

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Version 3, 29 June 2007

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## 1.196 freebsd 10.4

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- LZ4 homepage : <http://fastcompression.blogspot.com/p/lz4.html>

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## 1.200 unbound 1.5.7

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## 1.201 properties 1.8.0

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goproperties - properties file decoder for Go

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## 1.203 libjpeg 6b

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## 1.203.2 Available under license :

The Independent JPEG Group's JPEG software

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README for release 6b of 27-Mar-1998

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This software is the work of Tom Lane, Philip Gladstone, Jim Boucher, Lee Crocker, Julian Minguillon, Luis Ortiz, George Phillips, Davide Rossi, Guido Vollbeding, Ge' Weijers, and other members of the Independent JPEG Group.

IJG is not affiliated with the official ISO JPEG standards committee.

### DOCUMENTATION ROADMAP

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This file contains the following sections:

OVERVIEW        General description of JPEG and the IJG software.  
LEGAL ISSUES    Copyright, lack of warranty, terms of distribution.  
REFERENCES     Where to learn more about JPEG.  
ARCHIVE LOCATIONS Where to find newer versions of this software.  
RELATED SOFTWARE Other stuff you should get.  
FILE FORMAT WARS Software \*not\* to get.  
TO DO           Plans for future IJG releases.

Other documentation files in the distribution are:

User documentation:

install.doc    How to configure and install the IJG software.  
usage.doc     Usage instructions for cjpeg, djpeg, jpegtran,  
              rdjpgcom, and wrjpgcom.  
\*.1            Unix-style man pages for programs (same info as usage.doc).  
wizard.doc    Advanced usage instructions for JPEG wizards only.  
change.log    Version-to-version change highlights.

Programmer and internal documentation:

libjpeg.doc   How to use the JPEG library in your own programs.

example.c      Sample code for calling the JPEG library.  
structure.doc    Overview of the JPEG library's internal structure.  
filelist.doc    Road map of IJG files.  
coderrules.doc   Coding style rules --- please read if you contribute code.

Please read at least the files install.doc and usage.doc. Useful information can also be found in the JPEG FAQ (Frequently Asked Questions) article. See ARCHIVE LOCATIONS below to find out where to obtain the FAQ article.

If you want to understand how the JPEG code works, we suggest reading one or more of the REFERENCES, then looking at the documentation files (in roughly the order listed) before diving into the code.

## OVERVIEW

=====

This package contains C software to implement JPEG image compression and decompression. JPEG (pronounced "jay-peg") is a standardized compression method for full-color and gray-scale images. JPEG is intended for compressing "real-world" scenes; line drawings, cartoons and other non-realistic images are not its strong suit. JPEG is lossy, meaning that the output image is not exactly identical to the input image. Hence you must not use JPEG if you have to have identical output bits. However, on typical photographic images, very good compression levels can be obtained with no visible change, and remarkably high compression levels are possible if you can tolerate a low-quality image. For more details, see the references, or just experiment with various compression settings.

This software implements JPEG baseline, extended-sequential, and progressive compression processes. Provision is made for supporting all variants of these processes, although some uncommon parameter settings aren't implemented yet. For legal reasons, we are not distributing code for the arithmetic-coding variants of JPEG; see LEGAL ISSUES. We have made no provision for supporting the hierarchical or lossless processes defined in the standard.

We provide a set of library routines for reading and writing JPEG image files, plus two sample applications "cjpeg" and "djpeg", which use the library to perform conversion between JPEG and some other popular image file formats. The library is intended to be reused in other applications.

In order to support file conversion and viewing software, we have included considerable functionality beyond the bare JPEG coding/decoding capability; for example, the color quantization modules are not strictly part of JPEG decoding, but they are essential for output to colormapped file formats or colormapped displays. These extra functions can be compiled out of the library if not required for a particular application. We have also included "jpegtran", a utility for lossless transcoding between different JPEG



processes, and "rdjpgcom" and "wrjpgcom", two simple applications for inserting and extracting textual comments in JFIF files.

The emphasis in designing this software has been on achieving portability and flexibility, while also making it fast enough to be useful. In particular, the software is not intended to be read as a tutorial on JPEG. (See the REFERENCES section for introductory material.) Rather, it is intended to be reliable, portable, industrial-strength code. We do not claim to have achieved that goal in every aspect of the software, but we strive for it.

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The Unix configuration script "configure" was produced with GNU Autoconf. It is copyright by the Free Software Foundation but is freely distributable. The same holds for its supporting scripts (config.guess, config.sub, ltconfig, ltmain.sh). Another support script, install-sh, is copyright by M.I.T. but is also freely distributable.

It appears that the arithmetic coding option of the JPEG spec is covered by patents owned by IBM, AT&T, and Mitsubishi. Hence arithmetic coding cannot legally be used without obtaining one or more licenses. For this reason, support for arithmetic coding has been removed from the free JPEG software. (Since arithmetic coding provides only a marginal gain over the unpatented Huffman mode, it is unlikely that very many implementations will support it.) So far as we are aware, there are no patent restrictions on the remaining code.

The IJG distribution formerly included code to read and write GIF files. To avoid entanglement with the Unisys LZW patent, GIF reading support has been removed altogether, and the GIF writer has been simplified to produce "uncompressed GIFs". This technique does not use the LZW algorithm; the resulting GIF files are larger than usual, but are readable by all standard GIF decoders.

We are required to state that

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## REFERENCES

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We highly recommend reading one or more of these references before trying to understand the innards of the JPEG software.

The best short technical introduction to the JPEG compression algorithm is Wallace, Gregory K. "The JPEG Still Picture Compression Standard", Communications of the ACM, April 1991 (vol. 34 no. 4), pp. 30-44. (Adjacent articles in that issue discuss MPEG motion picture compression, applications of JPEG, and related topics.) If you don't have the CACM issue handy, a PostScript file containing a revised version of Wallace's article is available at <ftp://ftp.uu.net/graphics/jpeg/wallace.ps.gz>. The file (actually a preprint for an article that appeared in IEEE Trans. Consumer Electronics) omits the sample images that appeared in CACM, but it includes corrections and some added material. Note: the Wallace article is copyright ACM and IEEE, and it may not be used for commercial purposes.

A somewhat less technical, more leisurely introduction to JPEG can be found in "The Data Compression Book" by Mark Nelson and Jean-loup Gailly, published by M&T Books (New York), 2nd ed. 1996, ISBN 1-55851-434-1. This book provides good explanations and example C code for a multitude of compression methods including JPEG. It is an excellent source if you are comfortable reading C code but don't know much about data compression in general. The book's JPEG sample code is far from industrial-strength, but when you are ready to look at a full implementation, you've got one here...

The best full description of JPEG is the textbook "JPEG Still Image Data Compression Standard" by William B. Pennebaker and Joan L. Mitchell, published by Van Nostrand Reinhold, 1993, ISBN 0-442-01272-1. Price US\$59.95, 638 pp. The book includes the complete text of the ISO JPEG standards (DIS 10918-1 and draft DIS 10918-2). This is by far the most complete exposition of JPEG in existence, and we highly recommend it.

The JPEG standard itself is not available electronically; you must order a paper copy through ISO or ITU. (Unless you feel a need to own a certified official copy, we recommend buying the Pennebaker and Mitchell book instead; it's much cheaper and includes a great deal of useful explanatory material.) In the USA, copies of the standard may be ordered from ANSI Sales at (212) 642-4900, or from Global Engineering Documents at (800) 854-7179. (ANSI doesn't take credit card orders, but Global does.) It's not cheap: as of 1992, ANSI was charging \$95 for Part 1 and \$47 for Part 2, plus 7%

shipping/handling. The standard is divided into two parts, Part 1 being the actual specification, while Part 2 covers compliance testing methods. Part 1 is titled "Digital Compression and Coding of Continuous-tone Still Images, Part 1: Requirements and guidelines" and has document numbers ISO/IEC IS 10918-1, ITU-T T.81. Part 2 is titled "Digital Compression and Coding of Continuous-tone Still Images, Part 2: Compliance testing" and has document numbers ISO/IEC IS 10918-2, ITU-T T.83.

Some extensions to the original JPEG standard are defined in JPEG Part 3, a newer ISO standard numbered ISO/IEC IS 10918-3 and ITU-T T.84. IJG currently does not support any Part 3 extensions.

The JPEG standard does not specify all details of an interchangeable file format. For the omitted details we follow the "JFIF" conventions, revision 1.02. A copy of the JFIF spec is available from:

Literature Department  
C-Cube Microsystems, Inc.  
1778 McCarthy Blvd.  
Milpitas, CA 95035  
phone (408) 944-6300, fax (408) 944-6314

A PostScript version of this document is available by FTP at <ftp://ftp.uu.net/graphics/jpeg/jfif.ps.gz>. There is also a plain text version at <ftp://ftp.uu.net/graphics/jpeg/jfif.txt.gz>, but it is missing the figures.

The TIFF 6.0 file format specification can be obtained by FTP from <ftp://ftp.sgi.com/graphics/tiff/TIFF6.ps.gz>. The JPEG incorporation scheme found in the TIFF 6.0 spec of 3-June-92 has a number of serious problems. IJG does not recommend use of the TIFF 6.0 design (TIFF Compression tag 6). Instead, we recommend the JPEG design proposed by TIFF Technical Note #2 (Compression tag 7). Copies of this Note can be obtained from [ftp.sgi.com](ftp://ftp.sgi.com) or from <ftp://ftp.uu.net/graphics/jpeg/>. It is expected that the next revision of the TIFF spec will replace the 6.0 JPEG design with the Note's design. Although IJG's own code does not support TIFF/JPEG, the free libtiff library uses our library to implement TIFF/JPEG per the Note. libtiff is available from <ftp://ftp.sgi.com/graphics/tiff/>.

## ARCHIVE LOCATIONS

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The "official" archive site for this software is [ftp.uu.net](ftp://ftp.uu.net) (Internet address 192.48.96.9). The most recent released version can always be found there in directory [graphics/jpeg](ftp://ftp.uu.net/graphics/jpeg/). This particular version will be archived as <ftp://ftp.uu.net/graphics/jpeg/jpegsrc.v6b.tar.gz>. If you don't have direct Internet access, UUNET's archives are also available via UUCP; contact [help@uunet.uu.net](mailto:help@uunet.uu.net) for information on retrieving files that way.

Numerous Internet sites maintain copies of the UUNET files. However, only ftp.uu.net is guaranteed to have the latest official version.

You can also obtain this software in DOS-compatible "zip" archive format from the SimTel archives (<ftp://ftp.simtel.net/pub/simtelnet/msdos/graphics/>), or on CompuServe in the Graphics Support forum (GO CIS:GRAPHSUP), library 12 "JPEG Tools". Again, these versions may sometimes lag behind the ftp.uu.net release.

The JPEG FAQ (Frequently Asked Questions) article is a useful source of general information about JPEG. It is updated constantly and therefore is not included in this distribution. The FAQ is posted every two weeks to Usenet newsgroups comp.graphics.misc, news.answers, and other groups. It is available on the World Wide Web at <http://www.faqs.org/faqs/jpeg-faq/> and other news.answers archive sites, including the official news.answers archive at rtfm.mit.edu: <ftp://rtfm.mit.edu/pub/usenet/news.answers/jpeg-faq/>. If you don't have Web or FTP access, send e-mail to [mail-server@rtfm.mit.edu](mailto:mail-server@rtfm.mit.edu) with body  
send usenet/news.answers/jpeg-faq/part1  
send usenet/news.answers/jpeg-faq/part2

#### RELATED SOFTWARE

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Numerous viewing and image manipulation programs now support JPEG. (Quite a few of them use this library to do so.) The JPEG FAQ described above lists some of the more popular free and shareware viewers, and tells where to obtain them on Internet.

If you are on a Unix machine, we highly recommend Jef Poskanzer's free PBPLUS software, which provides many useful operations on PPM-format image files. In particular, it can convert PPM images to and from a wide range of other formats, thus making cjpeg/djpeg considerably more useful. The latest version is distributed by the NetPBM group, and is available from numerous sites, notably <ftp://wuarchive.wustl.edu/graphics/graphics/packages/NetPBM/>. Unfortunately PBPLUS/NETPBM is not nearly as portable as the IJG software is; you are likely to have difficulty making it work on any non-Unix machine.

A different free JPEG implementation, written by the PVRG group at Stanford, is available from <ftp://havefun.stanford.edu/pub/jpeg/>. This program is designed for research and experimentation rather than production use; it is slower, harder to use, and less portable than the IJG code, but it is easier to read and modify. Also, the PVRG code supports lossless JPEG, which we do not. (On the other hand, it doesn't do progressive JPEG.)

#### FILE FORMAT WARS

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Some JPEG programs produce files that are not compatible with our library. The root of the problem is that the ISO JPEG committee failed to specify a concrete file format. Some vendors "filled in the blanks" on their own, creating proprietary formats that no one else could read. (For example, none of the early commercial JPEG implementations for the Macintosh were able to exchange compressed files.)

The file format we have adopted is called JFIF (see REFERENCES). This format has been agreed to by a number of major commercial JPEG vendors, and it has become the de facto standard. JFIF is a minimal or "low end" representation. We recommend the use of TIFF/JPEG (TIFF revision 6.0 as modified by TIFF Technical Note #2) for "high end" applications that need to record a lot of additional data about an image. TIFF/JPEG is fairly new and not yet widely supported, unfortunately.

The upcoming JPEG Part 3 standard defines a file format called SPIFF. SPIFF is interoperable with JFIF, in the sense that most JFIF decoders should be able to read the most common variant of SPIFF. SPIFF has some technical advantages over JFIF, but its major claim to fame is simply that it is an official standard rather than an informal one. At this point it is unclear whether SPIFF will supersede JFIF or whether JFIF will remain the de-facto standard. IJG intends to support SPIFF once the standard is frozen, but we have not decided whether it should become our default output format or not. (In any case, our decoder will remain capable of reading JFIF indefinitely.)

Various proprietary file formats incorporating JPEG compression also exist. We have little or no sympathy for the existence of these formats. Indeed, one of the original reasons for developing this free software was to help force convergence on common, open format standards for JPEG files. Don't use a proprietary file format!

TO DO

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The major thrust for v7 will probably be improvement of visual quality. The current method for scaling the quantization tables is known not to be very good at low Q values. We also intend to investigate block boundary smoothing, "poor man's variable quantization", and other means of improving quality-vs-file-size performance without sacrificing compatibility.

In future versions, we are considering supporting some of the upcoming JPEG Part 3 extensions --- principally, variable quantization and the SPIFF file format.

As always, speeding things up is of great interest.

Please send bug reports, offers of help, etc. to [jpeg-info@uunet.uu.net](mailto:jpeg-info@uunet.uu.net).

## 1.204 sqlite 3.15.2

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```
#!/usr/bin/env python
#
# check if a file has the proper license in it
#
# USAGE: check-license.py [-C] file1 file2 ... fileN
#
# A 'file' may in fact be a directory, in which case it is recursively
# searched.
#
# If the license cannot be found, then the filename is printed to stdout.
# Typical usage:
# $ check-license.py . > bad-files
#
# -C switch is used to change licenses.
# Typical usage:
# $ check-license.py -C file1 file2 ... fileN
#

import sys, os, re

# Note: Right now, OLD_LICENSE and NEW_LICENSE are the same, because
# r878444 updated all the license blocks. In the future, if we update
# the license block again, change just NEW_LICENSE and use this script.
```

```
OLD_LICENSE = ""\
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'''

```

```
SH_OLD_LICENSE = re.subn(r'(?m)^\*\s*'#', OLD_LICENSE)[0]
```

```
# Remember not to do regexp quoting for NEW_LICENSE. Only OLD_LICENSE
```

```
# is used for matching; NEW_LICENSE is inserted as-is.
```

```
NEW_LICENSE = '''\
```

```

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'''

```

```
SH_NEW_LICENSE = re.subn(r'(?m)^\*\s*'#', NEW_LICENSE)[0]
```

```
re_OLD = re.compile(OLD_LICENSE)
```

```
re_SH_OLD = re.compile(SH_OLD_LICENSE)
```

```
re_EXCLUDE = re.compile(
    r'automatically generated by SWIG'
    + r'|Generated from configure\,in'
    + r'|placed into the public domain'
)
```

```
c_comment_suffices = ('.c', '.java', '.h', '.cpp', '.hw', '.pas')
```

```

# Yes, this is an empty tuple. No types that fit in this category uniformly
# have a copyright block.
# Possible types to add here:
# ('.bat', '.py', '.pl', '.in')
sh_comment_suffices = ()

def check_file(fname, old_re, new_lic):
    s = open(fname).read()
    if (not old_re.search(s)
        and not re_EXCLUDE.search(s)):
        print(fname)

def change_license(fname, old_re, new_lic):
    s = open(fname).read()
    m = old_re.search(s)
    if not m:
        print('ERROR: missing old license: %s' % fname)
    else:
        s = s[:m.start()] + new_lic + s[m.end():]
        open(fname, 'w').write(s)
        print('Changed: %s' % fname)

def visit(baton, dirname, dircontents):
    file_func = baton
    for i in dircontents:
        # Don't recurse into certain directories
        if i in ('.svn', '.libs'):
            dircontents.remove(i)
            continue

    extension = os.path.splitext(i)[1]
    fullname = os.path.join(dirname, i)

    if os.path.isdir(fullname):
        continue

    if extension in c_comment_suffices:
        file_func(fullname, re_OLD, NEW_LICENSE)
    elif extension in sh_comment_suffices:
        file_func(fullname, re_SH_OLD, SH_NEW_LICENSE)

def main():
    file_func = check_file
    if sys.argv[1] == '-C':
        print('Changing license text...')
        del sys.argv[1]
        file_func = change_license

```

```
for f in sys.argv[1:]:
    if os.path.isdir(f):
        baton = file_func
        for dirpath, dirs, files in os.walk(f):
            visit(baton, dirpath, dirs + files)
    else:
        baton = file_func
        dir, i = os.path.split(f)
        visit(baton, dir, i)

if __name__ == '__main__':
    main()
```

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\* Markus Kuhn -- 2007-05-26 (Unicode 5.0)

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- \* `configure.ac`, `Makefile.am`: The original versions were derived from the ones in the XML Catalog Manager project, version 2.2.

Author: Julio Merino <jmmv@users.sourceforge.net>

\* atf-c/ui.c: The format\_paragraph and format\_text functions were derived from the ones in the Monotone project, revision 3a0982da308228d796df35f98d787c5cff2bb5b6.

Author: Julio Merino <jmmv@NetBSD.org>

\* atf-c++/detail/io.hpp, atf-c++/detail/io.cpp, atf-c++/detail/io\_test.cpp: These files were derived from the file\_handle, systembuf, pipe and pistream classes and tests found in the Boost.Process library.

Author: Julio Merino <jmmv84@gmail.com>

\* admin/check-style.sh, admin/check-style-common.awk, admin/check-style-cpp.awk, admin/check-style-shell.awk: These files, except the first one, were first implemented in the Buildtool project. They were later adapted to be part of Boost.Process and, during that process, the shell script was created.

Author: Julio Merino <jmmv84@gmail.com>

```
vim: filetype=text:textwidth=75:expandtab:shiftwidth=2:softtabstop=2
```

```
<!--
```

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```
-
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```

```
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```

```
-->
```

```
<!-- $Id$ -->
```

```
<!-- Generate ISC copyright comments from Docbook copyright metadata. -->
```

```
<xsl:stylesheet version="1.0"
```

```
xmlns:xsl="http://www.w3.org/1999/XSL/Transform"
```

```
xmlns:xi="http://www.w3.org/2001/XInclude"
```

```
xmlns:db="http://docbook.org/ns/docbook">
```

```
<xsl:template name="isc.copyright.format">
```

```
<xsl:param name="text"/>
```

```
<xsl:value-of select="$isc.copyright.leader"/>
```

```
<xsl:value-of select="normalize-space(substring-before($text, '&#10;'))"/>
```

```
<xsl:text>&#10;</xsl:text>
```

```
<xsl:variable name="rest" select="substring-after($text, '&#10;')"/>
```

```

<xsl:if test="translate($rest, '&#9;&#32;', ')">
  <xsl:call-template name="isc.copyright.format">
    <xsl:with-param name="text" select="$rest"/>
  </xsl:call-template>
</xsl:if>
</xsl:template>

<xsl:variable name="isc.copyright.text">
  <xsl:text>
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    file, You can obtain one at http://mozilla.org/MPL/2.0/.
  </xsl:text>
</xsl:variable>

<xsl:variable name="isc.copyright">
  <xsl:call-template name="isc.copyright.format">
    <xsl:with-param name="text">
      <xsl:for-each select="book/info/copyright | refentry/docinfo/copyright">
        <xsl:text>Copyright (C) </xsl:text>
        <xsl:call-template name="copyright.years">
          <xsl:with-param name="years" select="year"/>
        </xsl:call-template>
        <xsl:text> </xsl:text>
        <xsl:value-of select="holder"/>
        <xsl:value-of select="$isc.copyright.breakline"/>
        <xsl:text>&#10;</xsl:text>
      </xsl:for-each>
      <xsl:value-of select="$isc.copyright.text"/>
    </xsl:with-param>
  </xsl:call-template>
</xsl:variable>
</xsl:stylesheet>

```

<!--

- Local variables:

- mode: sgml

- End:

-->

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## 1.217 accessors-smart 1.1

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C/W/GuQFfCJJykkL2jd9/Ag=  
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Zokttk0bTHfwaa5TtxQbScw=  
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6etDJ6AUj0jTuS159hUsWMQ=  
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pN0h9U/xdFTRMFsXYFHQeN4=

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Date: 8 January 2007

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-----BEGIN PGP SIGNATURE-----

Version: GnuPG v1.4.3 (GNU/Linux)

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Full name: Julian Anthony Fox Gosnell  
E-Mail: `jules@coredevelopers.net`  
Mailing Address: 2, Tannery Cottages, Tannery Lane, Bramley, Surrey, GU5 0AB, UK.

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Date: 10th March 2006

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Version: GnuPG v1.4.2.1 (GNU/Linux)

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=piHm

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Date: 2012-07-17

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-----BEGIN PGP SIGNATURE-----

Version: GnuPG v1.4.10 (GNU/Linux)

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Mailing Address: 1A Leigh Road, London, UK, N5 1ST

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Date: January 30th 2006

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Version: GnuPG v1.4.1 (Darwin)

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-----END PGP SIGNATURE-----

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mso-margin-bottom-alt:auto;

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Schedule A

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5hdry6ZjXRcUhQEyNz2F/T4=  
=I4Co

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<https://glassfish.dev.java.net/nonav/public/CDDL+GPL.html>

javax.servlet:javax.servlet-api  
javax.servlet.jsp:javax.servlet.jsp-api  
org.glassfish.web:javax.servlet.jsp  
org.glassfish.web:javax.servlet.jsp  
org.glassfish.web:javax.servlet.jsp.jstl  
org.eclipse.jetty.orbit:javax.servlet.jsp.jstl

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Apache

The following artifacts are ASL2 licensed.

org.apache.taglibs:taglibs-standard-spec  
org.apache.taglibs:taglibs-standard-impl

-----

MortBay

The following artifacts are ASL2 licensed. Based on selected classes from following Apache Tomcat jars, all ASL2 licensed.

org.mortbay.jasper:apache-jsp  
org.apache.tomcat:tomcat-jasper

org.apache.tomcat:tomcat-juli  
org.apache.tomcat:tomcat-jsp-api  
org.apache.tomcat:tomcat-el-api  
org.apache.tomcat:tomcat-jasper-el  
org.apache.tomcat:tomcat-api  
org.apache.tomcat:tomcat-util-scan  
org.apache.tomcat:tomcat-util

org.mortbay.jasper:apache-el  
org.apache.tomcat:tomcat-jasper-el  
org.apache.tomcat:tomcat-el-api

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<https://glassfish.dev.java.net/nonav/public/CDDL+GPL.html>

org.eclipse.jetty.toolchain:jetty-schemas

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Assorted

The UnixCrypt.java code implements the one way cryptography used by Unix systems for simple password protection. Copyright 1996 Aki Yoshida, modified April 2001 by Iris Van den Broeke, Daniel Deville.

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E-Mail: `tim@adjective.org`

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RUIoI0BYmYpaETSqxt2oLFU=  
=Tr57

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Full name: David Jencks

E-Mail: david\_jencks@yahoo.com

Mailing Address: 2215 SE 39th Ave, Portland OR 97214 USA

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iD8DBQFIZT2ToF6+5lbz4BsRAs3wAJ9puXC26Nr8nhFvTZ9oNwxDFV/DVACgnC8O  
VFUWPZrfLOJesKa0/rYNJIM=  
=jC7I

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# 1.219 hk2-locator 2.4.0

## 1.220 jersey 2.22

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To do so, attach the following notices to the program. It is safest to attach them to the start of each source file to most effectively state the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

```
<one line to give the program's name and a brief idea of what it does.>  
Copyright (C) <year> <name of author>
```

```
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```

```
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```

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```
<program> Copyright (C) <year> <name of author>  
This program comes with ABSOLUTELY NO WARRANTY; for details type `show w'.  
This is free software, and you are welcome to redistribute it  
under certain conditions; type `show c' for details.
```

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## 1.225 tomcat-catalina-ant 8.5.32

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## 1.226 dash 0.5.8

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```
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Gnomovision comes with ABSOLUTELY NO WARRANTY; for details type `show w'.
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under certain conditions; type `show c' for details.
@end smallexample
```

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```
@example
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`Gnomovision' (which makes passes at compilers) written by James Hacker.
```

```
@var{signature of Ty Coon}, 1 April 1989
Ty Coon, President of Vice
@end example
```

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```
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# 1.240 snake-yaml 1.18

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## 1.241 gorilla 1.7.1

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# 1.242 curl 7.64.1

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=====

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## 1.244 tomcat-i18n-es 8.5.32

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## 1.248 bean-validation-api 1.1.0

## 1.249 chardet 3.0.4

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## 1.250 goleveldb 1.0.0

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# 1.252 sbsigntool 0.7

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for

National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations, which became Zope Corporation. In 2001, the Python Software Foundation (PSF, see <https://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation was a sponsoring member of the PSF.

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| Release        | Derived from | Year      | Owner      | GPL-compatible? (1) |
|----------------|--------------|-----------|------------|---------------------|
| 0.9.0 thru 1.2 |              | 1991-1995 | CWI        | yes                 |
| 1.3 thru 1.5.2 | 1.2          | 1995-1999 | CNRI       | yes                 |
| 1.6            | 1.5.2        | 2000      | CNRI       | no                  |
| 2.0            | 1.6          | 2000      | BeOpen.com | no                  |
| 1.6.1          | 1.6          | 2001      | CNRI       | yes (2)             |
| 2.1            | 2.0+1.6.1    | 2001      | PSF        | no                  |
| 2.0.1          | 2.0+1.6.1    | 2001      | PSF        | yes                 |
| 2.1.1          | 2.1+2.0.1    | 2001      | PSF        | yes                 |
| 2.1.2          | 2.1.1        | 2002      | PSF        | yes                 |
| 2.1.3          | 2.1.2        | 2002      | PSF        | yes                 |
| 2.2 and above  | 2.1.1        | 2001-now  | PSF        | yes                 |

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see <http://www.zope.com>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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| 1.6            | 1.5.2        | 2000      | CNRI       | no                  |
| 2.0            | 1.6          | 2000      | BeOpen.com | no                  |
| 1.6.1          | 1.6          | 2001      | CNRI       | yes (2)             |
| 2.1            | 2.0+1.6.1    | 2001      | PSF        | no                  |
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| 2.1.1          | 2.1+2.0.1    | 2001      | PSF        | yes                 |
| 2.1.2          | 2.1.1        | 2002      | PSF        | yes                 |
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| 2.1.1          | 2.1+2.0.1    | 2001      | PSF        | yes             |
| 2.1.2          | 2.1.1        | 2002      | PSF        | yes             |
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#### Mersenne Twister

-----

The `:mod:`_random`` module includes code based on a download from <http://www.math.sci.hiroshima-u.ac.jp/~m-mat/MT/MT2002/emt19937ar.html>. The following are the verbatim comments from the original code::

A C-program for MT19937, with initialization improved 2002/1/26.  
Coded by Takuji Nishimura and Makoto Matsumoto.

Before using, initialize the state by using `init_genrand(seed)`  
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<http://www.math.sci.hiroshima-u.ac.jp/~m-mat/MT/emt.html>

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## Sockets

-----

The `:mod:`socket`` module uses the functions, `:func:`getaddrinfo``, and `:func:`getnameinfo``, which are coded in separate source files from the WIDE Project, <http://www.wide.ad.jp/>. ::

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-----

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## Asynchronous socket services

-----

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Modified by Jack Jansen, CWI, July 1995:

- Use binascii module to do the actual line-by-line conversion between ascii and binary. This results in a 1000-fold speedup. The C version is still 5 times faster, though.
- Arguments more compliant with Python standard

#### XML Remote Procedure Calls

-----

The `:mod:`xmlrpc.client`` module contains the following notice::

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test\_epoll

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Select kqueue

-----

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SipHash24

-----

The file `:file:`Python/pyhash.c`` contains Marek Majkowski's implementation of Dan Bernstein's SipHash24 algorithm. The contains the following note::

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Original location:

<https://github.com/majek/csiphash/>

Solution inspired by code from:

Samuel Neves (supercop/crypto\_auth/siphhash24/little)

djb (supercop/crypto\_auth/siphhash24/little2)

Jean-Philippe Aumasson (<https://131002.net/siphhash/siphhash24.c>)

strtod and dtoa

-----

The file `:file:`Python/dtoa.c``, which supplies C functions `dtoa` and `strtod` for conversion of C doubles to and from strings, is derived from the file of the same name by David M. Gay, currently available from <http://www.netlib.org/fp/>. The original file, as retrieved on March 16, 2009, contains the following copyright and licensing

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----

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cfuhash

-----

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Allbery, Russ  
Anderson, Jamie  
Andrew, Nick  
Andric, Dimitry  
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Bates, Tom  
Behan, Zdenk  
Bellis, Ray  
Benali, Elias  
Beverly, Jamie  
Boardman, Spider  
Bos, Sander  
Bostley, P.J.  
Bowes, Keith  
Boyce, Keith Garry  
Brantley, Michael  
Braun, Rob  
Bezina, Pavel  
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Cheloha, Scott  
ek, Vtislav

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Ryabinkin, Eygene  
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The following people have worked to translate sudo into other languages as part of the Translation Project, see <https://translationproject.org> for more details.

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--- end ---

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## 1.263 sqlite 3.7.8

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```
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**
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**
**   May you do good and not evil.
**   May you find forgiveness for yourself and forgive others.
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**
*****
** This header file defines the interface that the SQLite library
** presents to client programs. If a C-function, structure, datatype,
** or constant definition does not appear in this file, then it is
** not a published API of SQLite, is subject to change without
** notice, and should not be referenced by programs that use SQLite.
**
** Some of the definitions that are in this file are marked as
** "experimental". Experimental interfaces are normally new
** features recently added to SQLite. We do not anticipate changes
** to experimental interfaces but reserve the right to make minor changes
** if experience from use "in the wild" suggest such changes are prudent.
```

\*\*  
\*\* The official C-language API documentation for SQLite is derived  
\*\* from comments in this file. This file is the authoritative source  
\*\* on how SQLite interfaces are suppose to operate.  
\*\*  
\*\* The name of this file under configuration management is "sqlite.h.in".  
\*\* The makefile makes some minor changes to this file (such as inserting  
\*\* the version number) and changes its name to "sqlite3.h" as  
\*\* part of the build process.  
\*\*/  
\*\*

# 1.264 apache-log4j 1.2.17

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Apache log4j

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## 1.267 Idns 1.6.17

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"" Easy DNS (including DNSSEC) via ldns.

ldns is a great library. It is a powerful tool for working with DNS. python-ldns it is a straight up clone of the C interface, however that is not a very good interface for python. Its documentation is incomplete and some functions don't work as described. And some objects don't have a full python API.

ldnsx aims to fix this. It wraps around the ldns python bindings, working around its limitations and providing a well-documented, more pythonistic interface.

/\* From openssl 4.3p2 filename openbsd-compat/fake-rfc2553.h \*/

/\*

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# install - install a program, script, or datafile

scriptversion=2009-04-28.21; # UTC

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## 1.269 jersey-common 2.22

## 1.270 Idb 1.2.840

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## 1.271 jackson-databind 1.9.13

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## 1.273 jetty-webapp 9.2.13.v20150730

# 1.274 zlib 1.1.3

## 1.275 nginx 1.12.1

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```
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```

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\*

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/\* zlib.h -- interface of the 'zlib' general purpose compression library  
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\*/

# 1.280 go-proxy 1.1

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## 1.281 jersey-media-json-jackson 2.22

## 1.282 javassist 3.20.0

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## 1.283 libunwind 20170615

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## 1.285 jersey-server 2.22

## 1.286 httpcomponents-client 4.3.2

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## 1.290 tomcat-websocket-api 8.5.32

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If you develop a new library, and you want it to be of the greatest possible use to the public, we recommend making it free software that everyone can redistribute and change. You can do so by permitting redistribution under these terms (or, alternatively, under the terms of the ordinary General Public License).

To apply these terms, attach the following notices to the library. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

<one line to give the library's name and a brief idea of what it does.>

Copyright (C) <year> <name of author>

This library is free software; you can redistribute it and/or modify it under the terms of the GNU Library General Public License as published by the Free Software Foundation; either version 2 of the License, or (at your option) any later version.

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You should have received a copy of the GNU Library General Public License along with this library; if not, write to the Free Software Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.

Also add information on how to contact you by electronic and paper mail.

You should also get your employer (if you work as a programmer) or your school, if any, to sign a "copyright disclaimer" for the library, if necessary. Here is a sample; alter the names:

Yoyodyne, Inc., hereby disclaims all copyright interest in the library `Frob' (a library for tweaking knobs) written by James Random Hacker.

<signature of Ty Coon>, 1 April 1990  
Ty Coon, President of Vice

That's all there is to it!  
@node Library Copying  
@appendixsec GNU LESSER GENERAL PUBLIC LICENSE

@cindex LGPL, Lesser General Public License  
@center Version 2.1, February 1999

@display  
Copyright @copyright{ } 1991, 1999 Free Software Foundation, Inc.  
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[This is the first released version of the Lesser GPL. It also counts as the successor of the GNU Library Public License, version 2, hence the version number 2.1.]

@end display

@appendixsubsec Preamble

The licenses for most software are designed to take away your freedom to share and change it. By contrast, the GNU General Public Licenses are intended to guarantee your freedom to share and change free software---to make sure the software is free for all its users.

This license, the Lesser General Public License, applies to some specially designated software---typically libraries---of the Free Software Foundation and other authors who decide to use it. You can use it too, but we suggest you first think carefully about whether this license or the ordinary General Public License is the better strategy to use in any particular case, based on the explanations below.

When we speak of free software, we are referring to freedom of use, not price. Our General Public Licenses are designed to make sure that you have the freedom to distribute copies of free software (and charge for this service if you wish); that you receive source code or can get it if you want it; that you can change the software and use pieces of it in new free programs; and that you are informed that you can do these things.

To protect your rights, we need to make restrictions that forbid distributors to deny you these rights or to ask you to surrender these rights. These restrictions translate to certain responsibilities for you if you distribute copies of the library or if you modify it.

For example, if you distribute copies of the library, whether gratis or for a fee, you must give the recipients all the rights that we gave you. You must make sure that they, too, receive or can get the source code. If you link other code with the library, you must provide complete object files to the recipients, so that they can relink them with the library after making changes to the library and recompiling it. And you must show them these terms so they know their rights.

We protect your rights with a two-step method: (1) we copyright the library, and (2) we offer you this license, which gives you legal permission to copy, distribute and/or modify the library.

To protect each distributor, we want to make it very clear that there is no warranty for the free library. Also, if the library is modified by someone else and passed on, the recipients should know that what they have is not the original version, so that the original author's reputation will not be affected by problems that might be introduced by others.

Finally, software patents pose a constant threat to the existence of any free program. We wish to make sure that a company cannot

effectively restrict the users of a free program by obtaining a restrictive license from a patent holder. Therefore, we insist that any patent license obtained for a version of the library must be consistent with the full freedom of use specified in this license.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License. This license, the GNU Lesser General Public License, applies to certain designated libraries, and is quite different from the ordinary General Public License. We use this license for certain libraries in order to permit linking those libraries into non-free programs.

When a program is linked with a library, whether statically or using a shared library, the combination of the two is legally speaking a combined work, a derivative of the original library. The ordinary General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

We call this license the @dfn{Lesser} General Public License because it does @emph{Less} to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a ``work based on the library" and a ``work that uses the library". The former contains code derived from the library, whereas the latter must be combined with the library in order to run.

@iftex

@appendixsubsec TERMS AND CONDITIONS FOR COPYING, DISTRIBUTION AND MODIFICATION

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@ifinfo

@center GNU LESSER GENERAL PUBLIC LICENSE

@center TERMS AND CONDITIONS FOR COPYING, DISTRIBUTION AND MODIFICATION

@end ifinfo

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This License Agreement applies to any software library or other program which contains a notice placed by the copyright holder or other authorized party saying it may be distributed under the terms of this Lesser General Public License (also called ``this License"). Each licensee is addressed as ``you".

A ``library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The ``Library", below, refers to any such software library or work which has been distributed under these terms. A ``work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term ``modification".)

``Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

@item

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You may charge a fee for the physical act of transferring a copy, and you may at your option offer warranty protection in exchange for a fee.

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You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

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The modified work must itself be a software library.

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You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

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You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

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If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

@end enumerate

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

@item

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Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

@item

You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the



source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a ``work that uses the Library''. Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a ``work that uses the Library'' with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a ``work that uses the library''. The executable is therefore covered by this License.

Section 6 states terms for distribution of such executables.

When a ``work that uses the Library'' uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

@item

As an exception to the Sections above, you may also combine or link a ``work that uses the Library'' with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the

copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

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Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable ``work that uses the Library'', as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

@item

Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

@item

Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

@item

If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

@item

Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

@end enumerate

For an executable, the required form of the ``work that uses the Library'' must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major

components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

@item

You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

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Accompany the combined library with a copy of the same work based on the Library, uncombined with any other library facilities. This must be distributed under the terms of the Sections above.

@item

Give prominent notice with the combined library of the fact that part of it is a work based on the Library, and explaining where to find the accompanying uncombined form of the same work.

@end enumerate

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@heading END OF TERMS AND CONDITIONS

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@appendixsubsec How to Apply These Terms to Your New Libraries

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To apply these terms, attach the following notices to the library. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the ``copyright" line and a pointer to where the full notice is found.

@smallexample

@var{one line to give the library's name and an idea of what it does.}

Copyright (C) @var{year} @var{name of author}

This library is free software; you can redistribute it and/or modify it under the terms of the GNU Lesser General Public License as published by the Free Software Foundation; either version 2.1 of the License, or (at your option) any later version.

This library is distributed in the hope that it will be useful, but WITHOUT ANY WARRANTY; without even the implied warranty of MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the GNU Lesser General Public License for more details.

You should have received a copy of the GNU Lesser General Public License along with this library; if not, write to the Free Software Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston, MA 02110-1301, USA.

@end smallexample

Also add information on how to contact you by electronic and paper mail.

You should also get your employer (if you work as a programmer) or your school, if any, to sign a ``copyright disclaimer" for the library, if necessary. Here is a sample; alter the names:

```
@smallexample
```

```
Yoyodyne, Inc., hereby disclaims all copyright interest in the library
`Frob' (a library for tweaking knobs) written by James Random Hacker.
```

```
@var{signature of Ty Coon}, 1 April 1990
```

```
Ty Coon, President of Vice
```

```
@end smallexample
```

That's all there is to it!

## GNU GENERAL PUBLIC LICENSE

Version 3, 29 June 2007

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To protect your rights, we need to prevent others from denying you these rights or asking you to surrender the rights. Therefore, you have certain responsibilities if you distribute copies of the software, or if you modify it: responsibilities to respect the freedom of others.

For example, if you distribute copies of such a program, whether

gratis or for a fee, you must pass on to the recipients the same freedoms that you received. You must make sure that they, too, receive or can get the source code. And you must show them these terms so they know their rights.

Developers that use the GNU GPL protect your rights with two steps: (1) assert copyright on the software, and (2) offer you this License giving you legal permission to copy, distribute and/or modify it.

For the developers' and authors' protection, the GPL clearly explains that there is no warranty for this free software. For both users' and authors' sake, the GPL requires that modified versions be marked as changed, so that their problems will not be attributed erroneously to authors of previous versions.

Some devices are designed to deny users access to install or run modified versions of the software inside them, although the manufacturer can do so. This is fundamentally incompatible with the aim of protecting users' freedom to change the software. The systematic pattern of such abuse occurs in the area of products for individuals to use, which is precisely where it is most unacceptable. Therefore, we have designed this version of the GPL to prohibit the practice for those products. If such problems arise substantially in other domains, we stand ready to extend this provision to those domains in future versions of the GPL, as needed to protect the freedom of users.

Finally, every program is threatened constantly by software patents. States should not allow patents to restrict development and use of software on general-purpose computers, but in those that do, we wish to avoid the special danger that patents applied to a free program could make it effectively proprietary. To prevent this, the GPL assures that patents cannot be used to render the program non-free.

The precise terms and conditions for copying, distribution and modification follow.

## TERMS AND CONDITIONS

### 0. Definitions.

"This License" refers to version 3 of the GNU General Public License.

"Copyright" also means copyright-like laws that apply to other kinds of works, such as semiconductor masks.

"The Program" refers to any copyrightable work licensed under this License. Each licensee is addressed as "you". "Licensees" and "recipients" may be individuals or organizations.



To "modify" a work means to copy from or adapt all or part of the work in a fashion requiring copyright permission, other than the making of an exact copy. The resulting work is called a "modified version" of the earlier work or a work "based on" the earlier work.

A "covered work" means either the unmodified Program or a work based on the Program.

To "propagate" a work means to do anything with it that, without permission, would make you directly or secondarily liable for infringement under applicable copyright law, except executing it on a computer or modifying a private copy. Propagation includes copying, distribution (with or without modification), making available to the public, and in some countries other activities as well.

To "convey" a work means any kind of propagation that enables other parties to make or receive copies. Mere interaction with a user through a computer network, with no transfer of a copy, is not conveying.

An interactive user interface displays "Appropriate Legal Notices" to the extent that it includes a convenient and prominently visible feature that (1) displays an appropriate copyright notice, and (2) tells the user that there is no warranty for the work (except to the extent that warranties are provided), that licensees may convey the work under this License, and how to view a copy of this License. If the interface presents a list of user commands or options, such as a menu, a prominent item in the list meets this criterion.

#### 1. Source Code.

The "source code" for a work means the preferred form of the work for making modifications to it. "Object code" means any non-source form of a work.

A "Standard Interface" means an interface that either is an official standard defined by a recognized standards body, or, in the case of interfaces specified for a particular programming language, one that is widely used among developers working in that language.

The "System Libraries" of an executable work include anything, other than the work as a whole, that (a) is included in the normal form of packaging a Major Component, but which is not part of that Major Component, and (b) serves only to enable use of the work with that Major Component, or to implement a Standard Interface for which an implementation is available to the public in source code form. A "Major Component", in this context, means a major essential component (kernel, window system, and so on) of the specific operating system

(if any) on which the executable work runs, or a compiler used to produce the work, or an object code interpreter used to run it.

The "Corresponding Source" for a work in object code form means all the source code needed to generate, install, and (for an executable work) run the object code and to modify the work, including scripts to control those activities. However, it does not include the work's System Libraries, or general-purpose tools or generally available free programs which are used unmodified in performing those activities but which are not part of the work. For example, Corresponding Source includes interface definition files associated with source files for the work, and the source code for shared libraries and dynamically linked subprograms that the work is specifically designed to require, such as by intimate data communication or control flow between those subprograms and other parts of the work.

The Corresponding Source need not include anything that users can regenerate automatically from other parts of the Corresponding Source.

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for software interchange, for a price no more than your reasonable cost of physically performing this conveying of source, or (2) access to copy the Corresponding Source from a network server at no charge.

@item

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@item

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@item

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```
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```

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```
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@var{program} Copyright (C) @var{year} @var{name of author}
This program comes with ABSOLUTELY NO WARRANTY; for details type @samp{show w}.
This is free software, and you are welcome to redistribute it
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```

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@c man end

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License.

Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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- b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.
- c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.
- d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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Version 2.1, February 1999

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Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy

from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

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# 1.299 jetty 9.2.1.v20140609

## 1.299.1 Available under license :

```
<html xmlns:o="urn:schemas-microsoft-com:office:office" xmlns:w="urn:schemas-microsoft-com:office:word"
xmlns="http://www.w3.org/TR/REC-html40"><head>
```

```
<meta http-equiv="Content-Type" content="text/html; charset=windows-1252">
<meta name="ProgId" content="Word.Document">
<meta name="Generator" content="Microsoft Word 9">
<meta name="Originator" content="Microsoft Word 9">
<link rel="File-List"
href="http://www.eclipse.org/legal/Eclipse%20EPL%202003_11_10%20Final_files/filelist.xml">
<title>Eclipse Public License - Version 1.0 / Apache License - Version 2.0</title>
<!--[if gte mso 9]><xml>
<o:DocumentProperties>
<o:Revision>2</o:Revision>
<o:TotalTime>3</o:TotalTime>
<o:Created>2004-03-05T23:03:00Z</o:Created>
<o:LastSaved>2004-03-05T23:03:00Z</o:LastSaved>
<o:Pages>4</o:Pages>
<o:Words>1626</o:Words>
<o:Characters>9270</o:Characters>
<o:Lines>77</o:Lines>
<o:Paragraphs>18</o:Paragraphs>
<o:CharactersWithSpaces>11384</o:CharactersWithSpaces>
<o:Version>9.4402</o:Version>
</o:DocumentProperties>
</xml><![endif]--><!--[if gte mso 9]><xml>
<w:WordDocument>
<w:TrackRevisions/>
</w:WordDocument>
</xml><![endif]-->
<style>
<!--
/* Font Definitions */
@font-face
{ font-family:Tahoma;
panose-1:2 11 6 4 3 5 4 4 2 4;
mso-font-charset:0;
mso-generic-font-family:swiss;
mso-font-pitch:variable;
```

```

mso-font-signature:553679495 -2147483648 8 0 66047 0;}
/* Style Definitions */
p.MsoNormal, li.MsoNormal, div.MsoNormal
{mso-style-parent:"";
margin:0in;
margin-bottom:.0001pt;
mso-pagination:widow-orphan;
font-size:12.0pt;
font-family:"Times New Roman";
mso-fareast-font-family:"Times New Roman";}
p
{margin-right:0in;
mso-margin-top-alt:auto;
mso-margin-bottom-alt:auto;
margin-left:0in;
mso-pagination:widow-orphan;
font-size:12.0pt;
font-family:"Times New Roman";
mso-fareast-font-family:"Times New Roman";}
p.BalloonText, li.BalloonText, div.BalloonText
{mso-style-name:"Balloon Text";
margin:0in;
margin-bottom:.0001pt;
mso-pagination:widow-orphan;
font-size:8.0pt;
font-family:Tahoma;
mso-fareast-font-family:"Times New Roman";}
@page Section1
{size:8.5in 11.0in;
margin:1.0in 1.25in 1.0in 1.25in;
mso-header-margin:.5in;
mso-footer-margin:.5in;
mso-paper-source:0;}
div.Section1
{page:Section1;}
-->
</style>
</head><body style="" lang="EN-US">

<div class="Section1">

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```

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<p class="MsoNormal" style="margin-left: 0.5in;"><span style="font-size: 10pt;">ii)</span>  
additions to the Program;</span></p>

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<p class="MsoNormal"><!--[if !supportEmptyParas]-->&nbsp;&nbsp;&nbsp;<!--[endif]--><o:p></o:p></p>

</div>

<div class="Section2">

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Version 2.0, January 2004<br>

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</body></html>

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```

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</head><body lang="EN-US" link="blue" vlink="purple">
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version 1.2.1, November 17th, 2003
```

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References

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    <title>SLF4J License</title>
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```
<link rel="stylesheet" type="text/css" media="screen" href="css/site.css" />
</head>
<body>
  <script type="text/javascript">prefix=";</script>

  <script src="templates/header.js" type="text/javascript"></script>
  <div id="left">
    <script src="templates/left.js" type="text/javascript"></script>
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## 1.309 jersey-container-servlet-core 2.22

## 1.310 javax-inject 2.4.0

## 1.311 Izo 2.10\_1

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The initial developer of the original code is Rainer Budde (<http://www.speed-soft.de>).

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## 1.318 aopalliance-repackaged 2.4.0

## 1.319 ipmiutil 1.8.18

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Upstream-Name: ipmiutil

Upstream-Contact: Andy Cress <arccress at users.sourceforge.net>

Source: <http://ipmiutil.sourceforge.net>

Comment: This COPYING file includes detail about each source file in ipmiutil

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Upstream-Contact: Andy Cress <[arcress@users.sourceforge.net](mailto:arcress@users.sourceforge.net)>

Source: <http://ipmiutil.sourceforge.net>

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Upstream-Contact: Andy Cress <[arcress@users.sourceforge.net](mailto:arcress@users.sourceforge.net)>

Source: <http://ipmiutil.sourceforge.net>

<http://www.openmash.org/lxr/source/src/getopt.c?c=gsm>

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## 1.321 tomcat-catalina 8.5.32

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## 1.322 clang 3.4.1

### 1.322.1 Available under license :

```
; RUN: opt -consthoist -S -o - %s | FileCheck %s
target datalayout = "e-m:e-p:32:32-i64:64-v128:64:128-a:0:32-n32-S64"
target triple = "thumbv6m-none--musleabi"
```

```
; Check that for i8 type, the maximum legal offset is 31.
; Also check that an constant used as value to be stored rather than
; pointer in a store instruction is hoisted.
; CHECK: foo_i8
; CHECK-DAG: %[[C1:const[0-9]?]] = bitcast i32 805874720 to i32
; CHECK-DAG: %[[C2:const[0-9]?]] = bitcast i32 805874688 to i32
; CHECK-DAG: %[[C3:const[0-9]?]] = bitcast i32 805873720 to i32
```

```

; CHECK-DAG: %[[C4:const[0-9]?]] = bitcast i32 805873688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to i8*
; CHECK-NEXT: %1 = load volatile i8, i8* %0
; CHECK-NEXT: %[[M1:const_mat[0-9]?]] = add i32 %[[C2]], 4
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to i8*
; CHECK-NEXT: %3 = load volatile i8, i8* %2
; CHECK-NEXT: %[[M2:const_mat[0-9]?]] = add i32 %[[C2]], 31
; CHECK-NEXT: %4 = inttoptr i32 %[[M2]] to i8*
; CHECK-NEXT: %5 = load volatile i8, i8* %4
; CHECK-NEXT: %6 = inttoptr i32 %[[C1]] to i8*
; CHECK-NEXT: %7 = load volatile i8, i8* %6
; CHECK-NEXT: %[[M3:const_mat[0-9]?]] = add i32 %[[C1]], 7
; CHECK-NEXT: %8 = inttoptr i32 %[[M3]] to i8*
; CHECK-NEXT: %9 = load volatile i8, i8* %8
; CHECK-NEXT: %10 = inttoptr i32 %[[C4]] to i8*
; CHECK-NEXT: store i8 %9, i8* %10
; CHECK-NEXT: %[[M4:const_mat[0-9]?]] = add i32 %[[C4]], 31
; CHECK-NEXT: %11 = inttoptr i32 %[[M4]] to i8*
; CHECK-NEXT: store i8 %7, i8* %11
; CHECK-NEXT: %12 = inttoptr i32 %[[C3]] to i8*
; CHECK-NEXT: store i8 %5, i8* %12
; CHECK-NEXT: %[[M5:const_mat[0-9]?]] = add i32 %[[C3]], 7
; CHECK-NEXT: %13 = inttoptr i32 %[[M5]] to i8*
; CHECK-NEXT: store i8 %3, i8* %13
; CHECK-NEXT: %[[M6:const_mat[0-9]?]] = add i32 %[[C1]], 80
; CHECK-NEXT: %14 = inttoptr i32 %[[M6]] to i8*
; CHECK-NEXT: store i8* %14, i8** @goo

```

```
@goo = global i8* undef
```

```

define void @foo_i8() {
entry:
%0 = load volatile i8, i8* inttoptr (i32 805874688 to i8*)
%1 = load volatile i8, i8* inttoptr (i32 805874692 to i8*)
%2 = load volatile i8, i8* inttoptr (i32 805874719 to i8*)
%3 = load volatile i8, i8* inttoptr (i32 805874720 to i8*)
%4 = load volatile i8, i8* inttoptr (i32 805874727 to i8*)
store i8 %4, i8* inttoptr(i32 805873688 to i8*)
store i8 %3, i8* inttoptr(i32 805873719 to i8*)
store i8 %2, i8* inttoptr(i32 805873720 to i8*)
store i8 %1, i8* inttoptr(i32 805873727 to i8*)
store i8* inttoptr(i32 805874800 to i8*), i8** @goo
ret void
}

```

```
; Check that for i16 type, the maximum legal offset is 62.
```

```
; CHECK: foo_i16
```

```
; CHECK-DAG: %[[C1:const[0-9]?]] = bitcast i32 805874752 to i32
```

```

; CHECK-DAG: %[[C2:const[0-9]?]] = bitcast i32 805874688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to i16*
; CHECK-NEXT: %1 = load volatile i16, i16* %0, align 2
; CHECK-NEXT: %[[M1:const_mat[0-9]?]] = add i32 %[[C2]], 4
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to i16*
; CHECK-NEXT: %3 = load volatile i16, i16* %2, align 2
; CHECK-NEXT: %[[M2:const_mat[0-9]?]] = add i32 %[[C2]], 32
; CHECK-NEXT: %4 = inttoptr i32 %[[M2]] to i16*
; CHECK-NEXT: %5 = load volatile i16, i16* %4, align 2
; CHECK-NEXT: %[[M3:const_mat[0-9]?]] = add i32 %[[C2]], 62
; CHECK-NEXT: %6 = inttoptr i32 %[[M3]] to i16*
; CHECK-NEXT: %7 = load volatile i16, i16* %6, align 2
; CHECK-NEXT: %8 = inttoptr i32 %[[C1]] to i16*
; CHECK-NEXT: %9 = load volatile i16, i16* %8, align 2
; CHECK-NEXT: %[[M4:const_mat[0-9]?]] = add i32 %[[C1]], 22
; CHECK-NEXT: %10 = inttoptr i32 %[[M4]] to i16*
; CHECK-NEXT: %11 = load volatile i16, i16* %10, align 2

```

```

define void @foo_i16() {
entry:
%0 = load volatile i16, i16* inttoptr (i32 805874688 to i16*), align 2
%1 = load volatile i16, i16* inttoptr (i32 805874692 to i16*), align 2
%2 = load volatile i16, i16* inttoptr (i32 805874720 to i16*), align 2
%3 = load volatile i16, i16* inttoptr (i32 805874750 to i16*), align 2
%4 = load volatile i16, i16* inttoptr (i32 805874752 to i16*), align 2
%5 = load volatile i16, i16* inttoptr (i32 805874774 to i16*), align 2
ret void
}

```

```

; Check that for i32 type, the maximum legal offset is 124.
; CHECK: foo_i32
; CHECK-DAG: %[[C1:const[0-9]?]] = bitcast i32 805874816 to i32
; CHECK-DAG: %[[C2:const[0-9]?]] = bitcast i32 805874688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to i32*
; CHECK-NEXT: %1 = load volatile i32, i32* %0, align 4
; CHECK-NEXT: %[[M1:const_mat[0-9]?]] = add i32 %[[C2]], 4
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to i32*
; CHECK-NEXT: %3 = load volatile i32, i32* %2, align 4
; CHECK-NEXT: %[[M2:const_mat[0-9]?]] = add i32 %[[C2]], 124
; CHECK-NEXT: %4 = inttoptr i32 %[[M2]] to i32*
; CHECK-NEXT: %5 = load volatile i32, i32* %4, align 4
; CHECK-NEXT: %6 = inttoptr i32 %[[C1]] to i32*
; CHECK-NEXT: %7 = load volatile i32, i32* %6, align 4
; CHECK-NEXT: %[[M3:const_mat[0-9]?]] = add i32 %[[C1]], 8
; CHECK-NEXT: %8 = inttoptr i32 %[[M3]] to i32*
; CHECK-NEXT: %9 = load volatile i32, i32* %8, align 4
; CHECK-NEXT: %[[M4:const_mat[0-9]?]] = add i32 %[[C1]], 12
; CHECK-NEXT: %10 = inttoptr i32 %[[M4]] to i32*

```

```
; CHECK-NEXT: %11 = load volatile i32, i32* %10, align 4
```

```
define void @foo_i32() {  
entry:  
%0 = load volatile i32, i32* inttoptr (i32 805874688 to i32*), align 4  
%1 = load volatile i32, i32* inttoptr (i32 805874692 to i32*), align 4  
%2 = load volatile i32, i32* inttoptr (i32 805874812 to i32*), align 4  
%3 = load volatile i32, i32* inttoptr (i32 805874816 to i32*), align 4  
%4 = load volatile i32, i32* inttoptr (i32 805874824 to i32*), align 4  
%5 = load volatile i32, i32* inttoptr (i32 805874828 to i32*), align 4  
ret void  
}
```

```
; RUN: llc -mtriple=aarch64-apple-ios %s -o - | FileCheck %s
```

```
define <16 x double> @test_sitofp_fixed(<16 x i32> %in) {
```

```
; CHECK-LABEL: test_sitofp_fixed:
```

```
; First, extend each i32 to i64
```

```
; CHECK-DAG: sshll2.2d [[BLOCK0_HI:v[0-9]+]], v0, #0
```

```
; CHECK-DAG: sshll2.2d [[BLOCK1_HI:v[0-9]+]], v1, #0
```

```
; CHECK-DAG: sshll2.2d [[BLOCK2_HI:v[0-9]+]], v2, #0
```

```
; CHECK-DAG: sshll2.2d [[BLOCK3_HI:v[0-9]+]], v3, #0
```

```
; CHECK-DAG: sshll.2d [[BLOCK0_LO:v[0-9]+]], v0, #0
```

```
; CHECK-DAG: sshll.2d [[BLOCK1_LO:v[0-9]+]], v1, #0
```

```
; CHECK-DAG: sshll.2d [[BLOCK2_LO:v[0-9]+]], v2, #0
```

```
; CHECK-DAG: sshll.2d [[BLOCK3_LO:v[0-9]+]], v3, #0
```

```
; Next, convert each to double.
```

```
; CHECK-DAG: scvtf.2d v0, [[BLOCK0_LO]]
```

```
; CHECK-DAG: scvtf.2d v1, [[BLOCK0_HI]]
```

```
; CHECK-DAG: scvtf.2d v2, [[BLOCK1_LO]]
```

```
; CHECK-DAG: scvtf.2d v3, [[BLOCK1_HI]]
```

```
; CHECK-DAG: scvtf.2d v4, [[BLOCK2_LO]]
```

```
; CHECK-DAG: scvtf.2d v5, [[BLOCK2_HI]]
```

```
; CHECK-DAG: scvtf.2d v6, [[BLOCK3_LO]]
```

```
; CHECK-DAG: scvtf.2d v7, [[BLOCK3_HI]]
```

```
; CHECK: ret
```

```
%flt = sitofp <16 x i32> %in to <16 x double>
```

```
%res = fdiv <16 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0,  
double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0,  
double 64.0>
```

```
ret <16 x double> %res
```

```
}
```

```
; This one is small enough to satisfy isSimple, but still illegally large.
```

```
define <4 x double> @test_sitofp_fixed_shortish(<4 x i64> %in) {
```

```
; CHECK-LABEL: test_sitofp_fixed_shortish:
```

```

; CHECK-DAG: scvtf.2d v0, v0
; CHECK-DAG: scvtf.2d v1, v1

; CHECK: ret
%flt = sitofp <4 x i64> %in to <4 x double>
%res = fdiv <4 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0>
ret <4 x double> %res
}
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; RUN: llc -march=hexagon < %s  
; REQUIRES: asserts

; The two loads based on %struct.0, loading two different data types  
; cause LSR to assume type "void" for the memory type. This would then  
; cause an assert in isLegalAddressingMode. Make sure we no longer crash.

target triple = "hexagon"

%struct.0 = type { i8\*, i8, %union.anon.0 }

%union.anon.0 = type { i8\* }

define hidden fastcc void @fred() unnamed\_addr #0 {

entry:

br i1 undef, label %while.end, label %while.body.lr.ph

while.body.lr.ph: ; preds = %entry

br label %while.body

while.body: ; preds = %exit.2, %while.body.lr.ph

%lsr.iv = phi %struct.0\* [ %cgep22, %exit.2 ], [ undef, %while.body.lr.ph ]

switch i32 undef, label %exit [



```

    i32 1, label %sw.bb.i
    i32 2, label %sw.bb3.i
]

sw.bb.i:                                ; preds = %while.body
unreachable

sw.bb3.i:                                ; preds = %while.body
unreachable

exit:                                    ; preds = %while.body
switch i32 undef, label %exit.2 [
  i32 1, label %sw.bb.i17
  i32 2, label %sw.bb3.i20
]

sw.bb.i17:                               ; preds = %.exit
%0 = bitcast %struct.0* %lshr.iv to i32*
%1 = load i32, i32* %0, align 4
unreachable

sw.bb3.i20:                              ; preds = %exit
%2 = bitcast %struct.0* %lshr.iv to i8**
%3 = load i8*, i8** %2, align 4
unreachable

exit.2:                                  ; preds = %exit
%cgep22 = getelementptr %struct.0, %struct.0* %lshr.iv, i32 1
br label %while.body

while.end:                                ; preds = %entry
ret void
}

attributes #0 = { nounwind optimize "target-cpu"="hexagonv55" }
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```

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```
; RUN: opt %s -inline -S | FileCheck %s
```

```
define internal void @innerSmall() "min-legal-vector-width"="128" {  
  ret void  
}
```

```
define internal void @innerLarge() "min-legal-vector-width"="512" {  
  ret void  
}
```

```
define internal void @innerNoAttribute() {  
  ret void  
}
```

; We should not add an attribute during inlining. No attribute means unknown.

; Inlining doesn't change the fact that we don't know anything about this

; function.

```
define void @outerNoAttribute() {  
  call void @innerLarge()  
  ret void  
}
```

```
define void @outerConflictingAttributeSmall() "min-legal-vector-width"="128" {  
  call void @innerLarge()  
  ret void  
}
```

```
define void @outerConflictingAttributeLarge() "min-legal-vector-width"="512" {  
  call void @innerSmall()  
  ret void  
}
```

; We should remove the attribute after inlining since the callee's

; vector width requirements are unknown.

```
define void @outerAttribute() "min-legal-vector-width"="128" {  
  call void @innerNoAttribute()  
  ret void  
}
```

```
; CHECK: define void @outerNoAttribute() {
```

```
; CHECK: define void @outerConflictingAttributeSmall() #0
```

```
; CHECK: define void @outerConflictingAttributeLarge() #0
```

```
; CHECK: define void @outerAttribute() {
```

```
; CHECK: attributes #0 = { "min-legal-vector-width"="512" }
```

```
; RUN: llc < %s -mtriple=x86_64-linux-android -mattr=+mmx -enable-legalize-types-checking | FileCheck %s
```

```
;
```

```
; D31946
```

; Check that we dont end up with the ""LLVM ERROR: Cannot select" error.

; Additionally ensure that the output code actually put fp128 values in SSE registers.

```
declare fp128 @llvm.fabs.f128(fp128)
```

```
declare fp128 @llvm.copysign.f128(fp128, fp128)
```

```
define fp128 @TestSelect(fp128 %a, fp128 %b) {
```

```
  %cmp = fcmp ogt fp128 %a, %b
```

```
  %sub = fsub fp128 %a, %b
```

```
  %res = select i1 %cmp, fp128 %sub, fp128 0xL00000000000000000000000000000000
```

```
  ret fp128 %res
```

```
; CHECK-LABEL: TestSelect:
```

```
; CHECK      movaps 16(%rsp), %xmm1
```

```
; CHECK-NEXT callq __subtf3
```

```
; CHECK-NEXT testl %ebx, %ebx
```

```

; CHECK-NEXT jg .LBB0_2
; CHECK-NEXT # %bb.1:
; CHECK-NEXT movaps .LCPI0_0(%rip), %xmm0
; CHECK-NEXT .LBB0_2:
; CHECK-NEXT addq $32, %rsp
; CHECK-NEXT popq %rbx
; CHECK-NEXT retq
}

```

```

define fp128 @TestFabs(fp128 %a) {
%res = call fp128 @llvm.fabs.f128(fp128 %a)
ret fp128 %res
; CHECK-LABEL: TestFabs:
; CHECK andps .LCPI1_0(%rip), %xmm0
; CHECK-NEXT retq
}

```

```

define fp128 @TestCopsign(fp128 %a, fp128 %b) {
%res = call fp128 @llvm.copsign.f128(fp128 %a, fp128 %b)
ret fp128 %res
; CHECK-LABEL: TestCopsign:
; CHECK andps .LCPI2_1(%rip), %xmm0
; CHECK-NEXT orps %xmm1, %xmm0
; CHECK-NEXT retq
}

```

```

define fp128 @TestFneg(fp128 %a) {
%mul = fmul fp128 %a, %a
%res = fsub fp128 0xL00000000000000008000000000000000, %mul
ret fp128 %res
; CHECK-LABEL: TestFneg:
; CHECK movaps %xmm0, %xmm1
; CHECK-NEXT callq __multf3
; CHECK-NEXT xorps .LCPI3_0(%rip), %xmm0
; CHECK-NEXT popq %rax
; CHECK-NEXT retq
}

```

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; NOTE: Assertions have been autogenerated by utils/update\_llc\_test\_checks.py

; RUN: llc < %s -mtriple=x86\_64-unknown-unknown -mattr=avx512vl,avx512bw,avx512dq,prefer-256-bit | FileCheck %s

; This file primarily contains tests for specific places in X86ISelLowering.cpp that needed be made aware of the legalizer not allowing 512-bit vectors due to prefer-256-bit even though AVX512 is enabled.

```
define void @add256(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="256" {
; CHECK-LABEL: add256:
; CHECK:      # %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT: vpaddd (%rsi), %ymm0, %ymm0
; CHECK-NEXT: vpaddd 32(%rsi), %ymm1, %ymm1
```





```

%a = sext <32 x i16> %A to <32 x i32>
%b = sext <32 x i16> %B to <32 x i32>
%m = mul nsw <32 x i32> %a, %b
%odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32
12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
%even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32
13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
%ret = add <16 x i32> %odd, %even
store <16 x i32> %ret, <16 x i32>* %CPtr
ret void
}

```

```

define void @pmaddwd_32_512(<32 x i16>* %APtr, <32 x i16>* %BPtr, <16 x i32>* %CPtr) "min-legal-vector-
width"="512" {

```

```

; CHECK-LABEL: pmaddwd_32_512:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT:  vpmaddwd (%rsi), %zmm0, %zmm0
; CHECK-NEXT:  vmovdqa64 %zmm0, (%rdx)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%A = load <32 x i16>, <32 x i16>* %APtr
%B = load <32 x i16>, <32 x i16>* %BPtr
%a = sext <32 x i16> %A to <32 x i32>
%b = sext <32 x i16> %B to <32 x i32>
%m = mul nsw <32 x i32> %a, %b
%odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32
12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
%even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32
13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
%ret = add <16 x i32> %odd, %even
store <16 x i32> %ret, <16 x i32>* %CPtr
ret void
}

```

```

define void @psubus_64i8_max_256(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-
width"="256" {

```

```

; CHECK-LABEL: psubus_64i8_max_256:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:  vpsubusb (%rsi), %ymm0, %ymm0
; CHECK-NEXT:  vpsubusb 32(%rsi), %ymm1, %ymm1
; CHECK-NEXT:  vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT:  vmovdqa %ymm0, (%rdx)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%x = load <64 x i8>, <64 x i8>* %xptr

```

```

%y = load <64 x i8>, <64 x i8>* %yptr
%cmp = icmp ult <64 x i8> %x, %y
%max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
%res = sub <64 x i8> %max, %y
store <64 x i8> %res, <64 x i8>* %zptr
ret void
}

```

```

define void @psubus_64i8_max_512(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-
width"="512" {
; CHECK-LABEL: psubus_64i8_max_512:
; CHECK:      # %bb.0:
; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT: vpsubusb (%rsi), %zmm0, %zmm0
; CHECK-NEXT: vmovdqa64 %zmm0, (%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <64 x i8>, <64 x i8>* %xptr
%y = load <64 x i8>, <64 x i8>* %yptr
%cmp = icmp ult <64 x i8> %x, %y
%max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
%res = sub <64 x i8> %max, %y
store <64 x i8> %res, <64 x i8>* %zptr
ret void
}

```

```

define i32 @_Z9test_charPcS_i_256(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-
width"="256" {
; CHECK-LABEL: _Z9test_charPcS_i_256:
; CHECK:      # %bb.0: # %entry
; CHECK-NEXT: movl %edx, %eax
; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT: xorl %ecx, %ecx
; CHECK-NEXT: vpxor %xmm1, %xmm1, %xmm1
; CHECK-NEXT: vpxor %xmm2, %xmm2, %xmm2
; CHECK-NEXT: .p2align 4, 0x90
; CHECK-NEXT: .LBB8_1: # %vector.body
; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
; CHECK-NEXT: vpmovsxbw (%rdi,%rcx), %ymm3
; CHECK-NEXT: vpmovsxbw 16(%rdi,%rcx), %ymm4
; CHECK-NEXT: vpmovsxbw (%rsi,%rcx), %ymm5
; CHECK-NEXT: vpmaddwd %ymm3, %ymm5, %ymm3
; CHECK-NEXT: vpadd %ymm1, %ymm3, %ymm1
; CHECK-NEXT: vpmovsxbw 16(%rsi,%rcx), %ymm3
; CHECK-NEXT: vpmaddwd %ymm4, %ymm3, %ymm3
; CHECK-NEXT: vpadd %ymm2, %ymm3, %ymm2
; CHECK-NEXT: addq $32, %rcx
; CHECK-NEXT: cmpq %rcx, %rax

```

```

; CHECK-NEXT:   jne .LBB8_1
; CHECK-NEXT:   # %bb.2: # %middle.block
; CHECK-NEXT:   vpaddd %ymm0, %ymm1, %ymm1
; CHECK-NEXT:   vpaddd %ymm0, %ymm2, %ymm0
; CHECK-NEXT:   vpaddd %ymm0, %ymm1, %ymm0
; CHECK-NEXT:   vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT:   vpaddd %ymm1, %ymm0, %ymm0
; CHECK-NEXT:   vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT:   vpaddd %ymm1, %ymm0, %ymm0
; CHECK-NEXT:   vpshufd {{.*#+}} xmm1 = xmm0[1,1,2,3]
; CHECK-NEXT:   vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT:   vmovd %xmm0, %eax
; CHECK-NEXT:   vzeroupper
; CHECK-NEXT:   retq

```

entry:

```

%3 = zext i32 %2 to i64
br label %vector.body

```

vector.body:

```

%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%4 = getelementptr inbounds i8, i8* %0, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mul nsw <32 x i32> %9, %6
%11 = add nsw <32 x i32> %10, %vec.phi
%index.next = add i64 %index, 32
%12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body

```

middle.block:

```

%rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20,
i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef>
%bin.rdx1 = add <32 x i32> %11, %rdx.shuf1
%rdx.shuf = shufflevector <32 x i32> %bin.rdx1, <32 x i32> undef, <32 x i32> <i32 8, i32 9, i32 10, i32 11, i32
12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx = add <32 x i32> %bin.rdx1, %rdx.shuf
%rdx.shuf15 = shufflevector <32 x i32> %bin.rdx, <32 x i32> undef, <32 x i32> <i32 4, i32 5, i32 6, i32 7, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>

```

```

i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx32 = add <32 x i32> %bin.rdx, %rdx.shuf15
%rdx.shuf17 = shufflevector <32 x i32> %bin.rdx32, <32 x i32> undef, <32 x i32> <i32 2, i32 3, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx18 = add <32 x i32> %bin.rdx32, %rdx.shuf17
%rdx.shuf19 = shufflevector <32 x i32> %bin.rdx18, <32 x i32> undef, <32 x i32> <i32 1, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx20 = add <32 x i32> %bin.rdx18, %rdx.shuf19
%13 = extractelement <32 x i32> %bin.rdx20, i32 0
ret i32 %13
}

```

```

define i32 @_Z9test_charPcS_i_512(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-
width"="512" {

```

```

; CHECK-LABEL: _Z9test_charPcS_i_512:
; CHECK:    # %bb.0: # %entry
; CHECK-NEXT:  movl %edx, %eax
; CHECK-NEXT:  vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT:  xorl %ecx, %ecx
; CHECK-NEXT:  vpxor %xmm1, %xmm1, %xmm1
; CHECK-NEXT:  .p2align 4, 0x90
; CHECK-NEXT: .LBB9_1: # %vector.body
; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
; CHECK-NEXT: vpmovsxbw (%rdi,%rcx), %zmm2
; CHECK-NEXT: vpmovsxbw (%rsi,%rcx), %zmm3
; CHECK-NEXT: vpmaddwd %zmm2, %zmm3, %zmm2
; CHECK-NEXT: vpadd %zmm1, %zmm2, %zmm1
; CHECK-NEXT: addq $32, %rcx
; CHECK-NEXT: cmpq %rcx, %rax
; CHECK-NEXT: jne .LBB9_1
; CHECK-NEXT: # %bb.2: # %middle.block
; CHECK-NEXT: vpadd %zmm0, %zmm1, %zmm0
; CHECK-NEXT: vextracti64x4 $1, %zmm0, %ymm1
; CHECK-NEXT: vpadd %zmm1, %zmm0, %zmm0
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT: vpadd %zmm1, %zmm0, %zmm0
; CHECK-NEXT: vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT: vpadd %zmm1, %zmm0, %zmm0
; CHECK-NEXT: vpshufd {{.*#+}} xmm1 = xmm0[1,1,2,3]
; CHECK-NEXT: vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vmovd %xmm0, %eax
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq

```







```

%8 = sub nsw <16 x i32> zeroinitializer, %6
%9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
%10 = add nsw <16 x i32> %9, %vec.phi
%index.next = add i64 %index, 4
%11 = icmp eq i64 %index.next, 1024
br i1 %11, label %middle.block, label %vector.body

```

middle.block:

```

%.lcssa = phi <16 x i32> [ %10, %vector.body ]
%rdx.shuf = shufflevector <16 x i32> %.lcssa, <16 x i32> undef, <16 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12,
i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx = add <16 x i32> %.lcssa, %rdx.shuf
%rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> <i32 4, i32 5, i32 6, i32 7, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef>
%bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> <i32 2, i32 3, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef>
%bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> <i32 1, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef>
%bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
ret i32 %12
}

```

```

define i32 @sad_16i8_512() "min-legal-vector-width"="512" {
; CHECK-LABEL: sad_16i8_512:
; CHECK:      # %bb.0: # %entry
; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT: movq $-1024, %rax # imm = 0xFC00
; CHECK-NEXT: .p2align 4, 0x90
; CHECK-NEXT: .LBB11_1: # %vector.body
; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
; CHECK-NEXT: vmovdqu a+1024(%rax), %xmm1
; CHECK-NEXT: vpsadbw b+1024(%rax), %xmm1, %xmm1
; CHECK-NEXT: vpaddd %zmm0, %zmm1, %zmm0
; CHECK-NEXT: addq $4, %rax
; CHECK-NEXT: jne .LBB11_1
; CHECK-NEXT: # %bb.2: # %middle.block
; CHECK-NEXT: vextracti64x4 $1, %zmm0, %ymm1
; CHECK-NEXT: vpaddd %zmm1, %zmm0, %zmm0
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT: vpaddd %zmm1, %zmm0, %zmm0
; CHECK-NEXT: vpsshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT: vpaddd %zmm1, %zmm0, %zmm0

```

```

; CHECK-NEXT:  vpsufd { .*#+ } xmm1 = xmm0[1,1,2,3]
; CHECK-NEXT:  vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT:  vmovd %xmm0, %eax
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
entry:
br label %vector.body

vector.body:
%index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
%vec.phi = phi <16 x i32> [ zeroinitializer, %entry ], [ %10, %vector.body ]
%0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
%1 = bitcast i8* %0 to <16 x i8>*
%wide.load = load <16 x i8>, <16 x i8>* %1, align 4
%2 = zext <16 x i8> %wide.load to <16 x i32>
%3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
%4 = bitcast i8* %3 to <16 x i8>*
%wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4
%5 = zext <16 x i8> %wide.load1 to <16 x i32>
%6 = sub nsw <16 x i32> %2, %5
%7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>
%8 = sub nsw <16 x i32> zeroinitializer, %6
%9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
%10 = add nsw <16 x i32> %9, %vec.phi
%index.next = add i64 %index, 4
%11 = icmp eq i64 %index.next, 1024
br i1 %11, label %middle.block, label %vector.body

middle.block:
%.lcssa = phi <16 x i32> [ %10, %vector.body ]
%rdx.shuf = shufflevector <16 x i32> %.lcssa, <16 x i32> undef, <16 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx = add <16 x i32> %.lcssa, %rdx.shuf
%rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> <i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> <i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> <i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
ret i32 %12

```

```

}

define void @sbto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: sbto16f32_256:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vpmovw2m %ymm0, %k0
; CHECK-NEXT:  kshiftrw $8, %k0, %k1
; CHECK-NEXT:  vpmovm2d %k1, %ymm0
; CHECK-NEXT:  vcvtdq2ps %ymm0, %ymm0
; CHECK-NEXT:  vpmovm2d %k0, %ymm1
; CHECK-NEXT:  vcvtdq2ps %ymm1, %ymm1
; CHECK-NEXT:  vmovaps %ymm1, (%rdi)
; CHECK-NEXT:  vmovaps %ymm0, 32(%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @sbto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: sbto16f32_512:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vpmovw2m %ymm0, %k0
; CHECK-NEXT:  vpmovm2d %k0, %zmm0
; CHECK-NEXT:  vcvtdq2ps %zmm0, %zmm0
; CHECK-NEXT:  vmovaps %zmm0, (%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @sbto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: sbto16f64_256:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vpmovw2m %ymm0, %k0
; CHECK-NEXT:  kshiftrw $8, %k0, %k1
; CHECK-NEXT:  vpmovm2d %k1, %ymm0
; CHECK-NEXT:  vcvtdq2pd %xmm0, %ymm1
; CHECK-NEXT:  vextracti128 $1, %ymm0, %xmm0
; CHECK-NEXT:  vcvtdq2pd %xmm0, %ymm0
; CHECK-NEXT:  vpmovm2d %k0, %ymm2
; CHECK-NEXT:  vcvtdq2pd %xmm2, %ymm3
; CHECK-NEXT:  vextracti128 $1, %ymm2, %xmm2

```

```

; CHECK-NEXT:   vcvtdq2pd %xmm2, %ymm2
; CHECK-NEXT:   vmovaps %ymm2, 32(%rdi)
; CHECK-NEXT:   vmovaps %ymm3, (%rdi)
; CHECK-NEXT:   vmovaps %ymm0, 96(%rdi)
; CHECK-NEXT:   vmovaps %ymm1, 64(%rdi)
; CHECK-NEXT:   vzeroupper
; CHECK-NEXT:   retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

define void @sbto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: sbto16f64_512:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vpmovw2m %ymm0, %k0
; CHECK-NEXT:  vpmovm2d %k0, %zmm0
; CHECK-NEXT:  vcvtdq2pd %ymm0, %zmm1
; CHECK-NEXT:  vextracti64x4 $1, %zmm0, %ymm0
; CHECK-NEXT:  vcvtdq2pd %ymm0, %zmm0
; CHECK-NEXT:  vmovaps %zmm0, 64(%rdi)
; CHECK-NEXT:  vmovaps %zmm1, (%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

define void @ubto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: ubto16f32_256:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vpmovw2m %ymm0, %k0
; CHECK-NEXT:  kshiftrw $8, %k0, %k1
; CHECK-NEXT:  vpmovm2d %k1, %ymm0
; CHECK-NEXT:  vpsrld $31, %ymm0, %ymm0
; CHECK-NEXT:  vcvtdq2ps %ymm0, %ymm0
; CHECK-NEXT:  vpmovm2d %k0, %ymm1
; CHECK-NEXT:  vpsrld $31, %ymm1, %ymm1
; CHECK-NEXT:  vcvtdq2ps %ymm1, %ymm1
; CHECK-NEXT:  vmovaps %ymm1, (%rdi)
; CHECK-NEXT:  vmovaps %ymm0, 32(%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x float>

```

```

store <16 x float> %1, <16 x float>* %res
ret void
}

define void @ubto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: ubto16f32_512:
; CHECK:      # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: vpmovm2d %k0, %zmm0
; CHECK-NEXT: vpsrld $31, %zmm0, %zmm0
; CHECK-NEXT: vcvtdq2ps %zmm0, %zmm0
; CHECK-NEXT: vmovaps %zmm0, (%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @ubto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: ubto16f64_256:
; CHECK:      # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: kshiftrw $8, %k0, %k1
; CHECK-NEXT: vpmovm2d %k1, %ymm0
; CHECK-NEXT: vpsrld $31, %ymm0, %ymm0
; CHECK-NEXT: vcvtdq2pd %xmm0, %ymm1
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm0
; CHECK-NEXT: vcvtdq2pd %xmm0, %ymm0
; CHECK-NEXT: vpmovm2d %k0, %ymm2
; CHECK-NEXT: vpsrld $31, %ymm2, %ymm2
; CHECK-NEXT: vcvtdq2pd %xmm2, %ymm3
; CHECK-NEXT: vextracti128 $1, %ymm2, %xmm2
; CHECK-NEXT: vcvtdq2pd %xmm2, %ymm2
; CHECK-NEXT: vmovaps %ymm2, 32(%rdi)
; CHECK-NEXT: vmovaps %ymm3, (%rdi)
; CHECK-NEXT: vmovaps %ymm0, 96(%rdi)
; CHECK-NEXT: vmovaps %ymm1, 64(%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

define void @ubto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {

```

```

; CHECK-LABEL: ubto16f64_512:
; CHECK:      # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: vpmovm2d %k0, %zmm0
; CHECK-NEXT: vpsrld $31, %zmm0, %zmm0
; CHECK-NEXT: vcvtdq2pd %ymm0, %zmm1
; CHECK-NEXT: vextracti64x4 $1, %zmm0, %ymm0
; CHECK-NEXT: vcvtdq2pd %ymm0, %zmm0
; CHECK-NEXT: vmovaps %zmm0, 64(%rdi)
; CHECK-NEXT: vmovaps %zmm1, (%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

```

```

define <16 x i16> @test_16f32toub_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-
width"="256" {

```

```

; CHECK-LABEL: test_16f32toub_256:
; CHECK:      # %bb.0:
; CHECK-NEXT: vcvttps2dq (%rdi), %ymm1
; CHECK-NEXT: vpslld $31, %ymm1, %ymm1
; CHECK-NEXT: vpmovd2m %ymm1, %k0
; CHECK-NEXT: vcvttps2dq 32(%rdi), %ymm1
; CHECK-NEXT: vpslld $31, %ymm1, %ymm1
; CHECK-NEXT: vpmovd2m %ymm1, %k1
; CHECK-NEXT: kunpckbw %k0, %k1, %k1
; CHECK-NEXT: vmovdqu16 %ymm0, %ymm0 {%k1} {z}
; CHECK-NEXT: retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptoui <16 x float> %a to <16 x i1>
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select
}

```

```

define <16 x i16> @test_16f32toub_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-
width"="512" {

```

```

; CHECK-LABEL: test_16f32toub_512:
; CHECK:      # %bb.0:
; CHECK-NEXT: vcvttps2dq (%rdi), %zmm1
; CHECK-NEXT: vpslld $31, %zmm1, %zmm1
; CHECK-NEXT: vpmovd2m %zmm1, %k1
; CHECK-NEXT: vmovdqu16 %ymm0, %ymm0 {%k1} {z}
; CHECK-NEXT: retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptoui <16 x float> %a to <16 x i1>

```

```

%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select
}

```

```

define <16 x i16> @test_16f32tosb_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-
width"="256" {
; CHECK-LABEL: test_16f32tosb_256:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vcvttps2dq (%rdi), %ymm1
; CHECK-NEXT:  vpmovd2m %ymm1, %k0
; CHECK-NEXT:  vcvttps2dq 32(%rdi), %ymm1
; CHECK-NEXT:  vpmovd2m %ymm1, %k1
; CHECK-NEXT:  kunpckbw %k0, %k1, %k1
; CHECK-NEXT:  vmovdqu16 %ymm0, %ymm0 {%k1} {z}
; CHECK-NEXT:  retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptosi <16 x float> %a to <16 x i1>
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select
}

```

```

define <16 x i16> @test_16f32tosb_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-
width"="512" {
; CHECK-LABEL: test_16f32tosb_512:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vcvttps2dq (%rdi), %zmm1
; CHECK-NEXT:  vpmovd2m %zmm1, %k1
; CHECK-NEXT:  vmovdqu16 %ymm0, %ymm0 {%k1} {z}
; CHECK-NEXT:  retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptosi <16 x float> %a to <16 x i1>
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select
}

```

```

define void @mul256(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="256" {
; CHECK-LABEL: mul256:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:  vmovdqa (%rsi), %ymm2
; CHECK-NEXT:  vmovdqa 32(%rsi), %ymm3
; CHECK-NEXT:  vpunpckhbw {{.*#+}} %ymm4 =
ymm2[8],ymm0[8],ymm2[9],ymm0[9],ymm2[10],ymm0[10],ymm2[11],ymm0[11],ymm2[12],ymm0[12],ymm2[13],
ymm0[13],ymm2[14],ymm0[14],ymm2[15],ymm0[15],ymm2[24],ymm0[24],ymm2[25],ymm0[25],ymm2[26],ymm
m0[26],ymm2[27],ymm0[27],ymm2[28],ymm0[28],ymm2[29],ymm0[29],ymm2[30],ymm0[30],ymm2[31],ymm0[
31]
; CHECK-NEXT:  vpunpckhbw {{.*#+}} %ymm5 =

```



```

ymm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-NEXT: vpmullw %ymm4, %ymm5, %ymm4
; CHECK-NEXT: vmovdqa {{.*#+}} ymm5 =
[255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255]
; CHECK-NEXT: vpand %ymm5, %ymm4, %ymm4
; CHECK-NEXT: vpunpcklbw {{.*#+}} ymm2 =
ymm2[0],ymm0[0],ymm2[1],ymm0[1],ymm2[2],ymm0[2],ymm2[3],ymm0[3],ymm2[4],ymm0[4],ymm2[5],ymm0[
5],ymm2[6],ymm0[6],ymm2[7],ymm0[7],ymm2[16],ymm0[16],ymm2[17],ymm0[17],ymm2[18],ymm0[18],ymm2[
19],ymm0[19],ymm2[20],ymm0[20],ymm2[21],ymm0[21],ymm2[22],ymm0[22],ymm2[23],ymm0[23]
; CHECK-NEXT: vpunpcklbw {{.*#+}} ymm0 =
ymm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-NEXT: vpmullw %ymm2, %ymm0, %ymm0
; CHECK-NEXT: vpand %ymm5, %ymm0, %ymm0
; CHECK-NEXT: vpackuswb %ymm4, %ymm0, %ymm0
; CHECK-NEXT: vpunpckhbw {{.*#+}} ymm2 =
ymm3[8],ymm0[8],ymm3[9],ymm0[9],ymm3[10],ymm0[10],ymm3[11],ymm0[11],ymm3[12],ymm0[12],ymm3[13
],ymm0[13],ymm3[14],ymm0[14],ymm3[15],ymm0[15],ymm3[24],ymm0[24],ymm3[25],ymm0[25],ymm3[26],ym
m0[26],ymm3[27],ymm0[27],ymm3[28],ymm0[28],ymm3[29],ymm0[29],ymm3[30],ymm0[30],ymm3[31],ymm0[
31]
; CHECK-NEXT: vpunpckhbw {{.*#+}} ymm4 =
ymm1[8],ymm0[8],ymm1[9],ymm0[9],ymm1[10],ymm0[10],ymm1[11],ymm0[11],ymm1[12],ymm0[12],ymm1[13
],ymm0[13],ymm1[14],ymm0[14],ymm1[15],ymm0[15],ymm1[24],ymm0[24],ymm1[25],ymm0[25],ymm1[26],ym
m0[26],ymm1[27],ymm0[27],ymm1[28],ymm0[28],ymm1[29],ymm0[29],ymm1[30],ymm0[30],ymm1[31],ymm0[
31]
; CHECK-NEXT: vpmullw %ymm2, %ymm4, %ymm2
; CHECK-NEXT: vpand %ymm5, %ymm2, %ymm2
; CHECK-NEXT: vpunpcklbw {{.*#+}} ymm3 =
ymm3[0],ymm0[0],ymm3[1],ymm0[1],ymm3[2],ymm0[2],ymm3[3],ymm0[3],ymm3[4],ymm0[4],ymm3[5],ymm0[
5],ymm3[6],ymm0[6],ymm3[7],ymm0[7],ymm3[16],ymm0[16],ymm3[17],ymm0[17],ymm3[18],ymm0[18],ymm3[
19],ymm0[19],ymm3[20],ymm0[20],ymm3[21],ymm0[21],ymm3[22],ymm0[22],ymm3[23],ymm0[23]
; CHECK-NEXT: vpunpcklbw {{.*#+}} ymm1 =
ymm1[0],ymm0[0],ymm1[1],ymm0[1],ymm1[2],ymm0[2],ymm1[3],ymm0[3],ymm1[4],ymm0[4],ymm1[5],ymm0[
5],ymm1[6],ymm0[6],ymm1[7],ymm0[7],ymm1[16],ymm0[16],ymm1[17],ymm0[17],ymm1[18],ymm0[18],ymm1[
19],ymm0[19],ymm1[20],ymm0[20],ymm1[21],ymm0[21],ymm1[22],ymm0[22],ymm1[23],ymm0[23]
; CHECK-NEXT: vpmullw %ymm3, %ymm1, %ymm1
; CHECK-NEXT: vpand %ymm5, %ymm1, %ymm1
; CHECK-NEXT: vpackuswb %ymm2, %ymm1, %ymm1
; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT: vmovdqa %ymm0, (%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%d = load <64 x i8>, <64 x i8>* %a
%e = load <64 x i8>, <64 x i8>* %b
%f = mul <64 x i8> %d, %e
store <64 x i8> %f, <64 x i8>* %c
ret void
}

```

```

define void @mul512(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="512" {
; CHECK-LABEL: mul512:
; CHECK:      # %bb.0:
; CHECK-NEXT:  vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT:  vmovdqa64 (%rsi), %zmm1
; CHECK-NEXT:  vpunpckhbw {{.*#+}} zmm2 =
zmm1[8],zmm0[8],zmm1[9],zmm0[9],zmm1[10],zmm0[10],zmm1[11],zmm0[11],zmm1[12],zmm0[12],zmm1[13],
zmm0[13],zmm1[14],zmm0[14],zmm1[15],zmm0[15],zmm1[24],zmm0[24],zmm1[25],zmm0[25],zmm1[26],zmm0
[26],zmm1[27],zmm0[27],zmm1[28],zmm0[28],zmm1[29],zmm0[29],zmm1[30],zmm0[30],zmm1[31],zmm0[31],z
mm1[40],zmm0[40],zmm1[41],zmm0[41],zmm1[42],zmm0[42],zmm1[43],zmm0[43],zmm1[44],zmm0[44],zmm1[
45],zmm0[45],zmm1[46],zmm0[46],zmm1[47],zmm0[47],zmm1[56],zmm0[56],zmm1[57],zmm0[57],zmm1[58],z
mm0[58],zmm1[59],zmm0[59],zmm1[60],zmm0[60],zmm1[61],zmm0[61],zmm1[62],zmm0[62],zmm1[63],zmm0[
63]
; CHECK-NEXT:  vpunpckhbw {{.*#+}} zmm3 =
zmm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,4
2,42,43,43,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63]
; CHECK-NEXT:  vpmullw %zmm2, %zmm3, %zmm2
; CHECK-NEXT:  vmovdqa64 {{.*#+}} zmm3 =
[255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,2
55,255,255,255,255]
; CHECK-NEXT:  vpandq %zmm3, %zmm2, %zmm2
; CHECK-NEXT:  vpunpcklbw {{.*#+}} zmm1 =
zmm1[0],zmm0[0],zmm1[1],zmm0[1],zmm1[2],zmm0[2],zmm1[3],zmm0[3],zmm1[4],zmm0[4],zmm1[5],zmm0[5]
,zmm1[6],zmm0[6],zmm1[7],zmm0[7],zmm1[16],zmm0[16],zmm1[17],zmm0[17],zmm1[18],zmm0[18],zmm1[19]
,zmm0[19],zmm1[20],zmm0[20],zmm1[21],zmm0[21],zmm1[22],zmm0[22],zmm1[23],zmm0[23],zmm1[32],zmm
0[32],zmm1[33],zmm0[33],zmm1[34],zmm0[34],zmm1[35],zmm0[35],zmm1[36],zmm0[36],zmm1[37],zmm0[37],
zmm1[38],zmm0[38],zmm1[39],zmm0[39],zmm1[48],zmm0[48],zmm1[49],zmm0[49],zmm1[50],zmm0[50],zmm1
[51],zmm0[51],zmm1[52],zmm0[52],zmm1[53],zmm0[53],zmm1[54],zmm0[54],zmm1[55],zmm0[55]
; CHECK-NEXT:  vpunpcklbw {{.*#+}} zmm0 =
zmm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23,32,32,33,33,34,34,35,35,36,3
6,37,37,38,38,39,39,48,48,49,49,50,50,51,51,52,52,53,53,54,54,55,55]
; CHECK-NEXT:  vpmullw %zmm1, %zmm0, %zmm0
; CHECK-NEXT:  vpandq %zmm3, %zmm0, %zmm0
; CHECK-NEXT:  vpackuswb %zmm2, %zmm0, %zmm0
; CHECK-NEXT:  vmovdqa64 %zmm0, (%rdx)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%d = load <64 x i8>, <64 x i8>* %a
%e = load <64 x i8>, <64 x i8>* %b
%f = mul <64 x i8> %d, %e
store <64 x i8> %f, <64 x i8>* %c
ret void
}
$OpenBSD: COPYRIGHT,v 1.3 2003/06/02 20:18:36 millert Exp $

```

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LLVM Team

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| -----                                                                                                                                                                            | -----                                                       |
| Google Test                                                                                                                                                                      | llvm/utils/unittest/googletest                              |
| OpenBSD regex                                                                                                                                                                    | llvm/lib/Support/{reg*, COPYRIGHT.regex}                    |
| pyyaml tests                                                                                                                                                                     | llvm/test/YAMLParse/{*.data, LICENSE.TXT}                   |
| ARM contributions                                                                                                                                                                | llvm/lib/Target/ARM/LICENSE.TXT                             |
| md5 contributions                                                                                                                                                                | llvm/lib/Support/MD5.cpp<br>llvm/include/llvm/Support/MD5.h |
| ; NOTE: Assertions have been autogenerated by utils/update_analyze_test_checks.py                                                                                                |                                                             |
| ; RUN: opt < %s -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx2   FileCheck %s --check-prefixes=CHECK,VEC256,AVX                                            |                                                             |
| ; RUN: opt < %s -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+avx512bw,+avx512dq,+prefer-256-bit   FileCheck %s --check-prefixes=CHECK,VEC256,SKX256 |                                                             |
| ; RUN: opt < %s -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+avx512bw,+avx512dq,-prefer-256-bit   FileCheck %s --check-prefixes=CHECK,VEC512        |                                                             |
| define void @zext256() "min-legal-vector-width"="256" {                                                                                                                          |                                                             |
| ; VEC256-LABEL: 'zext256'                                                                                                                                                        |                                                             |
| ; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = zext <8 x i16> undef to <8 x i64>                                                                  |                                                             |
| ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>                                                                  |                                                             |
| ; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>                                                                 |                                                             |
| ; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = zext <16 x i16> undef to <16 x i32>                                                                |                                                             |
| ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>                                                                 |                                                             |
| ; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void                                                                                                |                                                             |
| ;                                                                                                                                                                                |                                                             |
| ; VEC512-LABEL: 'zext256'                                                                                                                                                        |                                                             |
| ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>                                                                  |                                                             |
| ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x                                                                       |                                                             |

```

i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x
i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x
i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x
i16>
; VEC512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
%A = zext <8 x i16> undef to <8 x i64>
%B = zext <8 x i32> undef to <8 x i64>
%C = zext <16 x i8> undef to <16 x i32>
%D = zext <16 x i16> undef to <16 x i32>
%E = zext <32 x i8> undef to <32 x i16>
ret void
}

```

```

define void @zext512() "min-legal-vector-width"="512" {
; AVX-LABEL: 'zext512'
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = zext <8 x i16> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = zext <16 x i16> undef to <16 x
i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX256-LABEL: 'zext512'
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x
i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x
i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x
i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; VEC512-LABEL: 'zext512'
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x
i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x
i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x
i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x

```

```

i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x
i16>
; VEC512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
%A = zext <8 x i16> undef to <8 x i64>
%B = zext <8 x i32> undef to <8 x i64>
%C = zext <16 x i8> undef to <16 x i32>
%D = zext <16 x i16> undef to <16 x i32>
%E = zext <32 x i8> undef to <32 x i16>
ret void
}

```

```

define void @sext256() "min-legal-vector-width"="256" {
; VEC256-LABEL: 'sext256'
; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %B = sext <8 x i16> undef to <8 x
i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x
i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x
i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %E = sext <16 x i16> undef to <16 x
i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x
i16>
; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; VEC512-LABEL: 'sext256'
; VEC512-NEXT: Cost Model: Found an estimated cost of 24 for instruction: %A = sext <8 x i8> undef to <8 x
i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x
i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x
i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x
i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x
i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x
i16>
; VEC512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
%A = sext <8 x i8> undef to <8 x i64>
%B = sext <8 x i16> undef to <8 x i64>
%C = sext <8 x i32> undef to <8 x i64>
%D = sext <16 x i8> undef to <16 x i32>
%E = sext <16 x i16> undef to <16 x i32>

```

```

%F = sext <32 x i8> undef to <32 x i16>
ret void
}

define void @sext512() "min-legal-vector-width"="512" {
; AVX-LABEL: 'sext512'
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX256-LABEL: 'sext512'
; SKX256-NEXT: Cost Model: Found an estimated cost of 24 for instruction: %A = sext <8 x i8> undef to <8 x
i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x
i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x
i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x
i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; VEC512-LABEL: 'sext512'
; VEC512-NEXT: Cost Model: Found an estimated cost of 24 for instruction: %A = sext <8 x i8> undef to <8 x
i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x
i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x
i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x
i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x
i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x
i16>
; VEC512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
%A = sext <8 x i8> undef to <8 x i64>
%B = sext <8 x i16> undef to <8 x i64>
%C = sext <8 x i32> undef to <8 x i64>
%D = sext <16 x i8> undef to <16 x i32>

```



```
%E = sext <16 x i16> undef to <16 x i32>
%F = sext <32 x i8> undef to <32 x i16>
ret void
}
```

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```
; RUN: llc < %s -mtriple=s390x-linux-gnu -mcpu=zEC12 -verify-machineinstrs | FileCheck %s
;
; Test that early if conversion produces LOCR with operands of the right
; register classes.
```

```
define void @autogen_SD4739(i8*) {
; CHECK-NOT: Expected a GR32Bit register, but got a GRX32Bit register
BB:
  %L34 = load i8, i8* %0
  %Cmp56 = icmp sgt i8 undef, %L34
  br label %CF246

CF246:                                ; preds = %CF246, %BB
  %S1163 = select i1 %Cmp56, i8 %L34, i8 undef
  br i1 undef, label %CF246, label %CF248

CF248:                                ; preds = %CF248, %CF246
  store i8 %S1163, i8* %0
  br label %CF248
```

}

# 1.323 slf4j 1.7.5

## 1.323.1 Available under license :

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"http://www.w3.org/TR/xhtml1/DTD/xhtml1-transitional.dtd">
```

```
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  <head>
    <meta http-equiv="content-type" content="text/html; charset=iso-8859-1" />
    <title>SLF4J License</title>
    <link rel="stylesheet" type="text/css" media="screen" href="css/site.css" />
  </head>
  <body>
    <script type="text/javascript">prefix="";</script>
```

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<script src="templates/header.js" type="text/javascript"></script>
<div id="left">
  <script src="templates/left.js" type="text/javascript"></script>
</div>
<div id="content">
```

```
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